



**Swami Keshvanand Institute of Technology,  
Management & Gramothan, Jaipur**

**Programme Scheme**

**B.Tech in**

**Electronics & Communication Engineering**

(For students admitted during A.Y. 2020-21 to 2023-24)



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Teaching and Examination Scheme

### I Semester: B.Tech Common to all branches of UG Engineering & Technology

SN	Category	Course Code	Course Title	Hours			Marks			Cr
				L	T	P	IA	ETE	Total	
1	BSC	1FY2-01	Engineering Mathematics-I	3	1	-	40	160	<b>200</b>	<b>4</b>
2	BSC	1FY2-02/ 1FY2-03	Engineering Physics/ Engineering Chemistry	3	1	-	40	160	<b>200</b>	<b>4</b>
3	HSMC	1FY1-04/ 1FY1-05	Communication Skills/ Human Values	2	-	-	20	80	<b>100</b>	<b>2</b>
4	ESC	1FY3-06/ 1FY3-07	Programming for Problem Solving/ Basic Mechanical Engineering	2	-	-	20	80	<b>100</b>	<b>2</b>
5	ESC	1FY3-08/ 1FY3-09	Basic Electrical Engineering/ Basic Civil Engineering	2	-	-	20	80	<b>100</b>	<b>2</b>
6	BSC	1FY2-20/ 1FY2-21	Engineering Physics Lab/ Engineering Chemistry Lab	-	-	2	30	20	<b>50</b>	<b>1</b>
7	HSMC	1FY1-22/ 1FY1-23	Language Lab/ Human Values Activities and Sports	-	-	2	30	20	<b>50</b>	<b>1</b>
8	ESC	1FY3-24/ 1FY3-25	Computer Programming Lab/ Manufacturing Practices Workshop	-	-	3	45	30	<b>75</b>	<b>1.5</b>
9	ESC	1FY3-26/ 1FY3-27	Basic Electrical Engineering Lab/ Basic Civil Engineering Lab	-	-	2	30	20	<b>50</b>	<b>1</b>
10	ESC	1FY3-28/ 1FY3-29	Computer Aided Engineering Graphics/ Computer Aided Machine Drawing	-	-	3	45	30	<b>75</b>	<b>1.5</b>
11	SODE CA	1FY8-00							<b>25</b>	<b>0.5</b>
<b>Total</b>									<b>1025</b>	<b>20.5</b>

**L** = Lecture, **T** = Tutorial,  
**P** = Practical, **IA**=Internal Assessment,  
**ETE**=End Term Exam, **Cr**=Credits



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Teaching and Examination Scheme

### II Semester: B.Tech.

### Common to all branches of UG Engineering & Technology

SN	Category	Course Code	Course Title	Hours			Marks			Cr
				L	T	P	IA	ETE	Total	
1	BSC	2FY2-01	Engineering Mathematics-II	3	1	-	40	160	<b>200</b>	<b>4</b>
2	BSC	2FY2-03/ 2FY2-02	Engineering Chemistry/ Engineering Physics	3	1	-	40	160	<b>200</b>	<b>4</b>
3	HSMC	2FY1-05/ 2FY1-04	Human Values/ Communication Skills	2	-	-	20	80	<b>100</b>	<b>2</b>
4	ESC	2FY3-07/ 2FY3-06	Basic Mechanical Engineering/ Programming for Problem Solving	2	-	-	20	80	<b>100</b>	<b>2</b>
5	ESC	2FY3-09/ 2FY3-08	Basic Civil Engineering/ Basic Electrical Engineering	2	-	-	20	80	<b>100</b>	<b>2</b>
6	BSC	2FY2-21/ 2FY2-20	Engineering Chemistry Lab/ Engineering Physics Lab	-	-	2	30	20	<b>50</b>	<b>1</b>
7	HSMC	2FY1-23/ 2FY1-22	Human Values Activities and Sports/ Language Lab	-	-	2	30	20	<b>50</b>	<b>1</b>
8	ESC	2FY3-25/ 2FY3-24	Manufacturing Practices Workshop/ Computer Programming Lab	-	-	3	45	30	<b>75</b>	<b>1.5</b>
9	ESC	2FY3-27/ 2FY3-26	Basic Civil Engineering Lab/ Basic Electrical Engineering Lab	-	-	2	30	20	<b>50</b>	<b>1</b>
10	ESC	2FY3-29/ 2FY3-28	Computer Aided Machine Drawing/ Computer Aided Engineering Graphics	-	-	3	45	30	<b>75</b>	<b>1.5</b>
11	SODE CA	1FY8-00							<b>25</b>	<b>0.5</b>
<b>Total</b>									<b>1025</b>	<b>20.5</b>

**L** = Lecture, **T** = Tutorial,  
**P** = Practical, **IA**=Internal Assessment,  
**ETE**=End Term Exam, **Cr**=Credits



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

### I Semester

#### Common to all branches of UG Engineering & Technology

#### 1FY2-01: Engineering Mathematics-I

**Credit: 4**  
**3L+1T+0P**

**Max. Marks: 200 (IA:40, ETE:160)**  
**End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	<b>Calculus:</b> Improper integrals (Beta and Gamma functions) and their properties; Applications of definite integrals to evaluate surface areas and volumes of revolutions.	8
2	<b>Sequences and Series:</b> Convergence of sequence and series, tests for convergence; Power series, Taylor's series, series for exponential, trigonometric and logarithm functions.	6
3	<b>Fourier Series:</b> Periodic functions, Fourier series, Euler's formula, Change of intervals, Half range sine and cosine series, Parseval's theorem.	6
4	<b>Multivariable Calculus (Differentiation):</b> Limit, continuity and partial derivatives, directional derivatives, total derivative; Tangent plane and normal line; Maxima, minima and saddle points; Method of Lagrange multipliers; Gradient, curl and divergence.	10
5	<b>Multivariable Calculus (Integration):</b> Multiple Integration: Double integrals (Cartesian), change of order of integration in double integrals, Change of variables (Cartesian to polar), Applications: areas and volumes, Centre of mass and Gravity (constant and variable densities); Triple integrals (Cartesian), Simple applications involving cubes, sphere and rectangular parallelepipeds; Scalar line integrals, vector line integrals, scalar surface integrals, vector surface integrals, Theorems of Green, Gauss and Stokes.	10
<b>TOTAL</b>		<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY2-02/ 2FY2-02: Engineering Physics

Credit: 4  
3L+1T+0P

Max. Marks: 200 (IA:40, ETE:160)  
End Term Exam: 3 Hours

SN	CONTENTS	Hours
1	<b>Wave Optics:</b> Newton's Rings, Michelson's Interferometer, Fraunhofer Diffraction from a Single Slit. Diffraction grating: Construction, theory and spectrum, Resolving power and Rayleigh criterion for limit of resolution, Resolving power of diffraction grating, X-Ray diffraction and Bragg's Law.	9
2	<b>Quantum Mechanics:</b> Introduction to quantum Mechanics, Wave-particle duality, Matter waves, Wave function and basic postulates, Time dependent and time independent Schrodinger's Wave Equation, Physical interpretation of wave function and its properties, Applications of the Schrodinger's Equation: Particle in one dimensional and three dimensional boxes.	6
3	<b>Coherence and Optical Fibers:</b> Spatial and temporal coherence: Coherence length; Coherence time and 'Q' factor for light, Visibility as a measure of Coherence and spectral purity, Optical fiber as optical wave guide, Numerical aperture; Maximum angle of acceptance and applications of optical fiber.	4
4	<b>Laser:</b> Einstein's Theory of laser action; Einstein's coefficients; Properties of Laser beam, Amplification of light by population inversion, Components of laser, Construction and working of He-Ne and semiconductor lasers, Applications of Lasers in Science, engineering and medicine.	6
5	<b>Material Science &amp; Semiconductor Physics:</b> Bonding in solids: covalent and metallic bonding, Energy bands in solids: Classification of solids as Insulators, Semiconductors and Conductors, Intrinsic and extrinsic semiconductors, Fermi dirac distribution function and Fermi energy, Conductivity in semiconductors, Hall Effect: Theory, Hall Coefficient and applications.	7
6	<b>Introduction to Electromagnetism:</b> Divergence and curl of electrostatic field, Laplace's and Poisson's equations for electrostatic potential, Bio-Savart law, Divergence and curl of static magnetic field, Faraday's law, Displacement current and magnetic field arising from time dependent electric field, Maxwell's equations, Flow of energy and Poynting vector.	8
<b>TOTAL</b>		<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY2-03/ 2FY2-03: Engineering Chemistry

**Credit: 4**  
**3L+1T+0P**

**Max. Marks: 200 (IA:40, ETE:160)**  
**End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	<b>Water:</b> Common impurities, hardness, determination of hardness by complexometric (EDTA method), Degree of hardness, Units of hardness Municipal water supply: Requisite of drinking water, Purification of water; sedimentation, filtration, disinfection, breakpoint chlorination. Boiler troubles: Scale and Sludge formation, Internal treatment methods, Priming and Foaming, Boiler corrosion and Caustic embrittlement Water softening; Lime-Soda process, Zeolite (Permutit) process, Demineralization process. Numerical problems based on Hardness, EDTA, Lime-Soda and Zeolite process.	10
2	<b>Organic Fuels:</b> Solid fuels: Coal, Classification of Coal, Proximate and Ultimate analyses of coal and its significance, Gross and Net Calorific value, Determination of Calorific value of coal by Bomb Calorimeter. Metallurgical coke, Carbonization processes; Otto-Hoffmann by-product oven method. Liquid fuels : Advantages of liquid fuels, Mining, Refining and Composition of petroleum, Cracking, Synthetic petrol, Reforming, Knocking, Octane number, Anti-knocking agents, Cetane number Gaseous fuels; Advantages, manufacturing, composition and Calorific value of coal gas and oil gas, Determination of calorific value of gaseous fuels by Junker's calorimeter Numerical problems based on determination of calorific value (bomb calorimeter/Junkers calorimeter/Dulong's formula, proximate analysis & ultimate and combustion of fuel.	10
3	<b>Corrosion and its control:</b> Definition and significance of corrosion, Mechanism of chemical (dry) and electrochemical (wet) corrosion, galvanic corrosion, concentration corrosion and pitting corrosion. Protection from corrosion; protective coatings-galvanization and tinning, cathodic protection, sacrificial anode and modifications in design.	3
4	<b>Engineering Materials:</b> Portland Cement; Definition, Manufacturing by Rotary kiln. Chemistry of setting and hardening of cement. Role of Gypsum. Glass: Definition, Manufacturing by tank furnace, significance of	10



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	annealing, Types and properties of soft glass, hard glass, borosilicate glass, glass wool, safety glass Lubricants: Classification, Mechanism, Properties; Viscosity and viscosity index, flash and fire point, cloud and pour point. Emulsification and steam emulsion number.	
<b>5</b>	<b>Organic reaction mechanism and introduction of drugs:</b> Organic reaction mechanism: Substitution; SN1, SN2, Electrophilic aromatic substitution in benzene, free radical halogenations of alkanes, Elimination; elimination in alkyl halides, dehydration of alcohols, Addition: electrophilic and free radical addition in alkenes, nucleophilic addition in aldehyde and ketones, Rearrangement; Carbocation and free radical rearrangements Drugs : Introduction, Synthesis, properties and uses of Aspirin, Paracetamol	<b>7</b>
<b>TOTAL</b>		<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY1-04/ 2FY1-04: Communication Skills

**Credit: 2**  
**2L+0T+0P**

**Max. Marks: 100 (IA:20, ETE:80)**  
**End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	<b>Communication:</b> Meaning, Importance and Cycle of Communication. Media and Types of Communication. Verbal and Non-Verbal Communication. Barriers to communication. Formal and Informal Channels of Communication (Corporate Communication). Divisions of Human Communication and Methods to improve Interpersonal Communication. Qualities of good communication.	5
2	<b>Grammar:</b> Passive Voice. Reported Speech. Conditional Sentences. Modal Verbs. Linking Words (Conjunctions)	5
3	<b>Composition:</b> Job Application and Curriculum-Vitae Writing. Business Letter Writing. Paragraph Writing. Report Writing.	5
4	<b>Short Stories:</b> "Luncheon" by Somerset Maugham. "How Much Land Does a Man Need?" by Count Leo Tolstoy. "The Night Train at Deoli" by Ruskin Bond.	5
5	<b>Poems:</b> "No Men are Foreign" by James Kirkup. "If" by Rudyard Kipling. "Where the Mind is without Fear" by Rabindranath Tagore.	5
<b>TOTAL</b>		<b>25</b>





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## I & II Semester Common to all branches of UG Engineering & Technology

### 1FY1-05/ 2FY1-05: Human Values

Credit: 2  
2L+0T+0P

Max. Marks: 100 (IA:20, ETE:80)  
End Term Exam: 2 Hours

SN	CONTENTS	Hours
1	<p><b>Course Introduction - Need, Basic Guidelines, Content and Process for Value Education</b></p> <p>Understanding the need, basic guidelines, Self Exploration - its content and process; 'Natural Acceptance' and Experiential Validation, Continuous Happiness and Prosperity- Human Aspirations, Right understanding, Relationship and Physical Facilities, Understanding Happiness and Prosperity correctly- A critical appraisal of the current scenario.</p> <p>Method to fulfill the above human aspirations: understanding and living in harmony at various levels</p>	5
2	<p><b>Understanding Harmony in the Human Being - Harmony in Myself</b></p> <p>Understanding human being as a co-existence of the sentient 'I' and the material 'Body'</p> <p>Understanding the needs of Self ('I') and 'Body' - Sukh and Suvidha</p> <p>Understanding the Body as an instrument of 'I', Understanding the characteristics and activities of 'I' and harmony in 'I' Understanding the harmony of I with the Body: Sanyam and Swasthya; correct appraisal of Physical needs, meaning of Prosperity in detail, Programs to ensure Sanyam and Swasthya.</p>	5
3	<p><b>Understanding Harmony in the Family and Society- Harmony in Human-Human Relationship</b></p> <p>Understanding harmony in the Family, Understanding values in human-human relationship; meaning of Nyaya and program for its fulfillment to ensure Ubhay-tripti; Trust (Vishwas) and Respect (Samman) , meaning of Vishwas; Difference between intention and competence, meaning of Samman, Difference between respect and differentiation;</p> <p>the other salient values in relationship, harmony in the society , Samadhan, Samridhi, Abhay, Sah-astitva as comprehensive Human Goals , Visualizing a universal harmonious order in society- Undivided Society (AkhandSamaj), Universal Order (SarvabhaumVyawastha )- from family to world family.</p>	5



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4	<b>Understanding Harmony in the Nature and Existence - Whole existence as Coexistence</b> Understanding the harmony in the Nature. Interconnectedness and mutual fulfillment among the four orders of nature- recyclability and self-regulation in nature. Understanding Existence as Co-existence (Sah-astitva) of mutually interacting units in all pervasive Space. Holistic perception of harmony at all levels of existence	5
5	<b>Implications of the above Holistic Understanding of Harmony on Professional Ethics. Natural acceptance of human values</b> Definitiveness of Ethical Human Conduct. Basis for Humanistic Education, Humanistic Constitution and Humanistic Universal Order. Competence in Professional Ethics: a) Ability to utilize the professional competence for augmenting universal human order, (b) Ability to identify the scope and characteristics of people-friendly and eco-friendly production systems, technologies and management models. Strategy for transition from the present state to Universal Human Order: (a). At the level of individual: as socially and ecologically responsible engineers, technologists and managers. (b). At the level of society: as mutually enriching institutions and organization. Case studies related to values in professional life and individual life.	5
<b>TOTAL</b>		<b>25</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY3-06/ 2FY3-06: Programming for Problem Solving

**Credit: 2**  
**2L+0T+0P**

**Max. Marks: 100 (IA:20, ETE:80)**  
**End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	<b>Fundamentals of Computer:</b> Stored program architecture of computers, Storage device- Primary memory, and Secondary storage, Random, Direct, Sequential access methods, Concepts of High-level, Assembly and Low-level languages, Representing algorithms through flowchart and pseudo code.	8
2	<b>Number system:</b> Data representations, Concepts of radix and representation of numbers in radix r with special cases of r=2, 8, 10 and 16 with conversion from radix r <sub>1</sub> to r <sub>2</sub> , r's and (r-1)'s complement, Binary addition, Binary subtraction, Representation of alphabets.	8
3	<b>C Programming:</b> Problem specification, flow chart, data types, assignment statements, input output statements, developing simple C programs, If statement, for loops, while loops, do-while loops, switch statement, break statement, continue statement, development of C programs using above statements, Arrays, functions, parameter passing, recursion, Programming in C using these statements, Structures, files, pointers and multi file handling.	12
<b>TOTAL</b>		<b>28</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY3-07/ 2FY3-07: Basic Mechanical Engineering

**Credit: 2**  
**2L+0T+0P**

**Max. Marks: 100 (IA:20, ETE:80)**  
**End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	<b>Fundamentals:</b> Introduction to mechanical engineering, concepts of thermal engineering, mechanical machine design, industrial engineering and manufacturing technology. Steam Boilers classification and types of steam boilers and steam turbines. Introduction and Classification of power plants.	
2	<b>Pumps and IC Engines:</b> Applications and working of Reciprocating and Centrifugal pumps. Introduction, Classification of IC Engines, Main Components of IC Engines, Working of IC Engines and its components.	
3	<b>Refrigeration and Air Conditioning:</b> Introduction, classification and types of refrigeration systems and air-conditioning. Applications of refrigeration and Air-conditioning.	
4	<b>Transmission of Power:</b> Introduction and types of Belt and Rope Drives, Gears.	
5	<b>Primary Manufacturing Processes:</b> Metal Casting Process: Introduction to Casting Process, Patterns, Molding, Furnaces. Metal Forming Processes: Introduction to Forging, Rolling, Extrusion, Drawing. Metal Joining Processes: Introduction to various types of Welding, Gas Cutting, Brazing, and Soldering.	
6	<b>Engineering Materials and Heat Treatment of Steel:</b> Introduction to various engineering materials and their properties.	



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY3-08/ 2FY3-08: Basic Electrical Engineering

**Credit: 2**  
**2L+0T+0P**

**Max. Marks: 100 (IA:20, ETE:80)**  
**End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	<b>DC Circuits:</b> Electrical circuit elements (R, L and C), voltage and current sources, Kirchhoff current and voltage laws, Series-Parallel circuits, Node voltage method, Mesh current method, Superposition, Thevenin's, Norton's and Maximum power transfer theorems.	5
2	<b>AC Circuits:</b> Representation of sinusoidal waveforms, peak and r.m.s values, phasor representation, real power, reactive power, apparent power, power factor. Analysis of single-phase AC circuits consisting of R, L, C, RL, RC and RLC combinations (series and parallel), resonance. Three phase balanced circuits, voltage and current relations in star and delta connections.	4
3	<b>Transformers:</b> Ideal and practical transformer, EMF equation, equivalent circuit, losses in transformers, regulation and efficiency.	4
4	<b>Electrical Machines:</b> Generation of rotating magnetic fields, Construction and working of a three-phase induction motor, Significance of torque-slip characteristic. Starting and speed control of induction motor, single-phase induction motor. Construction, working, torque-speed characteristic and speed control of separately excited DC motor. Construction and working of synchronous generators.	7
5	<b>Power Converters:</b> Semiconductor PN junction diode and transistor (BJT). Characteristics of SCR, power transistor and IGBT. Basic circuits of single phase rectifier with R load, Single phase Inverter, DC-DC converter.	4
6	<b>Electrical Installations:</b> Layout of LT switchgear: Switch fuse unit (SFU), MCB, ELCB, MCCB, Type of earthing. Power measurement, elementary calculations for energy consumption.	4
<b>TOTAL</b>		<b>28</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester Common to all branches of UG Engineering & Technology

### 1FY3-09/ 2FY3-09: Basic Civil Engineering

**Credit: 2**  
**2L+0T+0P**

**Max. Marks: 100 (IA:20, ETE:80)**  
**End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	<b>Introduction to objective, scope and outcome the subject</b>	1
2	<b>Introduction:</b> Scope and Specialization of Civil Engineering, Role of civil Engineer in Society, Impact of infrastructural development on economy of country.	2
3	<b>Surveying:</b> Object, Principles & Types of Surveying; Site Plans, Plans& Maps; Scales & Unit of different Measurements. Linear Measurements: Instruments used. Linear Measurement by Tape, Ranging out Survey Lines and overcoming Obstructions; Measurements on sloping ground; Tape corrections, conventional symbols. Angular Measurements: Instruments used; Introduction to Compass Surveying, Bearings and Longitude & Latitude of a Line, Introduction to total station. Levelling: Instrument used, Object of levelling, Methods of levelling in brief, Contour maps.	8
4	<b>Buildings:</b> Selection of site for Buildings, Layout of Building Plan, Types of buildings, Plinth area, carpet area, floor space index, Introduction to building byelaws, concept of sun light and ventilation. Components of Buildings & their functions, Basic concept of R.C.C., Introduction to types of foundation.	3
5	<b>Transportation:</b> Introduction to Transportation Engineering; Traffic and Road Safety: Types and Characteristics of Various Modes of Transportation; Various Road Traffic Signs, Causes of Accidents and Road Safety Measures.	2



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<b>6</b>	<b>Environmental Engineering:</b> Environmental Pollution, Environmental Acts and Regulations, Functional Concepts of Ecology, Basics of Species, Biodiversity, Ecosystem, Hydrological Cycle; Chemical Cycles: Carbon, Nitrogen & Phosphorus; Energy Flow in Eco-systems.	<b>4</b>
	Water Pollution: Water Quality standards, Introduction to Treatment & Disposal of Waste Water. Reuse and Saving of Water, Rain Water Harvesting.	<b>3</b>
	Solid Waste Management: Classification of Solid Waste, Collection, Transportation and Disposal of Solid. Recycling of Solid Waste: Energy Recovery, Sanitary Land fill, On-Site Sanitation.	<b>2</b>
	Air & Noise Pollution: Primary and Secondary air pollutants, Harmful effects of Air Pollution, Control of Air Pollution. . Noise Pollution, Harmful Effects of noise pollution, control of noise pollution, Global warming & Climate Change, Ozone depletion, Green House effect	<b>3</b>
<b>TOTAL</b>		<b>28</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY2-20/ 2FY2-20: Engineering Physics Lab

**Credit: 1**  
**OL+OT+2P**

**Max. Marks: 50 (IA:30, ETE:20)**

1. To determine the wave length of monochromatic light with the help of Michelson's interferometer.
2. To determine the wave length of sodium light by Newton's Ring.
3. To determine the wave length of prominent lines of mercury by plane diffraction grating with the help of spectrometer.
4. Determination of band gap using a P-N junction diode.
5. To determine the height of given object with the help of sextant.
6. To determine the dispersive power of material of a prism with the help of spectrometer.
7. To study the charge and discharge of a condenser and hence determine the same constant (both current and voltage graphs are to be plotted).
8. To determine the coherence length and coherence time of laser using He - Ne laser.
9. To measure the numerical aperture of an optical fibre.
10. To study the Hall Effect and determine the Hall Voltage and Hall coefficients.





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY2-21/ 2FY2-21: Engineering Chemistry Lab

Credit: 1  
OL+OT+2P

Max. Marks: 50 (IA:30, ETE:20)

1. Determination the hardness of water by EDTA method
2. Determination of residual chlorine in water
3. Determination of dissolved oxygen in water
4. Determination of the strength of Ferrous Ammonium sulphate solution with the help of  $K_2Cr_2O_7$  solution by using diphenyl amine indicator
5. Determination of the strength of  $CuSO_4$  solution iodometrically by using hypo solution
6. Determination of the strength of  $NaOH$  and  $Na_2CO_3$  in a given alkali mixture
7. Proximate analysis of Coal
8. Determination of the flash & fire point and cloud & pour point of lubricating oil
9. Determination of the kinematic viscosity of lubricating oil by Redwood viscometer no. 1 at different temperature
10. Synthesis of Aspirin/ Paracetamol



**I & II Semester**  
**Common to all branches of UG Engineering & Technology**

**1FY2-22/ 2FY2-22: Language Lab**

**Credit: 1**  
**OL+OT+2P**

**Max. Marks: 50 (IA:30, ETE:20)**

1. Phonetic Symbols and Transcriptions.
2. Extempore.
3. Group Discussion.
4. Dialogue Writing.
5. Listening comprehension.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY1-23/ 2FY1-23: Human Values Activities and Sports

**Credit: 1**  
**OL+OT+2P**

**Max. Marks: 50 (IA:30, ETE:20)**

#### **PS 1:**

Introduce yourself in detail. What are the goals in your life? How do you set your goals in your life? How do you differentiate between right and wrong? What have been your salient achievements and shortcomings in your life? Observe and analyze them.

#### **PS 2:**

Now-a-days, there is a lot of talk about many techno-genic maladies such as energy and material resource depletion, environmental pollution, global warming, ozone depletion, deforestation, soil degradation, etc. - all these seem to be manmade problems, threatening the survival of life Earth - What is the root cause of these maladies & what is the way out in opinion?

On the other hand, there is rapidly growing danger because of nuclear proliferation, arms race, terrorism, breakdown of relationships, generation gap, depression & suicidal attempts etc. - what do you think, is the root cause of these threats to human happiness and peace - what could be the way out in your opinion?

#### **PS 3:**

1. Observe that each of us has the faculty of 'Natural Acceptance', based on which one can verify what is right or not right for him. (As such we are not properly trained to listen to our 'Natural Acceptance' and may a time it is also clouded by our strong per-conditioning and sensory attractions).

Explore the following:

- (i) What is 'Naturally Acceptable' to you in relationship the feeling of respect or disrespect for yourself and for others?
- (ii) What is 'naturally Acceptable' to you - to nurture or to exploit others?

Is your living in accordance with your natural acceptance or different from it?

2. Out of the three basic requirements for fulfillment of your aspirations - right understanding, relationship and physical facilities - observe how the problems in your family are related to each. Also observe how much time & effort you devote for each in your daily routine.

#### **PS 4:**

1. a. Observe that any physical facility you use, follows the given sequence with time:

Necessary and tasteful - unnecessary but still tasteful - unnecessary and tasteless - intolerable

b. In contrast, observe that any feeling in you is either naturally acceptable or not acceptable at all. If not acceptable, you want it continuously and if not acceptable, you do not want it any moment!

2. List down all your important activities. Observe whether the activity is of 'I' or of Body or with the participation of both or with the participation of both 'I' and Body.

3. Observe the activities within 'I'. Identify the object of your attention for different



moments (over a period of say 5 to 10 minutes) and draw a line diagram connecting these points. Try to observe the link between any two nodes.

**PS 5:**

1. Write a narration in the form of a story, poem, skit or essay to clarify a salient Human Value to the children.
2. Recollect and narrate an incident in your life where you were able to exhibit willful adherence to values in a difficult situation.

**PS 6:**

List down some common units (things) of Nature which you come across in your daily life and classify them in the four orders of Nature. Analyse and explain the aspect of mutual fulfillment of each unit with other orders.

**PS 7:**

Identify any two important problems being faced by the society today and analyze the root cause of these problems. Can these be solved on the basis of natural acceptance of human values? If so, how should one proceed in this direction from the present situation?

**PS 8:**

1. Suggest ways in which you can use your knowledge of Science/Technology/Management etc. for moving towards a universal human order.
2. Propose a broad outline for humanistic Constitution at the level of Nation.

**Project:**

**Every student required to take-up a social project e.g. educating children in needy/weaker section; services in hospitals, NGO's and other such work i.e. social work at villages adopted by respective institute/ college.**

**Sports:**

- a) Planning in Sports,
- b) Sports & Nutrition
- c) Yoga and Life style
- d) Measures Physical Education & Sports for CWSN (Children with Special needs - Divyang)
- e) Children & Sports
- f) Women & Sports
- g) Test & Measurement in Sports
- h) Physiology & Sports
- i) Sports Medicine
- j) Kinesiology, Biomechanics & Sports
- k) Psychology & Sports
- l) Training in Sports



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester Common to all branches of UG Engineering & Technology

### 1FY3-24/ 2FY3-24: Computer Programming Lab

**Credit: 1.5**  
**OL+OT+3P**

**Max. Marks: 75 (IA:45, ETE:30)**

1. To learn about the C Library, Preprocessor directive, Input-output statement.
2. Programs to learn data type, variables, If-else statement
3. Programs to understand nested if-else statement and switch statement
4. Programs to learn iterative statements like while and do-while loops
5. Programs to understand for loops for iterative statements
6. Programs to learn about array and string operations
7. Programs to understand sorting and searching using array
8. Programs to learn functions and recursive functions
9. Programs to understand Structure and Union operation
10. Programs to learn Pointer operations
11. Programs to understand File handling operations
12. Programs to input data through Command line argument



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY3-25/ 2FY3-25: Manufacturing Practices Workshop

**Credit: 1.5**  
**OL+OT+3P**

**Max. Marks: 75 (IA:45, ETE:30)**

#### **Carpentry Shop**

1. T – Lap joint
2. Bridle joint

#### **Foundry Shop**

3. Mould of any pattern
4. Casting of any simple pattern

#### **Welding Shop**

5. Lap joint by gas welding
6. Butt joint by arc welding
7. Lap joint by arc welding
8. Demonstration of brazing, soldering & gas cutting

#### **Machine Shop Practice**

9. Job on lathe with one step turning and chamfering operations

#### **Fitting and Sheet Metal Shop**

10. Finishing of two sides of a square piece by filing
11. Making mechanical joint and soldering of joint on sheet metal
12. To cut a square notch using hacksaw and to drill a hole and tapping



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY3-26/ 2FY3-26: Basic Electrical Engineering Lab

**Credit: 1**  
**OL+OT+2P**

**Max. Marks: 50 (IA:30, ETE:20)**

1. Basic safety precautions. Introduction and use of measuring instruments – voltmeter, ammeter, multi-meter, oscilloscope. Real-life resistors, capacitors and inductors.
2. Transformers: Observation of the no-load current waveform on an oscilloscope. Loading of a transformer: measurement of primary and secondary voltages and currents, and power.
3. Three-phase transformers: Star and Delta connections. Voltage and Current relationships (line-line voltage, phase-to-neutral voltage, line and phase currents).Phase-shifts between the primary and secondary side.
4. Demonstration of cut-out sections of machines: dc machine (commutator-brush arrangement), induction machine (squirrel cage rotor), synchronous machine (field winding - slip ring arrangement) and single-phase induction machine.
5. Torque Speed Characteristic of separately excited dc motor.
6. Demonstration of (a) dc-dc converters (b) dc-ac converters – PWM waveform (c) the use of dc-ac converter for speed control of an induction motor and (d) Components of LT switchgear.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY3-27/ 2FY3-27: Basic Civil Engineering Lab

**Credit: 1**  
**OL+OT+2P**

**Max. Marks: 50 (IA:30, ETE:20)**

1. Linear Measurement by Tape:
  - a) Ranging and Fixing of Survey Station along straight line and across obstacles.
  - b) Laying perpendicular offset along the survey line
2. Compass Survey: Measurement of bearing of lines using Surveyor's and Prismatic compass
3. Levelling: Using Tilting/ Dumpy/ Automatic Level
  - a) To determine the reduced levels in closed circuit.
  - b) To carry out profile levelling and plot longitudinal and cross sections for road by Height of Instrument and Rise & Fall Method.
4. To study and take measurements using various electronic surveying instruments like EDM, Total Station etc.
5. To determine pH, hardness and turbidity of the given sample of water.
6. To study various water supply Fittings.
7. To determine the pH and total solids of the given sample of sewage.
8. To study various Sanitary Fittings.





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY3-28/ 2FY3-28: Computer Aided Engineering Graphics

**Credit: 1.5**  
**OL+OT+3P**

**Max. Marks: 75 (IA:45, ETE:30)**

**Introduction:** Principles of drawing, lines, type of lines, usage of Drawing instruments, lettering, Conic sections including parabola, hyperbola, Rectangular Hyperbola (General method only); Scales-Plain, Diagonal and Vernier Scales.

**Projections of Point & Lines:** Position of Point, Notation System, Systematic Approach for projections of points, front view & Top view of point, Position of straight lines, line parallel to Both the RPs, Line perpendicular to either of the RPs, Line inclined to one RP and parallel to the other, Line inclined to Both the RPs, Traces of a line (One drawing sheet, one assignment in sketch book).

**Projection of Planes:** Positions of planes, Terms used in projections of planes, plane parallel to RP, plane inclined to one RP and perpendicular to the other RP, plane perpendicular to Both the RPs, plane Inclined to Both the RPs, True shape of the plane, Distance of a point from plane, Angle between two planes.

**Projections of Regular Solids:** frustum and truncated solids, those inclined to both the Planes-Auxiliary Views.

**Section of Solids:** Theory of sectioning, section of prisms and cubes, section of pyramids and Tetrahedron section of Cylinders, section of cones, section of spheres (One drawing sheet, one assignment in sketch book)

**Overview of Computer Graphics :** Covering theory of CAD software [such as: The menu System, Toolbars (standard, Object Properties, Draw, Modify and Dimension), Drawing Area (Background, Crosshairs, Coordinate System), Dialog boxes and windows, Shortcut menus (Button Bars), Command Line (where applicable), The Status Bar, Different methods of zoom as used in CAD, Select and erase objects.: Isometric Views of lines, Planes, Simple and compound Solids.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## I & II Semester

### Common to all branches of UG Engineering & Technology

#### 1FY3-29/ 2FY3-29: Computer Aided Machine Drawing

**Credit: 1.5**  
**OL+OT+3P**

**Max. Marks: 75 (IA:45, ETE:30)**

**Introduction:** Principles of drawing, conventional representation of machine components and materials, lines, types of lines, dimensioning types, rules of dimensioning.

**Conversion of pictorial views into orthographic views:** (1 drawing sheet) Introduction to orthographic projection, concept of first angle and third angle projection, drawing of simple machine elements in first angle projection, missing view problems covering Principles of Orthographic Projections.

**Sectional views of mechanical components:** (1 drawing sheet) Introduction, cutting plane line, type of sectional views-full section, half section, partial or broken section, revolved section, removed section, offset section, sectioning conventions-spokes, web rib, shaft, pipes, different types of holes, conventions of section lines for different metals and materials.

**Fasteners and other mechanical components:** (Free hand sketch) Temporary and permanent fasteners, thread nomenclature and forms, thread series, designation, representation of threads, bolted joints, locking arrangement of nuts, screws, washers, foundation bolts etc., keys, types of keys, cotter and knuckle joints. Riveted joints, rivets and riveting, type of rivets, types of riveted joints etc. Bearing: Ball, roller, needle, foot step bearing. Coupling: Protected type, flange, and pin type flexible coupling. Other components: Welded joints, belts and pulleys, pipes and pipe joints, valves etc.

**Overview of Computer Graphics:** (2 drawing sheets) Covering theory of CAD software such as: The menu System, Toolbars (Standard, Object Properties, Draw, Modify and Dimension), Drawing Area (Background, Crosshairs, Coordinate System), Dialog boxes and windows, Shortcut menus (Button Bars), Command Line (Where applicable), The Status Bar, Different methods of zoom as used in CAD, Select and erase objects.: Isometric Views of Lines, Planes, Simple and compound Solids.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## II Semester

### Common to all branches of UG Engineering & Technology

#### 2FY2-01: Engineering Mathematics-II

Credit: 4  
3L+1T+0P

Max. Marks: 200 (IA:40, ETE:160)

End Term Exam: 3 Hours

SN	CONTENTS	Hours
1	<b>Matrices:</b> Rank of a matrix, rank-nullity theorem; System of linear equations; Symmetric, skew-symmetric and orthogonal matrices; Eigenvalues and eigenvectors; Diagonalization of matrices; Cayley-Hamilton Theorem, and Orthogonal transformation.	10
2	<b>First order ordinary differential equations:</b> Linear and Bernoulli's equations, Exact equations, Equations not of first degree: equations solvable for $p$ , equations solvable for $y$ , equations solvable for $x$ and Clairaut's type.	6
3	<b>Ordinary differential equations of higher orders:</b> Linear Differential Equations of Higher order with constant coefficients, Simultaneous Linear Differential Equations, Second order linear differential equations with variable coefficients: Homogenous and Exact forms, one part of CF is known, Change of dependent and independent variables, method of variation of parameters, Cauchy-Euler equation; Power series solutions including Legendre differential equation and Bessel differential equations.	12
4	<b>Partial Differential Equations – First order:</b> Order and Degree, Formation; Linear Partial differential equations of First order, Lagrange's Form, Non Linear Partial Differential equations of first order, Charpit's method, Standard forms.	6
5	<b>Partial Differential Equations– Higher order:</b> Classification of Second order partial differential equations, Separation of variables method to simple problems in Cartesian coordinates including two dimensional Laplace, one dimensional Heat and one dimensional Wave equations.	6
<b>TOTAL</b>		<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Teaching & Examination Scheme B.Tech. : Electronics & Communication Engineering 2<sup>nd</sup> Year - III Semester

THEORY											
SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
1	BSC	3EC2-01	Advanced Engineering Mathematics-I	3	0	0	3	30	70	100	3
2	HSMC	3EC1-02/ 3EC1-03	Technical Communication/Managerial Economics and Financial Accounting	2	0	0	2	30	70	100	2
3	PCC	3EC4-04	Digital System Design	3	0	0	3	30	70	100	3
4		3EC4-05	Signal & Systems	3	0	0	3	30	70	100	3
5		3EC4-06	Network Theory	3	1	0	3	30	70	100	4
6		3EC4-07	Electronics Devices	3	1	0	3	30	70	100	4
			<b>Sub Total</b>	17	2	0					19
PRACTICAL & SESSIONAL											
8	PCC	3EC4-21	Electronics Devices Lab	0	0	2		60	40	100	1
9		3EC4-22	Digital System Design Lab	0	0	2		60	40	100	1
10		3EC4-23	Signal Processing Lab	0	0	2		60	40	100	1
11	ESC	3EC3-24	Computer Programming Lab-I	0	0	2		60	40	100	1
13	PSIT	3EC7-30	Industrial Training	0	0	1		60	40	100	1
14	SODE CA	3EC8-00	Social Outreach, Discipline & Extra Curricular Activities							100	0.5
			<b>Sub- Total</b>	0	0	9					5.5
			<b>TOTAL OF III SEMESTER</b>	17	2	9					24.5

**L:** Lecture, **T:** Tutorial, **P:** Practical, **Cr:** Credits

**ETE:** End Term Exam, **IA:** Internal Assessment

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### 3EC2-01: Advance Engineering Mathematics-I

**3 Credits**

**Max. Marks: 100 (IA:30, ETE:100)**

**3L:0T:0P**

**End Term Exam: 3 Hours**

SN	Contents	Hours
<b>1</b>	<b>Numerical Methods – 1:</b> Finite differences, Relation between operators, Interpolation using Newton's forward and backward difference formulae. Gauss's forward and backward interpolation formulae. Stirling's Formulae. Interpolation with unequal intervals: Newton's divided difference and Lagrange's formulae. Numerical Differentiation, Numerical integration: Trapezoidal rule and Simpson's 1/3rd and 3/8 rules.	<b>10</b>
<b>2</b>	<b>Numerical Methods – 2:</b> Numerical solution of ordinary differential equations: Taylor's series, Euler and modified Euler's methods. Runge- Kutta method of fourth order for solving first and second order equations. Milne's and Adam's predictor-corrector methods. Solution of polynomial and transcendental equations-Bisection method, Newton-Raphson method and Regula-Falsi method.	<b>8</b>
<b>3</b>	<b>Laplace Transform:</b> Definition and existence of Laplace transform, Properties of Laplace Transform and formulae, Unit Step function, Dirac Delta function, Heaviside function, Laplace transform of periodic functions. Finding inverse Laplace transform by different methods, convolution theorem. Evaluation of integrals by Laplace transform, solving ODEs by Laplace transforms method.	<b>10</b>
<b>4</b>	<b>Fourier Transform:</b> Fourier Complex, Sine and Cosine transform, properties and formulae, inverse Fourier transforms, Convolution theorem, application of Fourier transforms to partial ordinary differential equation (One dimensional heat and wave equations only).	<b>7</b>
<b>5</b>	<b>Z-Transform:</b> Definition, properties and formulae, Convolution theorem, inverse Z-transform, application of Z-transform to difference equation.	<b>5</b>
<b>Total</b>		<b>40</b>

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### 3EC1-02/4EC1-02: Technical Communication

2 Credit

Max. Marks: 100 (IA:30, ETE:100)

2L:0T:0P

End Term Exam: 2 Hours

SN	Contents	Hours
1	<b>Introduction to Technical Communication-</b> Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	4
2	<b>Comprehension of Technical Materials/Texts and Information Design &amp; development-</b> Reading of technical texts, Reading and comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
3	<b>Technical Writing, Grammar and Editing-</b> Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
4	<b>Advanced Technical Writing-</b> Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
<b>Total</b>		<b>26</b>

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### 3EC1-03/4EC1-03: Managerial Economics And Financial Accounting

2 Credit

Max. Marks: 100 (IA:30, ETE:100)

2L:0T:0P

End Term Exam: 2 Hours

SN	Contents	Hours
1	Basic economic concepts- Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	4
2	Demand and Supply analysis- Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
3	Production and Cost analysis- Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
4	Market structure and pricing theory- Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
5	Financial statement analysis- Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
<b>Total</b>		<b>26</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### 3EC4-04: Digital System Design

3 Credits

Max. Marks: 100 (IA:30, ETE:100)

3L:0T:0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	Logic Simplification and Combinational Logic Design: Review of Boolean Algebra and De Morgan's Theorem, SOP & POS forms, Canonical forms, Karnaugh maps up to 6 variables, Binary codes, Code Conversion.	7
2	MSI devices like Comparators, Multiplexers, Encoder, Decoder, Driver & Multiplexed Display, Half and Full Adders, Subtractors, Serial and Parallel Adders, BCD Adder, Barrel shifter and ALU	8
3	Sequential Logic Design: Building blocks like S-R, JK and Master-Slave JK FF, Edge triggered FF, Ripple and Synchronous counters, Shift registers, Finite state machines, Design of Synchronous FSM, Algorithmic State Machines charts. Designing synchronous circuits like Pulse train generator, Pseudo Random Binary Sequence generator, Clock generation.	9
4	Logic Families and Semiconductor Memories: TTL NAND gate, Specifications, Noise margin, Propagation delay, fan-in, fan-out, Tristate TTL, ECL, CMOS families and their interfacing, memory elements, Concept of Programmable logic devices like FPGA. Logic implementation using programmable devices.	8
5	VLSI Design flow: Design entry: Schematic, FSM & HDL, different modeling styles in VHDL, Data types and objects, Dataflow, Behavioral and Structural Modeling, Synthesis and Simulation VHDL constructs and codes for combinational and sequential circuits.	8
<b>Total</b>		<b>40</b>





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-04	Digital System Design	CO 1	Develop the understanding of number system and its application in digital electronics.
		CO 2	Development and analysis of K-map to solve the Boolean function to the simplest form for the implementation of compact digital circuits.
		CO 3	Design various combinational and sequential circuits using various metrics: switching speed, throughput/latency, gate count and area, energy dissipation and power.
		CO 4	Understanding Interfacing between digital circuits and analog component using Analog to Digital Converter (ADC), Digital to Analog Converter (DAC) etc.
		CO 5	Design and implement semiconductor memories, programmable logic devices (PLDs) and field programmable gate arrays (FPGA) in digital electronics.

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
3EC4-04 Digital System Design	CO 1	3	2	2	1		1						
	CO 2	3	2	3	2								
	CO 3	2	2	3	1	1							
	CO 4	3	2	1	1	1							
	CO 5	2	1	3	1	1							

**3: Strongly**

**2: Moderate**

**1: Weak**

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Review of Boolean Algebra
Lecture 3	DeMorgan's Theorem, SOP & POS forms,
Lecture 4	Problem of SOP and POS forms of boolean functions.
Lecture 5	Simplification of karnaugh map up to 6 variables
Lecture 6	Simplification of karnaugh map up to 6 variables
Lecture 7	Simplification of karnaugh map up to 6 variables
Lecture 8	Binary codes and code conversion
Lecture 9	Binary codes and code conversion
Lecture 10	Encoder, Decoder
Lecture 11	Half and Full Adders, Subtractors, Serial and Parallel Adders
Lecture 12	BCD Adder, Barrel shifter
Lecture 13	S-R FF, edge triggered and level triggered
Lecture 14	D and J-K FF
Lecture 15	Master-Slave JK FF and T FF
Lecture 16	Ripple and Synchronous counters
Lecture 17	Other type of counters
Lecture 18	Shift registers, Finite state machines, Asynchronous FSM
Lecture 19	Design of synchronous FSM
Lecture 20	Design of synchronous FSM
Lecture 21	Design of synchronous FSM
Lecture 22	Designing synchronous circuits (pulse train generator, pseudo random binary sequence generator, clock generation)

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

Lecture 23	TTL NAND gate, specifications, noise margin, propagation delay, fan-in, fan-out
Lecture 24	TTL NAND gate
Lecture 25	Tristate TTL, ECL
Lecture 26	CMOS families and their interfacing
Lecture 27	CMOS families and their interfacing
Lecture 28	Read-Only Memory, Random Access Memory
Lecture 29	Programmable Logic Arrays (PLA)
Lecture 30	Programmable Array Logic (PAL),
Lecture 31	Field Programmable Gate Array (FPGA)
Lecture 32	Combinational PLD-Based State Machines,
Lecture 33	State Machines on a Chip
Lecture 34	Schematic, FSM & HDL
Lecture 35	Different modeling styles in VHDL
Lecture 36	Data types and objects, Data flow
Lecture 37	Behavioral and Structural Modeling
Lecture 38	Behavioral and Structural Modeling
Lecture 39	Simulation VHDL constructs and codes for combinational and sequential circuits
Lecture 40	Simulation VHDL constructs and codes for combinational and sequential circuits

### Content delivery method:

1. Chalk and Duster
2. PPT
3. Hand-outs



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Sample Assignments:

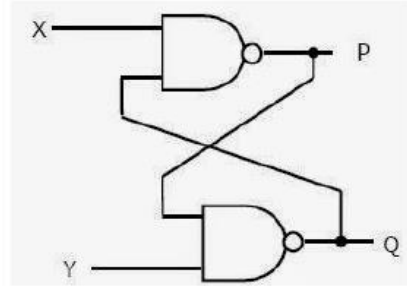
<b>Assignment 1</b>	<b>Q1.</b> Using K-maps, find the minimal Boolean expression of the following SOP and POS representations. a. $f(w,x,y,z) = \Sigma (7,13,14,15)$ b. $f(w,x,y,z) = \Sigma (1,3,4,6,9,11,14,15)$ c. $f(w,x,y,z) = \Pi(1,4,5,6,11,12,13,14,15)$ d. $f(w,x,y,z) = \Sigma (1,3,4,5,7,8,9,11,15)$ e. $f(w,x,y,z) = \Pi (0,4,5,7,8,9,13,15)$
	<b>Q2.</b> Find the function $h(a,b,c,d)$ such that $f = f^d$ . $f(a,b,c,d) = a \cdot b \cdot c + (a \cdot c + b) \cdot d + h(a,b,c,d)$
	<b>Q3.</b> Using K-maps of the functions $f_1$ and $f_2$ , find the following: (provide the canonical form expression and simplify) a. $T_1 = f_1 \cdot f_2$ b. $T_2 = f_1 + f_2$ c. $T_3 = f_1 \oplus f_2$ where $f_1(w,x,y,z) = \Sigma (0,2,4,9,12,15)$ , $f_2(w,x,y,z) = \Sigma (1,2,4,5,12,13)$
<b>Assignment 2</b>	<b>Q1.</b> Draw the state diagram of a serial adder.
	<b>Q2.</b> In the following circuit, given binary values were applied to the Inputs X and Y inputs of the NAND latch shown in the figure. X = 0, Y = 1; X = 0, Y = 0; X = 1, Y = 1. Find out the corresponding stable output P, Q.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

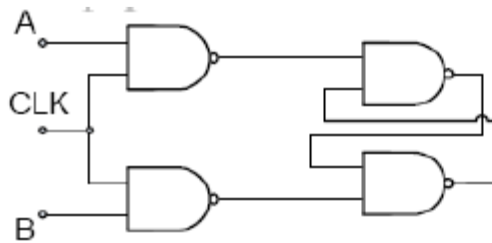
## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)



**Q3.** When the race around condition will occur in the circuit given

Below:





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### 3EC4-05: Signals & Systems

3 Credits

Max. Marks: 100 (IA:30, ETE:100)

3L:0T:0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	Energy and power signals, continuous and discrete time signals, continuous and discrete amplitude signals. System properties: linearity: additivity and homogeneity, shift-invariance, causality, stability, realizability.	6
2	Linear shift-invariant (LSI) systems, impulse response and step response, convolution, input output behavior with aperiodic convergent inputs. Characterization of causality and stability of linear shift-invariant systems. System representation through differential equations and difference equations	7
3	Periodic and semi-periodic inputs to an LSI system, the notion of a frequency response and its relation to the impulse response, Fourier series representation, the Fourier Transform, convolution/multiplication and their effect in the frequency domain, magnitude and phase response, Fourier domain duality. The Discrete-Time Fourier Transform (DTFT) and the Discrete Fourier Transform (DFT). Parseval's Theorem. The idea of signal space and orthogonal bases	8
4	The Laplace Transform, notion of eigen functions of LSI systems, a basis of eigen functions, region of convergence, poles and zeros of system, Laplace domain analysis, solution to differential equations and system behavior.	6
5	The z-Transform for discrete time signals and systems- eigen functions, region of convergence, z-domain analysis.	5
6	State-space analysis and multi-input, multi-output representation. The state-transition matrix and its role. The Sampling Theorem and its implications- Spectra of sampled signals. Reconstruction: ideal interpolator, zero-order hold, first-order hold, and so on. Aliasing and its effects. Relation between continuous and discrete time systems.	8
<b>Total</b>		<b>40</b>

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-05	Signals & Systems	CO 1	Analyze different types of signals and system properties
		CO 2	Represent continuous and discrete systems in time and frequency domain using different transforms
		CO 3	Investigate whether the system is stable.
		CO 4	Sampling and reconstruction of a signal.
		CO 5	Acquire an understanding of MIMO systems

### CO-PO Mapping:

Subject	Course Outcomes	POs											
		PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
3EC4-05 Signals & Systems	CO 1	3	3	1	2	2			1				2
	CO 2	3	1		2	3			1				2
	CO 3	3	2	2	3								2
	CO 4	3	2	3	3	1							
	CO 5	3	2	2	3	1			2				1

**3: Strongly**

**2: Moderate**

**1: Weak**

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Energy signals power signals
Lecture 3	Continuous and discrete time signals
Lecture 4	Continuous amplitude signals
Lecture 5	and discrete amplitude signals
Lecture 6	System properties: linearity: additivity and homogeneity
Lecture 7	shift-invariance, causality
Lecture 8	stability, realizability.
Lecture 9	Linear shift-invariant (LSI) systems
Lecture 10	impulse response
Lecture 11	Step response
Lecture 12	Convolution.
Lecture 13	Input output behavior with aperiodic convergent inputs
Lecture 14	Characterization of causality and stability of linear shift-invariant systems.
Lecture 15	System representation through differential equations and difference equations.
Lecture 16	Characterization of causality and stability of linear shift-invariant systems.
Lecture 17	System representation through differential equations and difference equations.
Lecture 18	Periodic and semi-periodic inputs to an LSI system
Lecture 19	The notion of a frequency response.
Lecture 20	Its relation to the impulse response
Lecture 21	Fourier series representation
Lecture 22	Fourier Transform
Lecture 23	Convolution/multiplication and their effect in the frequency domain
Lecture 24	Magnitude and phase response
Lecture 25	Fourier domain duality.
Lecture 26	The Discrete-Time Fourier Transform (DTFT) and Discrete Fourier Transform (DFT).
Lecture 27	Parseval's Theorem. The idea of signal space and orthogonal bases
Lecture 28	The Laplace Transform
Lecture 29	Notion of eigen functions of LSI systems

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

### II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

Lecture 30	A basis of eigen functions, region of convergence
Lecture 31	Poles and zeros of system, Laplace domain analysis,
Lecture 32	Solution to differential equations and system behavior.
Lecture 33	The z-Transform for discrete time signals and systems- eigen functions,
Lecture 34	Region of convergence, z-domain analysis.
Lecture 35	State-space analysis and multi-input, multi-output representation.
Lecture 36	The state-transition matrix and its role.
Lecture 37	The Sampling Theorem and its implications- Spectra of sampled signals.
Lecture 38	Reconstruction: ideal interpolator, zero-order hold, first-order hold, and so on
Lecture 39	Aliasing and its effects.
Lecture 40	Relation between continuous and discrete time systems.

#### Content delivery method:

1. Chalk and Duster
2. PPT
3. Animation
4. Hand-outs



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

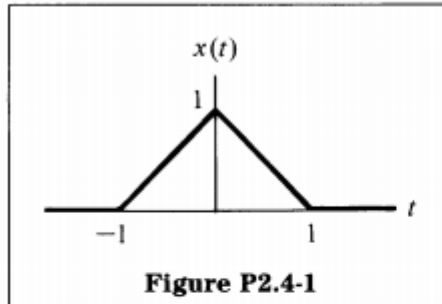
II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Assignments:

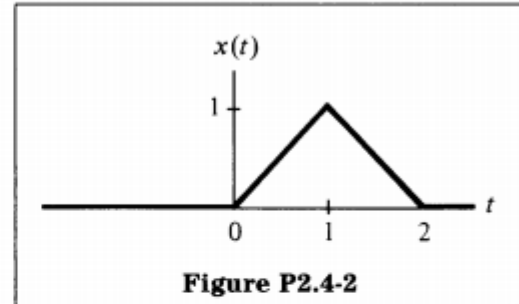
#### Assignment

For each of the following signals, determine whether it is even, odd, or neither.

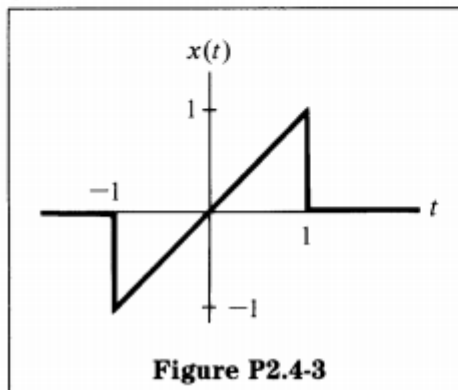
(a)



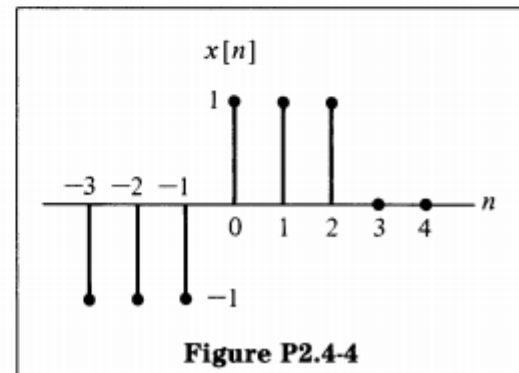
(b)



(c)



(d)





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

Evaluate the following sums:

(a)  $\sum_{n=0}^5 2 \left(\frac{3}{a}\right)^n$

(b)  $\sum_{n=2}^6 b^n$

(c)  $\sum_{n=0}^{\infty} \left(\frac{2}{3}\right)^{2n}$

*Hint:* Convert each sum to the form

$$C \sum_{n=0}^{N-1} \alpha^n = S_N \quad \text{or} \quad C \sum_{n=0}^{\infty} \alpha^n = S_{\infty}$$

and use the formulas

$$S_N = C \left( \frac{1 - \alpha^N}{1 - \alpha} \right), \quad S_{\infty} = \frac{C}{1 - \alpha} \quad \text{for } |\alpha| < 1$$

The first-order difference equation  $y[n] - ay[n - 1] = x[n]$ ,  $0 < a < 1$ , describes a particular discrete-time system initially at rest.

(a) Verify that the impulse response  $h[n]$  for this system is  $h[n] = a^n u[n]$ .

(b) Is the system

- (i) memoryless?
- (ii) causal?
- (iii) stable?

Clearly state your reasoning.

(c) Is this system stable if  $|a| > 1$ ?



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Assignment

Consider a discrete-time system with impulse response

$$h[n] = \left(\frac{1}{2}\right)^n u[n]$$

Determine the response to each of the following inputs:

(a)  $x[n] = (-1)^n = e^{j\pi n}$  for all  $n$

(b)  $x[n] = e^{j(\pi n/4)}$  for all  $n$

(c)  $x[n] = \cos\left(\frac{\pi n}{4} + \frac{\pi}{8}\right)$  for all  $n$

Consider two specific periodic sequences  $\hat{x}[n]$  and  $\hat{y}[n]$ .  $\hat{x}[n]$  has period  $N$  and  $\hat{y}[n]$  has period  $M$ . The sequence  $\hat{w}[n]$  is defined as  $\hat{w}[n] = \hat{x}[n] + \hat{y}[n]$ .

(a) Show that  $\hat{w}[n]$  is periodic with period  $MN$ .

(b) Since  $\hat{x}[n]$  has period  $N$ , its discrete Fourier series coefficients  $a_k$  also have period  $N$ . Similarly, since  $\hat{y}[n]$  has period  $M$ , its discrete Fourier series coefficients  $b_k$  also have period  $M$ . The discrete Fourier series coefficients of  $\hat{w}[n]$ ,  $c_k$ , have period  $MN$ . Determine  $c_k$  in terms of  $a_k$  and  $b_k$ .

The sequence  $x[n] = (-1)^n$  is obtained by sampling the continuous-time sinusoidal signal  $x(t) = \cos \omega_0 t$  at 1-ms intervals, i.e.,

$$\cos(\omega_0 n T) = (-1)^n, \quad T = 10^{-3} \text{ s}$$

Determine three *distinct* possible values of  $\omega_0$ .



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### 3EC4-06: Network Theory

4 Credits

Max. Marks: 100 (IA:30, ETE:100)

3L:1T:0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	Node and Mesh Analysis, matrix approach of network containing voltage and current sources, and reactances, source transformation and duality.	7
2	Network theorems: Superposition, reciprocity, Thevenin's, Norton's, Maximum power Transfer, compensation and Tallegen's theorem as applied to AC. circuits.	7
3	Trigonometric and exponential Fourier series: Discrete spectra and symmetry of waveform, steady state response of a network to non-sinusoidal periodic inputs, power factor, effective values, Fourier transform and continuous spectra, three phase unbalanced circuit and power calculation.	8
4	Laplace transforms and properties: Partial fractions, singularity functions, waveform synthesis, analysis of RC, RL, and RLC networks with and without initial conditions with Laplace transforms evaluation of initial conditions..	8
5	Transient behavior, concept of complex frequency, Driving points and transfer functions poles and zeros of immittance function, their properties, sinusoidal response from pole-zero locations, convolution theorem and Two four port network and interconnections, Behaviors of series and parallel resonant circuits, Introduction to band pass, low pass, high pass and band reject filters.	10
<b>Total</b>		<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-06	Network Theory	CO 1	Apply the basic circuit law and simplify the network using network theorems
		CO 2	Appreciate the frequency domain techniques in different applications.
		CO 3	Apply Laplace Transform for steady state and transient analysis
		CO 4	Evaluate transient response and two-port network parameters
		CO 5	Analyze the series resonant and parallel resonant circuit and design filters

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
3EC4-06 Network Theory	CO 1	3	2		3	2							
	CO 2	3	3	1	2	2							1
	CO 3	3	2	2		2							1
	CO 4	2	3	2	2	1							
	CO 5	2	3	3	2	1							

**3: Strongly**

**2: Moderate**

**1: Weak**

### Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Overview of Network Theory and its significance
Lecture 2	Node and Mesh Analysis
Lecture 3	matrix approach of network containing voltage and current sources and reactances
Lecture 4	source transformation and duality
Lecture 5	Network theorems: Superposition and reciprocity
Lecture 6	Thevenin's and Norton's theorem
Lecture 7	Maximum power Transfer theorem
Lecture 8	compensation and Tallegen's theorem as applied to AC. Circuits
Lecture 9	Trigonometric and exponential Fourier series
Lecture 10	Fourier series: Discrete spectra and symmetry of waveform
Lecture 11	Steady state response of a network to non-sinusoidal periodic inputs
Lecture 12	power factor and effective values
Lecture 13	Fourier transform and continuous spectra
Lecture 14	three phase unbalanced circuit and power calculation
Lecture 15	three phase unbalanced circuit and power calculation
Lecture 16	Laplace transforms
Lecture 17	Laplace transforms
Lecture 18	Laplace transforms properties: Partial fractions
Lecture 19	singularity functions and waveform synthesis

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

### II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

Lecture 20	analysis of RC networks
Lecture 21	analysis of RL networks
Lecture 22	analysis of RLC networks
Lecture 23	Analysis of networks with and without initial conditions
Lecture 24	Analysis of networks with and without initial conditions
Lecture 25	Analysis of networks with and without initial conditions with lapalace transforms evaluation
Lecture 26	Analysis of networks with and without initial conditions with lapalace transforms evaluation of initial condition
Lecture 27	Transient behavior
Lecture 28	concept of complex frequency
Lecture 29	Driving points and transfer functions poles and zeros of immittance function
Lecture 30	Driving points and transfer functions poles and zeros of immittance function: their properties
Lecture 31	sinusoidal response from pole-zero locations
Lecture 32	sinusoidal response from pole-zero locations
Lecture 33	convolution theorem
Lecture 34	sinusoidal response from pole-zero locations
Lecture 35	Two four port network and interconnections
Lecture 36	Two four port network and interconnections
Lecture 37	Behaviors of series and parallel resonant circuits
Lecture 38	Introduction to band pass and low pass
Lecture 39	Introduction to high pass and reject filters
Lecture 40	Spill over class

#### Content delivery method:

1. Chalk and Duster
2. PPT
3. Hand-outs



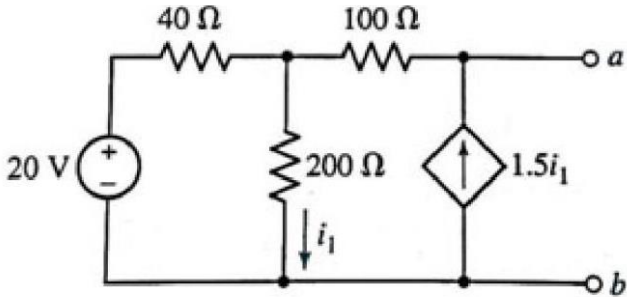
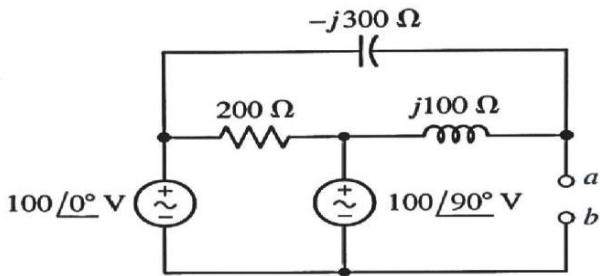


# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Sample assignments:

<b>Assignment 1</b>	<b>Q1.</b> Elaborate the significance of source transformation with relevant example
	<b>Q2.</b> State and prove time differentiation theorem in Laplace Transform
	<b>Q3.</b> Find the Thevenin equivalent of the network shown in figure. What power would be delivered to a load of 100 ohms at $a$ and $b$ ? 
<b>Assignment 2</b>	
<b>Q4.</b> Calculate Thevenin equivalent circuit with respect to terminals $a$ and $b$ 	
<b>Q5.</b> Derive transient current and voltage responses of sinusoidal driven RL and RC circuits.	
<b>Q6.</b> Specify the restrictions on pole and zero locations for transfer functions and driving-point functions.	



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### 3EC4-07: Electronic Devices

4 Credits

Max. Marks: 100 (IA:30, ETE:100)

3L:1T:0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	Introduction to Semiconductor Physics: Introduction, Energy band gap structures of semiconductors, Classifications of semiconductors, Degenerate and non-degenerate semiconductors, Direct and indirect band gap semiconductors, Electronic properties of Silicon, Germanium, Compound Semiconductor, Gallium Arsenide, Gallium phosphide & Silicon carbide, Variation of semiconductor conductivity, resistance and bandgap with temperature and doping. Thermistors, Sensitors.	6
2	Review of Quantum Mechanics, Electrons in periodic Lattices, E-k diagrams. Energy bands in intrinsic and extrinsic silicon; Carrier transport: diffusion current, drift current, mobility and resistivity; sheet resistance, design of resistors.	6
3	Generation and recombination of carriers; Poisson and continuity equation P-N junction characteristics, I-V characteristics, and small signal switching models; Avalanche breakdown, Zener diode, Schottky diode.	8
4	Bipolar Junction Transistor, I-V characteristics, Ebers-Moll Model, MOS capacitor, C-V characteristics, MOSFET, I-V characteristics, and small signal models of MOS transistor, LED, photodiode and solar cell.	11
5	Integrated circuit fabrication process: oxidation, diffusion, ion implantation, Photolithography, etching, chemical vapor deposition, sputtering, twin-tub CMOS process.	9
<b>Total</b>		<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-07	Electronic Devices	CO 1	Understanding the semiconductor physics of the intrinsic, P and N materials.
		CO 2	Understanding the characteristics of current flow in a bipolar junction transistor and MOSFET.
		CO 3	Understand and utilize the mathematical models of semiconductor junctions and MOS transistors for circuits and systems.
		CO 4	Analyze the characteristics of different electronic devices such as Amplifiers, LEDs, Solar cells, etc.
		CO 5	Theoretical as well as experimental understanding of Integrated circuit fabrication.

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
		3EC4-07 Electronic Devices	CO 1	3	1		2	1	1				
	CO 2	3	2	1			2						
	CO 3	2	1		2		1	2					
	CO 4	3	1	1				2					
	CO 5	3	1	1	1	1							2

**3: Strongly**

**2: Moderate**

**1: Weak**

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Introduction to Semiconductor Physics
Lecture 3	Introduction to Semiconductor Physics
Lecture 4	Introduction to Semiconductor Physics
Lecture 5	Review of Quantum Mechanics
Lecture 6	Electrons in periodic Lattices
Lecture 7	E-k diagrams
Lecture 8	Energy bands in intrinsic and extrinsic silicon
Lecture 9	Carrier transport: diffusion current, drift current, mobility and resistivity
Lecture 10	Sheet resistance and design of resistors
Lecture 11	Generation and recombination of carriers
Lecture 12	Poisson and continuity equation
Lecture 13	P-N junction characteristics and their I-V characteristics
Lecture 14	P-N junction characteristics and their I-V characteristics
Lecture 15	P-N junction small signal switching models
Lecture 16	P-N junction small signal switching models
Lecture 17	Avalanche breakdown
Lecture 18	Zener diode and Schottky diode
Lecture 19	Basics of Bipolar Junction Transistor
Lecture 20	I-V characteristics of BJT
Lecture 21	Ebers-Moll Model
Lecture 22	MOS capacitor
Lecture 23	MOS capacitor

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

**II Year - III Semester: B.Tech. (Electronics & Communication Engineering)**

Lecture 24	C-V characteristics
Lecture 25	Basics of MOSFET
Lecture 26	Basics of MOSFET
Lecture 27	I-V characteristics of MOSFET
Lecture 28	Small signal models of MOS transistor
Lecture 29	Small signal models of MOS transistor
Lecture 30	Light Emitting Diode
Lecture 31	Photodiode and solar cell
Lecture 32	Basics of Integrated Circuits
Lecture 33	Advancement in Integrated Circuits
Lecture 34	Oxidation, diffusion and ion implantation
Lecture 35	Photolithography and etching
Lecture 36	Chemical vapor deposition
Lecture 37	Sputtering
Lecture 38	Twin-tub CMOS process
Lecture 39	Spill over class
Lecture 40	Spill over class

### **Content delivery method:**

1. Chalk and Duster
2. PPT
3. Hand-outs



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Sample assignments:

<b>Assignment 1</b>	<b>Q1.</b> Investigates the input/output characteristics of various diodes?
	<b>Q2.</b> Investigate the applications of various diodes?
	<b>Q3.</b> A p-type sample of silicon has a resistivity of $5 \Omega\text{-cm}$ . In this sample, the hole mobility, $\mu_h$ , is $600 \text{ cm}^2/\text{V-s}$ and the electron mobility, $\mu_e$ , is $1600 \text{ cm}^2/\text{V-s}$ . Ohmic contacts are formed on the ends of the sample and a uniform electric field is imposed which results in a drift current density in the sample is $2 \times 10^3 \text{ A/cm}^2$ . [1]. What are the hole and electron concentrations in this sample? [2]. What are the hole and electron drift velocities under these conditions? [3]. What is the magnitude of the electric field?
<b>Assignment 2</b>	<b>Q1.</b> Discuss the applications of Ebers-Moll Model.
	<b>Q2.</b> Discuss different types of fabrication techniques.
	<b>Q3.</b> Discuss various characteristics of CMOS transistor.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### 3EC4-21: Electronics Devices Lab

1 Credit

Max. Marks: 100 (IA:60, ETE:40)

OL:0T:2P

#### List of Experiments

Sr. No.	Name of Experiment
1.	Study the following devices: (a) Analog & digital multimeters (b) Function/Signal generators (c) Regulated d. c. power supplies (constant voltage and constant current operations) (d) Study of analog and digital CRO, measurement of time period, amplitude, frequency & phase angle using Lissajous figures.
2.	Plot V-I characteristic of P-N junction diode & calculate cut-in voltage, reverse Saturation current and static & dynamic resistances.
3.	Plot the output waveform of half wave rectifier and effect of filters on waveform. Also calculate its ripple factor.
4.	Study bridge rectifier and measure the effect of filter network on D.C. voltage output & ripple factor.
5.	Plot and verify output waveforms of different clipper and clamper.
6.	Plot V-I characteristic of Zener diode
7.	Study of Zener diode as voltage regulator. Observe the effect of load changes and determine load limits of the voltage regulator
8.	Plot input-output characteristics of BJT in CB, CC and CE configurations. Find their h-parameters.
9.	Study of different biasing circuits of BJT amplifier and calculate its Q-point.
10.	Plot frequency response of two stage RC coupled amplifier & calculate its bandwidth .
11.	Plot input-output characteristics of field effect transistor and measure $I_{DSS}$ and $V_p$ .
12.	Plot frequency response curve for FET amplifier and calculate its gain bandwidth product.

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-21	Electronic Devices Lab	CO 1	Understand the characteristics of different Electronic Devices.
		CO 2	Verify the rectifier circuits using diodes and implement them using hardware.
		CO 3	Design various amplifiers like CE, CC, common source amplifiers and implement them using hardware and also observe their frequency responses
		CO 4	Understand the construction, operation and characteristics of JFET and MOSFET, which can be used in the design of amplifiers.
		CO 5	Understand the need and requirements to obtain frequency response from a transistor so that Design of RF amplifiers and other high frequency amplifiers is feasible

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
		3EC4-21 Electronic Devices Lab	CO 1	3	2	3	2	1					
	CO 2	2	3	1	3	3							2
	CO 3	2	1	2	3	3							
	CO 4	3	2	3	2	2							1
	CO 5	3	2	1	2	2							

**3: Strongly**

**2: Moderate**

**1: Weak**

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### 3EC4-22: Digital System Design Lab

1 Credit

Max. Marks: 100 (IA:60, ETE:40)

OL:OT:2P

#### List of Experiments

S.No.	Name of Experiment
<b>Part A: Combinational Circuits</b>	
1.	To verify the truth tables of logic gates: AND, OR, NOR, NAND, NOR, Ex-OR and Ex-NOR
2.	To verify the truth table of OR, AND, NOR, Ex-OR, Ex-NOR logic gates realized using NAND & NOR gates.
3.	To realize an SOP and POS expression.
4.	To realize Half adder/ Subtractor & Full Adder/ Subtractor using NAND & NOR gates and to verify their truth tables
5.	To realize a 4-bit ripple adder/ Subtractor using basic Half adder/ Subtractor & basic Full Adder/ Subtractor.
6.	To design 4-to-1 multiplexer using basic gates and verify the truth table. Also verify the truth table of 8-to-1 multiplexer using IC
7.	To design 1-to-4 demultiplexer using basic gates and verify the truth table. Also to construct 1-to-8 demultiplexer using blocks of 1-to-4 demultiplexer
8.	To design 2x4 decoder using basic gates and verify the truth table. Also verify the truth table of 3x8 decoder using IC
9.	Design & Realize a combinational circuit that will accept a 2421 BCD code and drive a TIL -312 seven-segment display
<b>Part B: Sequential Circuits</b>	
10.	Using basic logic gates, realize the R-S, J-K and D-flip flops with and without clock signal and verify their truth table.
11.	Construct a divide by 2, 4 & 8 asynchronous counter. Construct a 4-bit binary counter and ring counter for a particular output pattern using D flip flop.
12.	Design and construct unidirectional shift register and verify the
13.	Design and construct BCD ripple counter and verify the function.
14.	Design and construct a 4 Bit Ring counter and verify the function
15.	Perform input/output operations on parallel in/Parallel out and Serial in/Serial out registers using clock. Also exercise loading only one of multiple values into the register using multiplexer.

**Note:** Minimum 6 experiments to be conducted from **Part-A** & 4 experiments to be conducted from **Part-B**.

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-22	Digital System Design Lab	CO 1	
		CO 2	To minimize the complexity of digital logic circuits.
		CO 3	To design and analyse combinational logic circuits.
		CO 4	To design and analyse sequential logic circuits.
		CO 5	Able to implement applications of combinational & sequential logic circuits.

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
3EC4-22 Digital System Design Lab	CO 1	3	3	1									1
	CO 2	3	3	2	1	1							1
	CO 3	3	3	3	2	3	1						2
	CO 4	3	3	3	2	3	1						2
	CO 5	3	3	3	3	3	3						3

**3: Strongly**

**2: Moderate**

**1: Weak**

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### 3EC4-23: Signal Processing Lab

1 Credit

Max. Marks: 100 (IA:60, ETE:40)

0L:0T:2P

#### List of Experiments

Sr. No.	Name of Experiment (Simulate using MATLAB environment)
1.	Generation of continuous and discrete elementary signals (periodic and non periodic) using mathematical expression.
2.	Generation of Continuous and Discrete Unit Step Signal.
3.	Generation of Exponential and Ramp signals in Continuous & Discrete domain.
4.	Continuous and discrete time Convolution (using basic definition).
5.	Adding and subtracting two given signals. (Continuous as well as Discrete signals)
6.	To generate uniform random numbers between (0, 1).
7.	To generate a random binary wave.
8.	To generate and verify random sequences with arbitrary distributions, means and variances for following: (a) Rayleigh distribution (b) Normal distributions: $N(0,1)$ . (c) Gaussian distributions: $N(m, x)$
9.	To plot the probability density functions. Find mean and variance for the above distributions

#### Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-23	Signal Processing Lab	CO 1	Able to generate different Continuous and Discrete time signals.
		CO 2	Understand the basics of signals and different operations on signals.
		CO 3	Develop simple algorithms for signal processing and test them using MATLAB
		CO 4	Able to generate the random signals having different distributions, mean and variance.
		CO 5	Design and conduct experiments, interpret and analyse data and report results.

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
<b>3EC4-23</b> <b>Signal Processing</b> <b>Lab</b>	CO 1	2		1		2							
	CO 2	3		1									
	CO 3	1	2	3	1	3							
	CO 4	2	1	1		2							
	CO 5	1	1	2	2	2							

**3: Strongly**

**2: Moderate**

**1: Weak**

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - III Semester: B.Tech. (Electronics & Communication Engineering)

### 3EC3-24: Computer Programming Lab-I

**1 Credit**

**Max. Marks: 100 (IA:60, ETE:40)**

**OL:0T:2P**

1.	Write a simple C program on a 32 bit compiler to understand the concept of array storage, size of a word. The program shall be written illustrating the concept of row major and column major storage. Find the address of element and verify it with the theoretical value. Program may be written for arrays upto 4-dimensions.
2.	Simulate a stack, queue, circular queue and dequeue using a one dimensional array as storage element. The program should implement the basic addition, deletion and traversal operations.
3.	Represent a 2-variable polynomial using array. Use this representation to implement addition of polynomials.
4.	Represent a sparse matrix using array. Implement addition and transposition operations using the representation.
5.	Implement singly, doubly and circularly connected linked lists illustrating operations like addition at different locations, deletion from specified locations and traversal.
6.	Repeat exercises 2, 3 & 4 with linked structures.
7.	Implementation of binary tree with operations like addition, deletion, traversal.
8.	Depth first and breadth first traversal of graphs represented using adjacency matrix and list.
9.	Implementation of binary search in arrays and on linked Binary Search Tree.
10.	Implementation of insertion, quick, heap, topological and bubble sorting algorithms.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Teaching & Examination Scheme B.Tech. : Electronics & Communication Engineering 2<sup>nd</sup> Year - IV Semester

THEORY											
SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
1	BSC	4EC2-01	Advanced Engineering Mathematics-II	3	0	0	3	30	70	100	3
2	HSMC	4EC1-03/ 4EC1-02	Managerial Economics and Financial Accounting/ Technical Communication	2	0	0	2	30	70	100	2
3	PCC	4EC4-04	Analog Circuits	3	0	0	3	30	70	100	3
4		4EC4-05	Microcontrollers	3	0	0	3	30	70	100	3
5	ESC	4EC3-06	Electronics Measurement & Instrumentation	3	0	0	3	30	70	100	3
6	PCC	4EC4-07	Analog and Digital Communication	3	0	0	3	30	70	100	3
<b>Sub Total</b>				17	0	0					17
PRACTICAL & SESSIONAL											
8	PCC	4EC4-21	Analog and Digital Communication Lab	0	0	3		60	40	100	1.5
9		4EC4-22	Analog Circuits Lab	0	0	3		60	40	100	1.5
10		4EC4-23	Microcontrollers Lab	0	0	3		60	40	100	1.5
11		4EC4-24	Electronics Measurement & Instrumentation Lab	0	0	3		60	40	100	1.5
12	SODE CA	4EC18-00	Social Outreach, Discipline & Extra Curricular Activities							100	0.5
<b>Sub- Total</b>				0	0	12					6.5
<b>TOTAL OF IV SEMEESTER</b>				17	0	12					23.5

**L:** Lecture, **T:** Tutorial, **P:** Practical, **Cr:** Credits

**ETE:** End Term Exam, **IA:** Internal Assessment

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### 4EC2-01: Advance Engineering Mathematics-II

Credit: 3

Max. Marks: 100(IA:30, ETE:70)

3L+0T+0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>Complex Variable – Differentiation:</b> Differentiation, Cauchy-Riemann equations, analytic functions, harmonic functions, finding harmonic conjugate; elementary analytic functions (exponential, trigonometric, logarithm) and their properties; Conformal mappings, Mobius transformations and their properties.	7
3	<b>Complex Variable - Integration:</b> Contour integrals, Cauchy-Goursat theorem (without proof), Cauchy Integral formula (without proof), Liouville's theorem and Maximum-Modulus theorem (without proof); Taylor's series, zeros of analytic functions, singularities, Laurent's series; Residues, Cauchy Residue theorem (without proof).	8
4	<b>Applications of complex integration by residues:</b> Evaluation of definite integral involving sine and cosine. Evaluation of certain improper integrals.	4
5	<b>Special Functions:</b> Legendre's function, Rodrigues formula, generating function, Simple recurrence relations, orthogonal property. Bessel's functions of first and second kind, generating function, simple recurrence relations, orthogonal property.	10
6	<b>Linear Algebra:</b> Vector Spaces, subspaces, Linear independence, basis and dimension, Inner product spaces, Orthogonality, Gram Schmidt orthogonalization, characteristic polynomial, minimal polynomial, positive definite matrices and canonical forms, QR decomposition.	10
<b>Total</b>		<b>40</b>





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

4EC1-03/3EC1-03: Managerial Economics And Financial Accounting

2 Credit

Max. Marks: 100(IA:30, ETE:70)

2L:0T:0P

End Term Exam: 2 Hours

SN	Contents	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>Basic economic concepts:</b> Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	3
3	<b>Demand and Supply analysis:</b> Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
4	<b>Production and Cost analysis:</b> Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
5	<b>Market structure and pricing theory:</b> Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
6	<b>Financial statement analysis:</b> Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	<b>Total</b>	<b>26</b>





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### 4EC1-02/3EC1-02: Technical Communication

2 Credit  
2L:0T:0P

Max. Marks: 100(IA:30, ETE:70)  
End Term Exam: 2 Hours

SN	Contents	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>Introduction to Technical Communication-</b> Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	3
3	<b>Comprehension of Technical Materials/Texts and Information Design &amp; development-</b> Reading of technical texts, Reading and comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
4	<b>Technical Writing, Grammar and Editing-</b> Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
5	<b>Advanced Technical Writing-</b> Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
<b>Total</b>		<b>26</b>

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### 4EC4-04: Analog Circuits

Credit: 3

Max. Marks: 100(IA:30, ETE:70)

3L+0T+0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	Diode Circuits, Amplifier models: Voltage amplifier, current amplifier, trans-conductance amplifier and trans-resistance amplifier. Biasing schemes for BJT and FET amplifiers, bias stability, various configurations (such as CE/CS, CB/CG, CC/CD) and their features, small signal analysis, low frequency transistor models, estimation of voltage gain, input resistance, output resistance etc., design procedure for particular specifications, low frequency analysis of multistage amplifiers.	8
3	High frequency transistor models, frequency response of single stage and multistage amplifiers, cascode amplifier. Various classes of operation (Class A, B, AB, C etc.), their power efficiency and linearity issues. Feedback topologies: Voltage series, current series, voltage shunt, current shunt, effect of feedback on gain, bandwidth etc., calculation with practical circuits, concept of stability, gain margin and phase margin.	8
4	Oscillators: Review of the basic concept, Barkhausen criterion, RC oscillators (phase shift, Wien bridge etc.), LC oscillators (Hartley, Colpitt, Clapp etc.), non-sinusoidal oscillators. Current mirror: Basic topology and its variants, V-I characteristics, output resistance and minimum sustainable voltage (VON), maximum usable load. Differential amplifier: Basic structure and principle of operation, calculation of differential gain, common mode gain, CMRR and ICMR. OP-AMP design: design of differential amplifier for a given specification, design of gain stages and output stages, compensation.	8
5	OP-AMP applications: review of inverting and non-inverting amplifiers, integrator and differentiator, summing amplifier, precision rectifier, Schmitt trigger and its applications. Active filters: Low pass, high pass, band pass and band stop, design guidelines.	8
6	Digital-to-analog converters (DAC): Weighted resistor, R-2R ladder, resistor string etc. Analog to digital converters (ADC): Single slope, dual slope, successive approximation, flash etc. Switched capacitor circuits: Basic concept, practical configurations, application in amplifier, integrator, ADC etc.	7
<b>Total</b>		<b>40</b>

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-04	Analog Circuits	CO 1	Understand the characteristics of diodes and transistors
		CO 2	Design and analyze various rectifier and amplifier circuits
		CO 3	Design sinusoidal and non-sinusoidal oscillators
		CO 4	Understand the functioning of OP-AMP and design OP-AMP based circuits
		CO 5	Understanding the designing of ADCs and DACs

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-04 Analog Circuits	CO 1	3		1	1	2							
	CO 2	1	1	2		1							
	CO 3	3	1		1								
	CO 4	2				2							
	CO 5	2	3		2								

3: Strongly

2: Moderate

1: Weak

Office of Dean Academic Affairs  
Rajasthan Technical University, Kota



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Diode Circuits and Amplifier models
Lecture 3	Voltage amplifier, current amplifier, trans-conductance amplifier and trans-resistance amplifier
Lecture 4	Biasing schemes for BJT and FET amplifiers
Lecture 5	Bias stability in various configurations such as CE/CS, CB/CG, CC/CD
Lecture 6	Small signal analysis of BJT and FET
Lecture 7	low frequency transistor models
Lecture 8	Estimation of voltage gain, input resistance, output resistance etc.
Lecture 9	Design procedure for particular specifications, low frequency analysis of multistage amplifiers.
Lecture 10	High frequency transistor models
Lecture 11	frequency response of single stage and multistage amplifiers
Lecture 12	Cascode Amplifier
Lecture 13	Various classes of operation (Class A, B, AB, C etc.), their power efficiency and linearity issues
Lecture 14	Feedback topologies: Voltage series, current series, voltage shunt, current shunt
Lecture 15	Effect of feedback on gain, bandwidth etc.,
Lecture 16	Calculation with practical circuits
Lecture 17	Concept of stability, gain margin and phase margin.
Lecture 18	Basics of oscillator
Lecture 19	Barkhausen criterion, RC oscillators (phase shift, Wien bridge etc.)

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

Lecture 20	LC oscillators (Hartley, Colpitt, Clapp etc.)
Lecture 21	Non-sinusoidal oscillators. Current mirror: Basic topology and its variants,
Lecture 22	V-I characteristics, output resistance and minimum sustainable voltage (VON), maximum usable load.
Lecture 23	Differential amplifier: Basic structure and principle of operation, calculation of differential gain, common mode gain, CMRR and ICMR.
Lecture 24	OP-AMP design: design of differential amplifier for a given specification
Lecture 25	Design of gain stages and output stages, compensation
Lecture 26	OP-AMP applications: review of inverting and non-inverting amplifiers
Lecture 27	Integrator and differentiator, summing amplifier
Lecture 28	Precision rectifier, Schmitt trigger and its applications
Lecture 29	Active filters: Low pass, high pass
Lecture 30	Band pass and band stop Filters
Lecture 31	Filter Design guidelines
Lecture 32	Digital-to-analog converters (DAC): Weighted resistor, R-2R ladder, resistor string etc
Lecture 33	Analog to digital converters (ADC): Single slope, dual slope
Lecture 34	successive approximation, flash TYPE ADC
Lecture 35	Switched capacitor circuits: Basic concept
Lecture 36	Switched capacitor circuits: practical configurations
Lecture 37	Switched capacitor circuits: applications
Lecture 38	Spill over classes
Lecture 39	Spill over classes
Lecture 40	Spill over classes

### Content delivery method:

1. Chalk and Duster
2. PPT
3. Hand-outs

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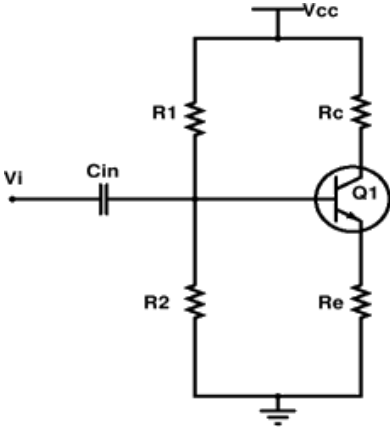
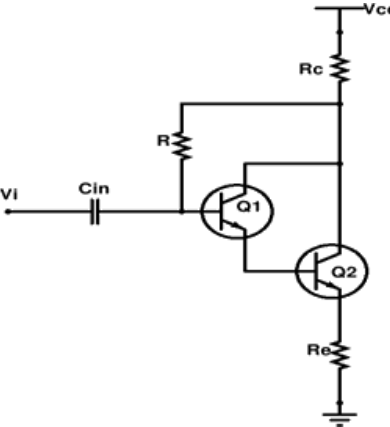


# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Sample assignments:

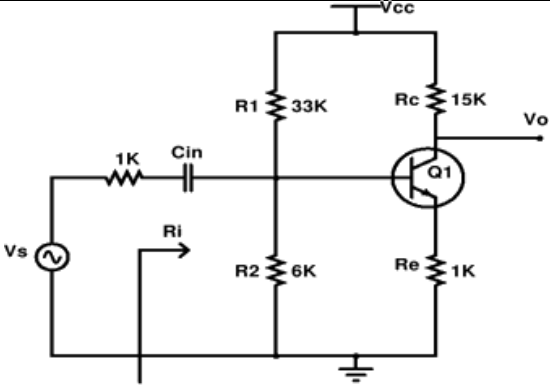
<p><b>Assignment 1</b></p>	<p><b>Q1.</b> Assume that a silicon transistor with <math>\beta = 50</math>, <math>V_{BE\text{active}} = 0.7 \text{ V}</math>, <math>V_{CC} = 15\text{V}</math> and <math>R_C = 10\text{K}</math> is used in the Fig.1. It is desired to establish a Q-point at <math>V_{CE} = 7.5 \text{ V}</math> and <math>I_C = 5\text{mA}</math> and stability factor <math>S \leq 5</math>. Find <math>R_e</math>, <math>R_1</math> and <math>R_2</math>.</p> 
	<p><b>Q2.</b> In the Darlington stage shown in Fig.2, <math>V_{CC} = 15\text{V}</math>, <math>\beta_1 = 50</math>, <math>\beta_2 = 75</math>, <math>V_{BE} = 0.7\text{V}</math>, <math>R_C = 750 \Omega</math> and <math>R_E = 100 \Omega</math>. If at the quiescent point <math>V_{CE2} = 6\text{V}</math> determine the value of <math>R</math>.</p> 
	<p><b>Q3.</b> For the amplifier shown in Fig.3 using a transistor whose parameters are <math>h_{ie} = 1100</math>, <math>h_{re} = 2.5 \times 10^{-4}</math>, <math>h_{fe} = 50</math>, <math>h_{oe} = 24 \mu\text{A/V}</math>. Find <math>A_I</math>, <math>A_V</math>, <math>A_{VS}</math> and <math>R_i</math>.</p>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

	
<b>Assignment 2</b>	<p><b>Q1.</b> Discuss the applications of operational amplifier.</p> <p><b>Q2.</b> Discuss different types of filters.</p> <p><b>Q3.</b> Discuss Dual counter type DAC and its applications</p>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### 4EC4-05: Microcontrollers

Credit: 3

Max. Marks: 100(IA:30, ETE:70)

3L+0T+0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	Overview of microcomputer systems and their building blocks, memory interfacing, concepts of interrupts and Direct Memory Access, instruction sets of microprocessors (with examples of 8085 and 8086);	10
3	Interfacing with peripherals - timer, serial I/O, parallel I/O, A/D and D/A converters; Arithmetic Coprocessors; System level interfacing design;	8
4	Concepts of virtual memory, Cache memory, Advanced coprocessor Architectures- 286, 486, Pentium; Microcontrollers: 8051 systems,	10
5	Introduction to RISC processors; ARM microcontrollers interface designs.	11
	<b>Total</b>	<b>40</b>

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-05	Microcontrollers	CO 1	Develop assembly language programming skills.
		CO 2	Able to build interfacing of peripherals like, I/O, A/D, D/A, timer etc.
		CO 3	Develop systems using different microcontrollers.
		CO 4	Explain the concept of memory organization.
		CO 5	Understand RSIC processors and design ARM microcontroller based systems.

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC04-05 Microcontrollers	CO 1			3	1								
	CO 2			3		1							
	CO 3	1	2	3									
	CO 4	3	2	1									
	CO 5			3	2	1							

3: Strongly

2: Moderate

1: Weak

### Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Overview of microcomputer systems and their building blocks
Lecture 3	Overview of microcomputer systems and their building blocks
Lecture 4	Memory interfacing
Lecture 5	Memory interfacing
Lecture 6	Concepts of interrupts

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

Lecture 7	Direct Memory Access
Lecture 8	Direct Memory Access
Lecture 9	Instruction sets of microprocessors (with examples of 8085 and 8086)
Lecture 10	Instruction sets of microprocessors (with examples of 8085 and 8086)
Lecture 11	Instruction sets of microprocessors (with examples of 8085 and 8086)
Lecture 12	Instruction sets of microprocessors (with examples of 8085 and 8086)
Lecture 13	Interfacing with peripherals
Lecture 14	Timer
Lecture 15	Serial I/O
Lecture 16	Parallel I/O
Lecture 17	A/D and D/A converters;
Lecture 18	A/D and D/A converters
Lecture 19	Arithmetic Coprocessors
Lecture 20	System level interfacing design
Lecture 21	Concepts of virtual memory, Cache memory
Lecture 22	Concepts of virtual memory, Cache memory
Lecture 23	Advanced coprocessor Architectures- 286, 486, Pentium
Lecture 24	Advanced coprocessor Architectures- 286, 486, Pentium
Lecture 25	Advanced coprocessor Architectures- 286, 486, Pentium
Lecture 26	Microcontrollers: 8051 systems,
Lecture 27	Microcontrollers: 8051 systems,
Lecture 28	Microcontrollers: 8051 systems,
Lecture 29	Microcontrollers: 8051 systems,
Lecture 30	Microcontrollers: 8051 systems,
Lecture 31	Introduction to RISC processors
Lecture 32	Introduction to RISC processors
Lecture 33	Introduction to RISC processors
Lecture 34	ARM microcontrollers interface designs
Lecture 35	ARM microcontrollers interface designs
Lecture 36	ARM microcontrollers interface designs
Lecture 37	ARM microcontrollers interface designs
Lecture 38	ARM microcontrollers interface designs
Lecture 39	Spill Over Classes
Lecture 40	Spill Over Classes

### Content delivery method:

1. Chalk and Duster
2. PPT
3. Hand-outs

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Rajasthan Technical University, Kota



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Assignments:

<b>Assignment 1</b>	<b>Q1.</b> Compare between microprocessor & microcontroller based on no. of instructions used, registers, memory and applications.
	<b>Q2.</b> Interface external program memory with 8051 & explain how the data is transfer.
	<b>Q3.</b> List the I/O ports of microcontroller 8051. Explain their alternative function?
<b>Assignment 2</b>	<b>Q1.</b> Explain RISC and CISC?
	<b>Q2.</b> Without using MUL instruction, perform multiplication operation on any two operands, with both of them being: a. Positive numbers b. One positive and other negative number c. Both negative numbers Verify the values computed.
	<b>Q3.</b> Can you brief up the evolution of ARM architecture?



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### 4EC3-06: Electronics Measurement & Instrumentation

Credit: 3

Max. Marks: 100(IA:30, ETE:70)

3L+0T+0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>THEORY OF ERRORS</b> - Accuracy & precision, Repeatability, Limits of errors, Systematic & random errors, Modeling of errors, Probable error & standard deviation, Gaussian error analysis, Combination of errors.	8
3	<b>ELECTRONIC INSTRUMENTS</b> - Electronic Voltmeter, Electronic Multimeters, Digital Voltmeter, and Component Measuring Instruments: Q meter, Vector Impedance meter, RF Power & Voltage Measurements, Introduction to shielding & grounding.	8
4	<b>OSCILLOSCOPES</b> – CRT Construction, Basic CRO circuits, CRO Probes, Techniques of Measurement of frequency, Phase Angle and Time Delay, Multibeam, multi trace, storage & sampling Oscilloscopes.	7
5	<b>SIGNAL GENERATION AND SIGNAL ANALYSIS</b> - Sine wave generators, Frequency synthesized signal generators, Sweep frequency generators. Signal Analysis - Measurement Technique, Wave Analyzers, and Frequency - selective wave analyser, Heterodyne wave analyser, Harmonic distortion analyser, and Spectrum analyser.	8
6	<b>TRANSDUCERS</b> - Classification, Selection Criteria, Characteristics, Construction, Working Principles and Application of following Transducers:- RTD, Thermocouples, Thermistors, LVDT, Strain Gauges, Bourdon Tubes, Seismic Accelerometers, Tachogenerators, Load Cell, Piezoelectric Transducers, Ultrasonic Flow Meters.	8
<b>Total</b>		<b>40</b>

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC3-06	ELECTRONIC MEASUREMENT & INSTRUMENTATION	CO 1	<b>Describe</b> the use of various electrical/electronic instruments, their block diagram, applications, and principles of operation, standards errors and units of measurements.
		CO 2	<b>Develop</b> basic skills in the design of electronic equipments
		CO 3	<b>Analyse</b> different electrical/electronic parameters using state of equipments of measuring instruments which is require to all types of industries.
		CO 4	<b>Solve</b> :Identify electronics/ electrical instruments, understanding associated with the instruments
		CO 5	<b>Explain</b> use of transducers in different types of field applications

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC3-06 ELECTRONIC MEASUREMENT & INSTRUMENTATION	CO 1	3	2	1									
	CO 2	2	2	2	3								
	CO 3	2	3										
	CO 4	2	1	1				2					
	CO 5	3	1										2

3: Strongly

2: Moderate

1: Weak

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Theory of errors
Lecture 3	Accuracy & precision, Repeatability
Lecture 4	Limits of Time-Hours errors
Lecture 5	Systematic & random errors
Lecture 6	Modeling of errors
Lecture 7	Probable error
Lecture 8	standard deviation
Lecture 9	Gaussian error analysis
Lecture 10	Combination of errors
Lecture 11	Electronic instruments - Electronic Voltmeter
Lecture 12	Electronic Multimeters
Lecture 13	Digital Voltmeter
Lecture 14	Component Measuring Instruments: Q meter
Lecture 15	Vector Impedance meter
Lecture 16	RF Power & Voltage Measurements
Lecture 17	Introduction to shielding & grounding
Lecture 18	Oscilloscopes - CRT Construction
Lecture 19	Basic CRO circuits, CRO Probes
Lecture 20	Techniques of Measurement of frequency, Phase Angle and Time Delay
Lecture 21	Multibeam, multi trace, storage & sampling Oscilloscopes
Lecture 22	Multibeam, multi trace, storage & sampling Oscilloscopes
Lecture 23	Signal generation and signal analysis - Sine wave generators,
Lecture 24	Frequency synthesized signal generators
Lecture 25	Sweep frequency generators
Lecture 26	Signal Analysis - Measurement Technique
Lecture 27	Wave Analyzers, and Frequency - selective wave analyser
Lecture 28	Heterodyne wave analyser
Lecture 29	Harmonic distortion analyser
Lecture 30	Spectrum analyser
Lecture 31	Transducers – Classification
Lecture 32	Selection Criteria Characteristics
Lecture 33	Construction, Working Principles and Application of following Transducers:- RTD

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

Lecture 34	Thermocouples
Lecture 35	Thermistors
Lecture 36	LVDT Strain Gauges, Bourdon Tubes
Lecture 37	Seismic Accelerometers
Lecture 38	Tachogenerators, Load Cell,
Lecture 39	Piezoelectric Transducers
Lecture 40	Ultrasonic Flow Meters

### Content delivery method:

1. Chalk and Duster
2. PPT
3. Hand-outs

### Sample assignments:

<b>Assignment 1</b>	<b>Q1.</b> Write the principal of an AC Bridge used for the measurement of Unknown capacitor
	<b>Q2.</b> Distinguish Between Accuracy and Precision?
	<b>Q3.</b> Explain flow measurement with a suitable example.
<b>Assignment 2</b>	<b>Q1.</b> What are primary sensing elements and transducers?
	<b>Q2.</b> A Wheatstone Bridge requires to change of $7\Omega$ in unknown arm of bridge to change in deflection of 14 mm. of galvanometer determine the sensitivity and deflection factor.
	<b>Q3.</b> Explain the terms static error, static correction, relative error and percentage relative error.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### 4EC4-07: Analog and Digital Communication

Credit: 3  
3L+0T+0P

Max. Marks: 100(IA:30, ETE:70)  
End Term Exam: 3 Hours

SN	Contents	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	Review of signals and systems, Frequency domain representation of signals, Principles of Amplitude Modulation Systems- DSB, SSB and VSB modulations. Angle Modulation, Representation of FM and PM signals, Spectral characteristics of angle modulated signals.	8
3	Review of probability and random process. Gaussian and white noise characteristics, Noise in amplitude modulation systems, Noise in Frequency modulation systems. Pre-emphasis and Deemphasis, Threshold effect in angle modulation.	7
4	Pulse modulation. Sampling process. Pulse Amplitude and Pulse code modulation (PCM), Differential pulse code modulation. Delta modulation, Noise considerations in PCM, Time Division multiplexing, Digital Multiplexers.	8
5	Elements of Detection Theory, Optimum detection of signals in noise, Coherent communication with waveforms- Probability of Error evaluations. Baseband Pulse Transmission- Inter symbol Interference and Nyquist criterion. Pass band Digital Modulation schemes- Phase Shift Keying, Frequency Shift Keying, Quadrature Amplitude Modulation, Continuous Phase Modulation and Minimum Shift Keying.	8
6	Digital Modulation tradeoffs. Optimum demodulation of digital signals over band-limited channels- Maximum likelihood sequence detection (Viterbi receiver). Equalization Techniques. Synchronization and Carrier Recovery for Digital modulation.	8
<b>Total</b>		<b>40</b>

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-07	Analog and Digital Communication	CO 1	Analyze and compare different analog modulation schemes for their efficiency and bandwidth
		CO 2	Analyze the behavior of a communication system in presence of noise
		CO 3	Investigate pulsed modulation system and analyze their system performance
		CO 4	Analyze different digital modulation schemes and can compute the bit error performance
		CO 5	Design a communication system comprised of both analog and digital modulation techniques

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-07 Analog & Digital Communication	CO 1	3	3		3		1				1		
	CO 2	3	2		3		1						
	CO 3	3	2		3		2						
	CO 4	3	3		3		2				1		
	CO 5	3	2	3	3		3			2	2		

3: Strongly

2: Moderate

1: Weak

### Content delivery method:

1. Chalk and Duster
2. PPT

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Introduction to the COURSE
Lecture 2	Review of signals and systems, Frequency domain representation of signals
Lecture 3	Principles of Amplitude Modulation Systems- DSB, SSB and VSB modulations
Lecture 4	Principles of Amplitude Modulation Systems- DSB, SSB and VSB modulations
Lecture 5	Principles of Amplitude Modulation Systems- DSB, SSB and VSB modulations
Lecture 6	Angle Modulation, Representation of FM and PM signals
Lecture 7	Angle Modulation, Representation of FM and PM signals
Lecture 8	Spectral characteristics of angle modulated signals.
Lecture 9	Review of probability and random process
Lecture 10	Review of probability and random process
Lecture 11	Noise in amplitude modulation systems
Lecture 12	Noise in amplitude modulation systems
Lecture 13	Noise in Frequency modulation systems
Lecture 14	Pre-emphasis and Deemphasis
Lecture 15	Threshold effect in angle modulation
Lecture 16	Pulse modulation. Sampling
Lecture 17	Pulse Amplitude and Pulse code modulation (PCM)
Lecture 18	Pulse Amplitude and Pulse code modulation (PCM)
Lecture 19	Differential pulse code modulation
Lecture 20	Delta modulation
Lecture 21	Noise considerations in PCM

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

**II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)**

Lecture 22	Time Division multiplexing, Digital Multiplexers
Lecture 23	Elements of Detection Theory
Lecture 24	Optimum detection of signals in noise
Lecture 25	Coherent communication with waveforms- Probability of Error evaluations
Lecture 26	Coherent communication with waveforms- Probability of Error evaluations
Lecture 27	Baseband Pulse Transmission- Inter symbol Interference and Nyquist criterion
Lecture 28	Baseband Pulse Transmission- Inter symbol Interference and Nyquist criterion
Lecture 29	Pass band Digital Modulation schemes
Lecture 30	Phase Shift Keying
Lecture 31	Frequency Shift Keying
Lecture 32	Quadrature Amplitude Modulation
Lecture 33	Continuous Phase Modulation and Minimum Shift Keying.
Lecture 34	Digital Modulation tradeoffs
Lecture 35	Optimum demodulation of digital signals over band-limited channels
Lecture 36	Optimum demodulation of digital signals over band-limited channels
Lecture 37	Maximum likelihood sequence detection (Viterbi receiver)
Lecture 38	Equalization Techniques
Lecture 39	Synchronization and Carrier Recovery for Digital modulation
Lecture 40	Synchronization and Carrier Recovery for Digital modulation



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Assignments:

<b>Assignment 1</b>	<b>Q1.</b> Design Modulator and Demodulator of SSB-SC Modulation based on its mathematical expression.
	<b>Q2.</b> Derive the figure of merit in a) FM Receiver      b) PM Receiver
	<b>Q3.</b> A Carrier signal $c(t) = 20 \cos (2\pi 10^6 t)$ is modulated by a message signal having three frequencies 5 KHz, 10 KHz & 20 KHz. The corresponding modulation indexes are 0.4, 0.5 & 0.6. Sketch the spectrum. Calculate bandwidth, power and efficiency.
<b>Assignment 2</b>	<b>Q1.</b> Derive the expression for probability of error in ASK, FSK and PSK systems and compare them.
	<b>Q2.</b> With block diagrams explain about DPCM & DM. also compare them.
	<b>Q3.</b> A message signal $m(t) = 4 \cos (2\pi 10^3 t)$ is sampled at nyquist rate and transmitted through a channel using 3-bit PCM system. i. Calculate all the parameters of the PCM. ii. If the sampled values are 3.8, 2.1, 0.5, -1.7, -3.2 & -4 then determine the quantizer output, encoder output and quantization error per each sample. iii. Sketch the transfer characteristics of the quantizer.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### 4EC4-21: Analog and Digital Communication Lab

Credit: 1.5

Max. Marks: 100(IA:60, ETE:40)

0L+0T+3P

List of Experiments	
Sr. No.	Name of Experiment
1.	Observe the Amplitude modulated wave form & measure modulation index and demodulation of AM signal.
2.	Harmonic analysis of Amplitude Modulated wave form.
3.	Generation & Demodulation of DSB – SC signal.
4.	Modulate a sinusoidal signal with high frequency carrier to obtain FM signal and demodulation of the FM signal.
5.	Verification of Sampling Theorem.
6.	To study & observe the operation of a super heterodyne receiver.
7.	PAM, PWM & PPM: Modulation and demodulation.
8.	To observe the transmission of four signals over a single channel using TDM-PAM method.
9.	To study the PCM modulation & demodulation and study the effect of channel like attenuation, noise in between modulator & demodulator through the experimental setup.
10.	To study the 4 channel PCM multiplexing & de-multiplexing in telephony system.
11.	To study the Delta & Adaptive delta modulation & demodulation and also study the effect of channel like attenuation, noise in between modulator & demodulator through the experimental setup.
12.	To perform the experiment of generation and study the various data formatting schemes (Unipolar, Bipolar, Manchester, AMI etc.)
13.	To perform the experiment of generation and detection of ASK, FSK, BPSK, DBPSK signals with variable length data pattern.

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-21	Analog and Digital Communication Lab	CO 1	Understand different analog modulation schemes and evaluate modulation index
		CO 2	Able to understand the principle of superhetrodyne receiver
		CO 3	Develop time division multiplexing concepts in real time applications
		CO 4	Develop and able to comprehend different data formatting schemes
		CO 5	Comprehend and analyze the concepts of different digital modulation techniques in communication.

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-21 Analog and Digital Communication Lab	CO 1	3	2		1								
	CO 2	3	2	1									
	CO 3	3	3	2	2	1							
	CO 4	3	3	2	2	1							
	CO 5	3	3	2	2	1							

3: Strongly

2: Moderate

1: Weak

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### 4EC4-22: Analog Circuits Lab

Credit: 1.5

Max. Marks: 100(IA:60, ETE:40)

0L+0T+3P

List of Experiments	
Sr. No.	Name of Experiment
1.	Study and implementation of Voltage Series and Current Series Negative Feedback Amplifier.
2.	Study and implementation of Voltage Shunt and Current Shunt Negative Feedback Amplifier.
3.	Plot frequency response of BJT amplifier with and without feedback in the emitter circuit and calculate bandwidth, gain bandwidth product with and without negative feedback.
4.	Study and implementation of series and shunt voltage regulators and calculate line regulation and ripple factor.
5.	Plot and study the characteristics of small signal amplifier using FET.
6.	Study and implementation of push pull amplifier. Measure variation of output power & distortion with load and calculate the efficiency.
7.	Study and implementation of Wein bridge oscillator and observe the effect of variation in oscillator frequency.
8.	Study and implementation of transistor phase shift oscillator and observe the effect of variation in R & C on oscillator frequency and compare with theoretical value.
9.	Study and implementation of the following oscillators and observe the effect of variation of capacitance on oscillator frequency: (a) Hartley (b) Colpitts.
10.	Study and implementation of the Inverting And Non-Inverting Operational Amplifier.
11.	Study and implementation of Summing, Scaling And Averaging of Operational Amplifier
12.	Implementation of active filters using OPAMP.

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-22	Analog Circuits Lab	CO 1	Discuss and observe the operation of a bipolar junction transistor and field-effect transistor in different region of operations.
		CO 2	Analyze and design of transistor Amplifier and Oscillators. Importance of negative feedback.
		CO 3	Analyze the frequency response of amplifiers and operational amplifier circuits. Develop an intuition for analog circuit behavior in both linear and nonlinear operation.
		CO 4	Design op-amps for specific gain, speed, or switching performance. Compensate operational amplifiers for stability.
		CO 5	Design and conduct experiments, interpret and analyze data, and report results.

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-22 Analog Circuits Lab	CO 1	3	2	1	2	2							
	CO 2	2	3	1	2	3							
	CO 3	1	3	2	3	2							
	CO 4	1	2	3	2	3							
	CO 5	1	2	3	3	3							

3: Strongly

2: Moderate

1: Weak

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### 4EC4-23: Microcontrollers Lab

Credit: 1.5

Max. Marks: 100(IA:60, ETE:40)

0L+0T+3P

List of Experiments	
Sr. No.	Name of Experiment
<b>Following exercises has to be Performed on 8085</b>	
1.	Write a program for 1.1 Multiplication of two 8 bit numbers 1.2 Division of two 8 bit numbers
2.	Write a program to arrange a set of data in Ascending and Descending order.
3.	Write a program to find Factorial of a given number.
4.	Write a program to generate a Software Delay. 4.1 Using a Register 4.2 Using a Register Pair
<b>8085 Interfacing Programs</b>	
5.	5.1 Write a program to Interface ADC with 8085. 5.2 Write a program to interface Temperature measurement module with 8085.
6.	Write a program to interface Keyboard with 8085.
7.	Write a program to interface DC Motor and stepper motor with 8085.
<b>Following exercises has to be Performed on 8051</b>	
8.	Write a program to convert a given Hex number to Decimal.
9.	Write a program to find numbers of even numbers and odd numbers among 10 Numbers.
10.	Write a program to find Largest and Smallest Numbers among 10 Numbers.
11.	11.1 To study how to generate delay with timer and loop. 11.2 Write a program to generate a signal on output pin using timer.
<b>8051 Interfacing Programs</b>	
12.	12.1 Write a program to interface Seven Segment Display with 8051. 12.2 Write a program to interface LCD with 8051.
13.	Write a program for Traffic light Control using 8051.
14.	Write a program for Elevator Control using 8051.

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-23	Microcontrollers Lab	CO 1	Develop skills related to assembly level programming of microprocessors and microcontroller.
		CO 2	Interpret the basic knowledge of microprocessor and microcontroller interfacing, delay generation, waveform generation and Interrupts.
		CO 3	Interfacing the external devices to the microcontroller and microprocessor to solve real time problems.
		CO 4	Illustrate functions of various general purpose interfacing devices.
		CO 5	Develop a simple microcontroller and microprocessor based systems

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-23 Microcontrollers Lab	CO 1	2	1	2	1	3							
	CO 2	3	2	1	2	1							
	CO 3	1	1	3	1	3							
	CO 4	2	2	1									
	CO 5	1	1	3	2	2		2					

3: Strongly

2: Moderate

1: Weak

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### 4EC4-24: Electronics Measurement & Instrumentation Lab

Credit: 1.5

Max. Marks: 100(IA:60, ETE:40)

0L+0T+3P

List of Experiments	
Sr. No.	Name of Experiment
1.	Measure earth resistance using fall of potential method.
2.	Plot V-I characteristics & measure open circuit voltage & short circuit current of a solar panel.
3.	Measure unknown inductance capacitance resistance using following bridges (a) Anderson Bridge (b) Maxwell Bridge
4.	To measure unknown frequency & capacitance using Wein's bridge.
5.	Measurement of the distance with the help of ultrasonic transmitter & receiver.
6.	Measurement of displacement with the help of LVDT.
7.	Draw the characteristics of the following temperature transducers (a) RTD (Pt-100) (b) Thermistors.
8.	Draw the characteristics between temperature & voltage of a K type thermocouple
9.	Calibrate an ammeter using D.C. slide wire potentiometer
10.	Measurement of strain/force with the help of strain gauge load cell.
11.	Study the working of Q-meter and measure Q of coils.
12.	Calibrate a single-phase energy meter (Analog and Digital) by phantom loading at different power factor by: (i) Phase shifting transformer (ii) Auto transformer.

### Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-24	Electronic Measurement & Instrumentation Lab	CO 1	Understanding of the fundamentals of Electronic Instrumentation. Explain and identify measuring instruments.
		CO 2	Able to measure resistance, inductance and capacitance by various methods.
		CO 3	Design an instrumentation system that meets desired specifications and requirements.
		CO 4	Design and conduct experiments, interpret and analyze data, and report results.
		CO 5	Explain the principle of electrical transducers. Confidence to apply instrumentation solutions for given industrial applications.

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

II Year - IV Semester: B.Tech. (Electronics & Communication Engineering)

### CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-24 Electronic Measurement & Instrumentation Lab	CO 1	3	2	1	2	2							
	CO 2	2	3	1	2	3							
	CO 3	1	3	2	3	2							
	CO 4	1	2	3	2	3							
	CO 5	1	2	3	3	3							

3: Strongly

2: Moderate

1: Weak

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**Teaching & Examination Scheme**  
**B.Tech. : Electronics & Communication**  
**Engineering 3<sup>rd</sup> Year –V Semester**

THEORY												
SN	Category	Course		Contact hrs/week			Marks			Cr		
		Code	Title	L	T	P	Exm Hrs	IA	ETE		Total	
1	ESC	5EC3-01	Computer Architecture	2	0	0	3	30	70	100	2	
2	PCC / PEC	5EC4-02	Electromagnetics Waves	3	0	0	3	30	70	100	3	
3		5EC4-03	Control system	3	0	0	3	30	70	100	3	
4		5EC4-04	Digital Signal Processing	3	0	0	3	30	70	100	3	
5		5EC4-05	Microwave Theory & Techniques	3	0	0	3	30	70	100	3	
6		Professional Elective I (any one)		2	0	0	3	30	70	100	2	
		5EC5-11	Bio-Medical Electronics									
		5EC5-12	Embedded Systems									
		5EC5-13	Probability Theory & Stochastic Process									
		5EC5-14	Satellite Communication									
			<b>Sub Total</b>		<b>16</b>	<b>0</b>	<b>0</b>		<b>210</b>	<b>490</b>	<b>700</b>	<b>16</b>
PRACTICAL & SESSIONAL												
7	PCC	5EC4-21	RF Simulation Lab	0	0	3	2	60	40	100	1.5	
8		5EC4-22	Digital Signal Processing Lab	0	0	3	2	60	40	100	1.5	
9		5EC4-23	Microwave Lab	0	0	2	2	60	40	100	1	
10	PSIT	5EC7-30	Industrial Training	0	0	1		60	40	100	2.5	
11	SOD E CA	5EC8-00	Social Outreach, Discipline & Extra Curricular Activities	0	0	0		60	40	100	0.5	
		Sub- Total		0	0	9		<b>300</b>	<b>200</b>	<b>500</b>	<b>7</b>	
		<b>TOTAL OF V SEMESTER</b>		<b>16</b>	<b>0</b>	<b>9</b>		<b>510</b>	<b>690</b>	<b>1200</b>	<b>23</b>	

**L: Lecture, T: Tutorial, P: Practical, Cr: Credits**

**ETE: End Term Exam, IA: Internal Assessment**

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Teaching & Examination Scheme B. Tech.: Electronics & Communication Engineering 3<sup>rd</sup> Year – VI Semester

THEORY											
SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
1	ESC	6EC 3-01	Power Electronics	2	0	0	3	30	70	100	2
2	PCC/ PEC	6EC 4-02	Computer Network	3	0	0	3	30	70	100	3
3		6EC 4-03	Fiber Optics Communications	3	0	0	3	30	70	100	3
4		6EC 4-04	Antennas and Propagation	3	0	0	3	30	70	100	3
5		6EC 4-05	5G Communication	3	0	0	3	30	70	100	3
6		Professional Elective II (any one)		3	0	0	3	30	70	100	3
			6EC 5-11	Introduction to MEMS							
		6EC 5-12	Nano Electronics								
		6EC 5-13	Neural Network And Fuzzy Logic Control								
		6EC 5-14	HighSpeed Electronics								
		<b>Sub Total</b>		<b>17</b>	<b>0</b>	<b>0</b>		<b>180</b>	<b>420</b>	<b>600</b>	<b>17</b>
PRACTICAL & SESSIONAL											
7	PCC	6EC 4-21	Computer Network Lab	0	0	4	2	60	40	100	2
8		6EC 4-22	Antenna and wave propagation Lab	0	0	2	2	60	40	100	1
9		6EC 4-23	Electronics Design Lab	0	0	4	2	60	40	100	2
10		6EC 4-24	Power Electronics Lab	0	0	2	2	60	40	100	1
11	SODE CA	6EC 8-00	Social Outreach, Discipline & Extra Curricular Activities	0	0	0		60	40	100	0.5
		Sub- Total		0	0	12		<b>300</b>	<b>200</b>	<b>500</b>	<b>6.5</b>
		<b>TOTAL OF VI SEMESTER</b>		<b>17</b>	<b>0</b>	<b>12</b>		<b>480</b>	<b>620</b>	<b>1100</b>	<b>23.5</b>

**L: Lecture, T: Tutorial, P: Practical, Cr: Credits**

**ETE: End Term Exam, IA: Internal Assessment**

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC3-01: Computer Architecture

**Credit:2**

**2L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Basic Structure of Computers, Functional units, software, performance issues software, machineinstructions and programs, Types of instructions, Instruction sets: Instruction formats,Assembly language, Stacks, Ques, Subroutines.	6
3	Processor organization, Information representation, number formats. Multiplication & division, ALU design, Floating Point arithmetic, IEEE 754 floating pointformats.	5
4	Control Design, Instruction sequencing, Interpretation, Hard wired controlDesignmethods, and CPU control unit. Microprogrammed Control - Basic concepts, minimizing microinstruction size, multiplier control unit. Microprogrammed computers - CPU control unit.	6
5	Memory organizations, device characteristics, RAM, ROM, Memory management, Concept ofCache & associative memories, Virtual memory.	5
6	System organization, Input - Output systems, Interrupt, DMA, Standard I/O interfacesConcept of parallel processing, Pipelining, Forms of parallel processing, interconnect network.	5
	<b>Total</b>	<b>28</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC4-02: Electromagnetics Waves

Credit:3

Max. Marks: 100(IA:30,ETE:70)

3L+0T+0P

End Term Exam: 3Hours

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Transmission Lines-Equations of Voltage and Current on TXline, Propagation constant and characteristic impedance, and reflection coefficient and VSWR, Impedance Transformation on Loss-less and Low loss Transmission line, Power transfer on TXline, Smith Chart, Admittance Smith Chart, Applications of transmission lines: Impedance Matching, use transmission line sections as circuit elements.	08
3	Maxwell's Equations-Basics of Vectors, Vector calculus, Basic laws of Electromagnetics, Maxwell's Equations, Boundary conditions at Media Interface.	03
4	Uniform Plane Wave-Uniform plane wave, Propagation of wave, Wave polarization, Poincare's Sphere, Wave propagation in conducting medium, phase and group velocity, Power flow and Poynting vector, Surface current and power loss in a conductor.	08
5	Plane Waves at a Media Interface-Plane wave in arbitrary direction, Reflection and refraction at dielectric interface, Total internal reflection, wave polarization at media interface, Reflection from a conducting boundary.	07
6	Waveguides- Wave propagation in parallel plate waveguide, Analysis of waveguide general approach, Rectangular waveguide, Modal propagation in rectangular waveguide, Surface currents on the waveguide walls, Field visualization, Attenuation in waveguide.	08
7	Radiation-Solution for potential function, Radiation from the Hertz dipole, Power radiated by hertz dipole, Radiation Parameters of antenna, receiving antenna, Monopole and Dipole antenna.	07
	<b>Total</b>	<b>42</b>





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC4-03: Control system

**Credit:3**

**3L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Introduction to control problem- Industrial Control examples. Transfer function. System with dead-time. System response. Control hardware and their models: potentiometers, synchros, LVDT, dc and ac servomotors, tacho-generators, electro hydraulic valves, hydraulic servomotors, electro pneumatic valves, pneumatic actuators. Closed-loop systems. Block diagram and signal flow graph analysis.	8
3	Feedback control systems- Stability, steady-state accuracy, transient accuracy, disturbance rejection, insensitivity and robustness. proportional, integral and derivative systems. Feedforward and multi-loop control configurations, stability concept, relative stability, Routh stability criterion.	7
4	Time response of second-order systems- steady-state errors and error constants. Performance specifications in time-domain. Root locus method of design. Lead and lag compensation.	6
5	Frequency-response analysis- Polar plots, Bode plot, stability in frequency domain, Nyquist plots. Nyquist stability criterion. Performance specifications in frequency-domain. Frequency domain methods of design, Compensation & their realization in time & frequency domain. Lead and Lag compensation. Op-amp based and digital implementation of compensators. Tuning of process controllers. State variable formulation and solution.	8
6	State variable Analysis- Concepts of state, state variable, state model, state models for linear continuous time functions, diagonalization of transfer function, solution of state equations, concept of controllability & observability.	6
7	Introduction to Optimal control & Nonlinear control, Optimal Control problem, Regulator problem, Output regulator, tracking problem. Nonlinear system – Basic concept & analysis.	6
	<b>Total</b>	<b>42</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC4-04: Digital Signal Processing

**Credit:3**

**3L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Discrete time signals: Sequences; representation of signals on orthogonal basis; Sampling and reconstruction of signals; Discrete systems attributes, Z-Transform, Analysis of LSI systems, frequency Analysis, Inverse Systems.	10
3	Discrete Fourier Transform (DFT), Fast Fourier Transform Algorithm, Implementation of Discrete Time Systems.	9
4	Design of FIR Digital filters: Window method, Park-McClellan's method. Design of IIR Digital Filters: Butterworth, Chebyshev and Elliptic Approximations; Lowpass, Bandpass, Bandstop and High pass filters.	10
5	Effect of finite register length in FIR filter design. Parametric and non-parametric spectral estimation. Introduction to multirate signal processing. Application of DSP.	10
	<b>Total</b>	<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC4-05: Microwave Theory & Techniques

Credit:3

3L+0T+0P

Max. Marks: 100(IA:30,ETE:70)

End Term Exam: 3Hours

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Introduction to Microwaves-History of Microwaves, Microwave Frequency bands; Applications of Microwaves: Civil and Military, Medical, EMI/EMC.	4
3	Mathematical Model of Microwave Transmission-Concept of Mode, Features of TEM, TE and TM Modes, Losses associated with microwave transmission, Concept of Impedance in Microwave transmission.	5
4	Analysis of RF and Microwave Transmission Lines-Coaxial line, Rectangular waveguide, Circular waveguide, Strip line, Micro strip line.	4
5	Microwave Network Analysis-Equivalent voltages and currents for non-TEM lines, Network parameters for microwave circuits, Scattering Parameters.	4
6	Passive and Active Microwave Devices-Microwave passive components: Directional Coupler, Power Divider, Magic Tee, Attenuator, Resonator. Microwave active components: Diodes, Transistors, Oscillators, Mixers. Microwave Semiconductor Devices: Gunn Diodes, IMPATT diodes, Schottky Barrier diodes, PIN diodes. Microwave Tubes: Klystron, TWT, Magnetron.	6
7	Microwave Design Principles-Impedance transformation, Impedance Matching, Microwave Filter Design, RF and Microwave Amplifier Design, Microwave Power Amplifier Design, Low Noise Amplifier Design, Microwave Mixer Design, Microwave Oscillator Design. Microwave Antennas- Antenna parameters, Antenna for ground based systems, Antennas for airborne and satellite borne systems, Planar Antennas.	6
8	Microwave Measurements-Power, Frequency and impedance measurement at microwave frequency, Network Analyzer and measurement of scattering parameters, Spectrum Analyzer and measurement of spectrum of a microwave signal, Noise at microwave frequency and measurement of noise figure. Measurement of Microwave antenna parameters.	6
9	Microwave Systems-Radar, Terrestrial and Satellite Communication, Radio Aidsto Navigation, RFID, GPS. Modern Trends in Microwaves Engineering- Effect of Microwaves on human body, Medical and Civil applications of microwaves, Electromagnetic interference and Electromagnetic Compatibility (EMI & EMC), Monolithic Microwave ICs, RFMEMS for microwave components, Microwave Imaging.	6
	<b>Total</b>	<b>42</b>

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# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC5-11: Bio-Medical Electronics

**Credit:2**

**2L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Brief introduction to human physiology. Biomedical transducers: displacement, velocity, force, acceleration, flow, temperature, potential, dissolved ions and gases.	9
3	Bio-electrodes and biopotential amplifiers for ECG, EMG, EEG, etc.	7
4	Measurement of blood temperature, pressure and flow. Impedance plethysmography. Ultrasonic, X-ray and nuclear imaging. Prostheses and aids: pacemakers, defibrillators, heart-lung machine, artificial kidney, aids for the handicapped. Safety aspects.	11
	<b>Total</b>	<b>28</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC5-12: Embedded Systems

**Credit:2**

**2L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	The concept of embedded systems design, Embedded microcontroller cores, embedded memories.	5
3	Examples of embedded systems, Technological aspects of embedded systems: interfacing between analog and digital blocks, signal conditioning, digital signal processing. Sub system interfacing, interfacing with external systems, user interfacing.	10
4	Design tradeoffs due to process compatibility, thermal considerations, etc., Software aspects of embedded systems: real time programming languages and operating systems for embedded systems.	12
	<b>Total</b>	<b>28</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC5-13: Probability Theory & Stochastic Process

**Credit:2**

**2L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Sets and set operations; Probability space; Conditional probability and Bayes theorem; Combinatorial probability and sampling models.	5
3	Discrete random variables, probability mass function, probability distribution function, example random variables and distributions; Continuous random variables, probability density function, probability distribution function, example distributions;	6
4	Joint distributions, functions of one and two random variables, moments of random variables; Conditional distribution, densities and moments; Characteristic functions of a random variable; Markov, Chebyshev and Chernoff bounds;	6
5	Random sequences and modes of convergence (everywhere, almost everywhere, probability, distribution and mean square); Limit theorems; Strong and weak laws of large numbers, central limit theorem	5
6	Random process. Stationary processes. Mean and covariance functions. Ergodicity. Transmission of random process through LTI. Power spectral density.	4
	<b>Total</b>	<b>27</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC5-14: Satellite Communication

**Credit:2**

**2L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Introduction to Satellite Communication: Principles and architecture of satellite Communication, Brief history of Satellite systems, advantages, disadvantages, applications and frequency bands used for satellite communication.	4
3	Orbital Mechanics: Orbital equations, Kepler's laws, Apogee and Perigee for an elliptical orbit, evaluation of velocity, orbital period, angular velocity etc. of a satellite, concepts of Solar day and Sidereal day.	4
4	Satellite sub-systems: Study of Architecture and Roles of various sub-systems of a satellite system such as Telemetry, tracking, command and monitoring (TTC & M), Attitude and orbit control system (AOCS), Communication sub-system, power sub-systems etc.	5
5	Typical Phenomena in Satellite Communication: Solar Eclipse on satellite, its effects, remedies for Eclipse, Sun Transit Outage phenomena, its effects and remedies, Doppler frequency shift phenomena and expression for Doppler shift. Satellite link budget	5
6	Flux density and received signal power equations, Calculation of System noise temperature for satellite receiver, noise power calculation, Drafting of satellite link budget and C/N ratio calculations in clear air and rainy conditions.	4
7	Modulation and Multiple Access Schemes: Various modulation schemes used in satellite communication, Meaning of Multiple Access, Multiple access schemes based on time, frequency, and code sharing namely TDMA, FDMA and CDMA.	4
	<b>Total</b>	<b>27</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC4-21: RF Simulation Lab

**Credit:1.5**

**Max. Marks: 100(IA:60,ETE:40)**

**OL+OT+3P**

**End Term Exam: 2Hours**

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
2	Study of field pattern of various modes inside a rectangular and circular waveguide.
3	Find the change in characteristics impedance and reflection coefficients of the transmission line by changing the dielectric properties of materials embedded between two conductors.
4	Design and simulate the following Planar Transmission Lines: I. Strip and micro-striplines II. Parallel coupled stripline III. Coplanar and Slotlines Determine their field patterns and characteristic impedance.
5	Design and simulate the following: I. 3-dB branch line coupler II. Wilkinson powerdivider III. Hybridring IV. Backward wavecoupler V. Low passfilters VI. Band passfilters
6	Design RF amplifier using microwave BJT.
7	Design RF amplifier using microwave FET.





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC4-22: Digital Signal Processing Lab

Credit:1.5

Max. Marks: 100(IA:60,ETE:40)

OL+OT+3P

End Term Exam: 2Hours

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
2	Generation of continuous and discrete elementary signals (impulse, unit-step, ramp) using mathematical expression.
3	Perform basic operations on signals like adding, subtracting, shifting and scaling.
4	Perform continuous and discrete time Convolution (using basic definition).
5	Checking Linearity and Time variance property of a system using convolution, shifting.
6	To generate and verify random sequences with arbitrary distributions, means and variances for following: (a) Rayleigh distribution (b) Normal distributions: $N(0, 1)$ . (c) Gaussian distributions: $N(m, x)$ (d) Random binary wave.
7	To find DFT / IDFT of given DT signal.
8	N-point FFT algorithm.
9	To implement Circular convolution.
10	MATLAB code for implementing z-transform and inverse z-transform.
11	Perform inverse z-transform using residuez MATLAB function.
12	MATLAB program to find frequency response of analog LP/HP filters.
13	To design FIR filter (LP/HP) using windowing (rectangular, triangular, Kaiser) technique using simulink.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - V Semester: B.Tech. (Electronics & Communication Engineering)

### 5EC4-23: Microwave Lab

**Credit:1**  
**OL+OT+2P**

**Max. Marks: 100(IA:60,ETE:40)**

**End Term Exam: 2Hours**

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
2	Study of various microwave components and instruments like frequency meter, attenuator, detector and VSWRmeter.  (a) Measurement of guide wavelength and frequency using a X-band slotted line setup. (b) Measurement of low and high VSWR using a X-band slotted linesetup.
3	Introduction to Smith chart, measurement of SWR, shift in minimum standing wave with unknown load and calculation of unknown load impedance using Smith chart.
4	Study the behavior of terminated coaxial transmission lines in time and frequency domain.
5	(a) Draw the V-I characteristics of a Gunn diode and determine the output power and frequency as a function of voltage. (b) Study the square wave modulation of microwave signal using PINdiode.
6	Study the square wave modulation of microwave signal using PIN diode. Study and measure the power division and isolation characteristics of a microstrip 3dB power divider.
7	Study of rat race hybrid ring (equivalent of waveguide Magic-Tee ) in micro-strip.
8	(a) To study the characteristics of micro-strip 3dB branch line coupler, strip line backward wave coupler as a function of frequency and compare their bandwidth. (b) Measure the microwave input, direct, coupled and isolated powers of a backward wave strip line coupler at the centre frequency using a power meter. From the measurements calculate the coupling, isolation and directivity of the coupler.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC3-01: Power Electronics

**Credit:2**

**2L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	SEMICONDUCTOR POWER DEVICES: Introduction. Basic characteristics & working of Power Diodes, Diac, Triac, MOSFETs, IGBT, GTO, Power Transistor and SCR- Principle of operation, V-I Characteristics, Turn-On mechanism and its applications.	6
3	CONVERTERS: Basic concept, Working Principles of Single phase half Wave bridge converter, Single Phase Full Bridge Converter, 3 Phase Bridge Converter.	5
4	INVERTERS: Voltage Source Inverter, Current Source Inverter, PWM Control of Voltage Source Converter and applications.	5
5	INDUSTRIAL POWER SUPPLIES: Principle of operation of choppers. Step up, Step down and reversible choppers. Chopper control techniques, High frequency electronic ballast, Switch Mode Power Supply: Fly back converter, forward/buck converter, Boost converter and buck-boost converter. Uninterruptible Power Supply.	6
6	MOTOR CONTROL: Introduction to speed control of DC motors using phase controlled converters and choppers, Basic idea of speed control of three phase induction motors using voltage and frequency control methods.	5
	<b>Total</b>	<b>28</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC4-02: Computer Network

**Credit:3**

**3L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Queuing Theory- Pure birth, Pure death & Birth-death processes, Mathematical models for M/M/1, M/M/∞, M/M/m, M/M/1/Kand M/M/m/m queues. Little's formula.	7
3	Introduction to computer networks and the Internet: Application layer: Principles of network applications, The Web and Hyper Text Transfer Protocol, File transfer, Electronic mail, Domain name system, Peer-to-Peer file sharing, Socket programming, Layering concepts. Packet switching, Blocking in packet switches, Three generations of packet switches, switch fabric, Buffering, Multicasting, Statistical Multiplexing.	9
4	Transport layer: Connectionless transport - User Datagram Protocol, Connection oriented transport - Transmission Control Protocol, Remote Procedure Call. Congestion Control and Resource Allocation: Issues in Resource Allocation, Queuing Disciplines, TCP congestion Control, Congestion Avoidance Mechanisms and Quality of Service.	9
5	Network layer: Virtual circuit and Datagram networks, Router, Internet Protocol, Routing algorithms, Broadcast and Multicast routing.	7
6	Link layer: ALOHA, Multiple access protocols, IEEE 802 standards, Local Area Networks, addressing, Ethernet, Hubs, Switches. Fundamental of SDN, Open flow.	7
	<b>Total</b>	<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC4-03: Fiber Optics Communications

**Credit:3**

**3L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Introduction to vector nature of light, propagation of light, propagation of light in a cylindrical dielectric rod, Ray model, wave model. Different types of optical fibers, Modal analysis of a step index fiber.	8
3	Signal degradation on optical fiber due to dispersion and attenuation. Fabrication of fibers and measurement techniques like OTDR	7
4	Optical sources - LEDs and Lasers, Photo-detectors - pin-diodes, APDs, detector responsivity, noise, optical receivers. Optical link design - BER calculation, quantum limit, power penalties.	8
5	Optical switches - coupled mode analysis of directional couplers, electro-optic switches. Optical amplifiers - EDFA, Raman amplifier.	8
6	WDM and DWDM systems. Principles of WDM networks. Nonlinear effects in fiber optic links. Concept of self-phase modulation, group velocity dispersion and soliton based communication.	8
	<b>Total</b>	<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC4-04: Antennas and Propagation

**Credit:3**

**3L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Fundamental Concepts-Physical concept of radiation, Radiation pattern, near and far-field regions, reciprocity, directivity and gain, effective aperture, polarization, input impedance, efficiency, Friis transmission equation, radiation integrals and auxiliary potential functions.	7
3	Radiation from Wires and Loops-Infinitesimal dipole, finite-length dipole, linear elements near conductors, dipoles for mobile communication, small circular loop.	6
4	Aperture and Reflector Antennas-Huygens' principle, radiation from rectangular and circular apertures, design considerations, Babinet's principle, Radiation from sectoral and pyramidal horns, design concepts, prime-focus parabolic reflector and cassegrain antennas.	7
5	Broadband Antennas-Log-periodic and Yagi-Uda antennas, frequency independent antennas, broadcast antennas.	5
6	Micro strip Antennas-Basic characteristics of micro strip antennas, feeding methods, methods of analysis, design of rectangular and circular patch antennas.	6
7	Antenna Arrays-Analysis of uniformly spaced arrays with uniform and non-uniform excitation amplitudes, extension to planar arrays, synthesis of antenna arrays using Schelkunoff polynomial method, Woodward-Lawson method.	5
8	Basic Concepts of Smart Antennas-Concept and benefits of smart antennas, fixed weight beamforming basics, Adaptive beam forming.	4
9	Different modes of Radio Wave propagation used in current practice.	1
	<b>Total</b>	<b>42</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC4-05: 5G Communication

**Credit:3**  
**3L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**  
**End Term Exam: 3Hours**

Unit	Topic	
1	<b>Introduction:</b> Introduction of 3G and 4G (LTE, LTEA, LTEA Pro), 5G overview , requirements, Spectrum access modes and Sharing for 5G . <b>Channel Modeling :</b> Channel modeling requirements, propagation scenarios and challenges in the 5G modeling	4
2	<b>System Architecture:</b> 5G core network architecture, Radio Accesses Network (RAN) architectures, Interference management, mobility management and handover in 5G. <b>Physical Layer and Deployment:</b> 5G Physical channels , signals and frame structure ; Small cell deployments: different types, Deployment scenarios, performance and analysis, 3GPP RAN standards for small cell	8
3	<b>Modulation and Accesses Techniques :</b> Orthogonal frequency division multiplexing (OFDM), filter bank multi-carriers (FBMC) , orthogonal frequency division multiple accesses (OFDMA), non-orthogonal multiple accesses (NOMA)	5
4	<b>Device-to-device (D2D) and machine-to-machine (M2M) type communications:</b> Extension of 4G D2D standardization to 5G, radio resource management for mobile broadband D2D, multi-hop and multi-operator D2D communications	5
5	<b>Millimeter-wave Communications:</b> Millimeter bands, radio-wave propagation, Physical layer design, beam-forming, interference and mobility management ; Massive MIMO(Sub 6Ghz) -mm wave MIMO (above 6GHz), Smart Antennas for 5G	8
6	<b>5G Network Slicing:</b> Introduction of Network Slicing, E2E Slicing, SDN and NFV Slicing <b>Vehicular Communication:</b> From V2V to AV2X, key standards, VC architectures basics	6
Total Lectures		40

#### Text books

1. Martin Sauter, From GSM to LTE—Advanced Pro and 5G: An Introduction to Mobile Networks and Mobile Broadband, Wiley-Blackwell
2. Afif Osseiran, Jose.F.Monserrat, Patrick Marsch, Fundamentals of 5G Mobile Networks , Cambridge University Press
3. Athanasios G.Kanatos, Konstantina S.Nikita, Panagiotis Mathiopoulos, New Directions in Wireless Communication Systems from Mobile to 5G, CRC Press
4. Theodore S.Rappaport, Robert W.Heath, Robert C.Daniels, James N.Murdock, Millimeter Wave Wireless Communications, Prentice Hall Communications

#### Reference Books

1. Jonathan Rodriguez, "Fundamentals of 5G Mobile Networks", John Wiley & Sons
2. Alagan Anpalagan, Mehdi Bennis, Rath Vannithamby, Design and deployment of small cell networks, Cambridge university press, 2015
3. M. Vaezi, Z. Ding, and H. V. Poor, Multiple Access techniques for 5G Wireless Networks and Beyond., Springer Nature, Switzerland, 2019
4. Principles of Modern Wireless communication systems by Aditya k Jagannathan
5. Manish, M., Devendra, G., Pattanayak, P., Ha, N., 5G and Beyond Wireless Systems PHY Layer Perspective, Series in Wireless Technology Springer, 2021
6. Erik Dahlman, Stefan and Parkvall, Johan Skoid, 5G NR: The Next Generation Wireless Access Technology, Elsevier, First Edition, 2016
7. Harri Holma, Antti Toskala, Takehiro Nakamura, "5G Technology 3GPP NEW RADIO", John Wiley & Sons First Edition,2020



# **RAJASTHAN TECHNICAL UNIVERSITY, KOTA**

## **SYLLABUS**

**III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)**





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC5-11: Introduction to MEMS

**Credit:3**

**3L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Introduction and Historical Background.	1
3	Mechanics of solids in MEMS/NEMS: Stresses, Strain, Hookes's law, Poisson effect, Linear Thermal Expansion, Bending; Energy methods, Overview of Finite Element Method, Modeling of Coupled Electromechanical Systems.	14
4	Scaling Effects. Micro/Nano Sensors, Actuators and Systems overview: Case studies. Review of Basic MEMS fabrication modules: Oxidation, Deposition Techniques, Lithography (LIGA), and Etching.	14
5	Micromachining: Surface Micromachining, sacrificial layer processes, Stiction; Bulk Micromachining, Isotropic Etching and Anisotropic Etching, Wafer Bonding.	10
	<b>Total</b>	<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC5-12: Nano Electronics

**Credit:3**

**3L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Introduction to nanotechnology, meso structures, Basics of Quantum Mechanics: Schrodinger equation, Density of States. Particle in a box Concepts, Degeneracy. Band Theory of Solids. Kronig-Penny Model. Brillouin Zones.	15
3	Shrink-down approaches: Introduction, CMOS Scaling, The nano scale MOSFET, Finfets, Vertical MOSFETs, limits to scaling, system integration limits (interconnect issuesetc.).	10
4	Resonant Tunneling Diode, Coulomb dots, Quantum blockade, Single electron transistors, Carbon nanotube electronics, Band structure and transport, devices, applications, 2D semiconductors and electronic devices, Graphene, atomistic simulation.	14
	<b>Total</b>	<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC5-13: Neural Network And Fuzzy Logic Control

**Credit:3**  
**3L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	<b>NEUROPHYSIOLOGY:</b> Introduction: Elementary neurophysiology – From neurons to ANNs - Neuron model McCulloch-Pitts model, Hebbian Hypothesis; limitations of single-layered neural networks. Applications Of Neural Networks: Pattern classification, Associative memories, Optimization, Applications in Image Processing-Iris, fingerprint & face, Applications in decision making.	8
3	<b>THE PERCEPTRON:</b> The Perceptron and its learning law. Classification of linearly separable patterns. Linear Networks: Adaline - the adaptive linear element. Linear regression. The Wiener-Hopf equation. The Least-Mean-Square (Widrow-Hoff) learning algorithm. Method of steepest descent. Adaline as a linear adaptive filter. A sequential regression algorithm. Multi-Layer Feed forward Neural Networks: Multi-Layer Perceptrons. Supervised Learning. Approximation and interpolation of functions. Back-Propagation Learning law. Fast training algorithms. Applications of multilayer perceptrons: Image coding, Paint-quality inspection, Nettetalk.	9
4	<b>FUZZY LOGIC:</b> Introduction -Uncertainty & precision, Statistics and random process, Uncertainty in information, Fuzzy sets and membership. Membership Functions: Features of membership function. Standard forms and boundaries, Fuzzification, Membership value assignment – Intuition, Inference, Neural networks. Fuzzy To Crisp Conversions: Maximum membership principle.	7
5	<b>DEFUZZIFICATION METHODS-</b> Centroid method, Weighted average method, Meanmax membership. Fuzzy Rule Based Systems: Natural language, linguistic hedges, Rule based system –Canonical rule forms, Decomposition of compound rules, Likelihood and truth qualification Aggregation of Fuzzy rules. Graphical techniques of reference.	8
6	<b>FUZZY CONTROL SYSTEM-</b> Simple Fuzzy Logic controller, General FLC, Control System Design Problem Control (Decision) Surface, Assumptions in a Fuzzy Control System Design, Special forms of FLC system models, Industrial application: Aircraft Landing Control Problem. Fuzzy Engineering Process Control: Classical Feedback Control, Classical PID Control, Multi-input, Multi-output (MIMO) Control Systems, Fuzzy Statistical Process Control.	9
	<b>Total</b>	<b>42</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC5-14: High Speed Electronics

**Credit:3**

**3L+0T+0P**

**Max. Marks: 100(IA:30,ETE:70)**

**End Term Exam: 3Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Transmission line theory (basics) crosstalk and nonideal effects; signal integrity: impact of packages, vias, traces, connectors; non-ideal return current paths, high frequency powerdelivery, methodologies for design of high speed buses; radiated emissions and minimizing system noise; Noise Analysis: Sources, Noise Figure, Gain compression, Harmonic distortion, Intermodulation, Cross-modulation, Dynamic range.	10
3	Devices: Passive and active, Lumped passive devices (models), Active (models, low vs High frequency)	6
4	RF Amplifier Design, Stability, Low Noise Amplifiers, Broadband Amplifiers (and Distributed)Power Amplifiers, Class A, B, AB and C, D E Integrated circuit realizations, Cross-over distortion Efficiency RF power output stages.	8
5	Mixers –Up conversion Down conversion, Conversion gain and spurious response. OscillatorsPrinciples.PLL Transceiver architectures.	8
6	Printed Circuit Board Anatomy, CAD tools for PCB design, Standard fabrication, Micro via Boards. Board Assembly: Surface Mount Technology, Through Hole Technology, Process Control and Design challenges.	8
	<b>Total</b>	<b>41</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC4-21: Computer Network Lab

Credit:2

Max. Marks: 100(IA:60,ETE:40)

OL+OT+4P

End Term Exam: 2Hours

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
2	PRELIMINARIES: Study and use of common TCP/IP protocols and term viz. telnet rlogin ftp, ping, finger, Socket, Port etc.
3	DATA STRUCTURES USED IN NETWORK PROGRAMMING: Representation of unidirectional, Directional weighted and unweighted graphs.
4	ALGORITHMS IN NETWORKS: computation of shortest path for one source-one destination and one source -all destination
5	SIMULATION OF NETWORK PROTOCOLS: i. Simulation of M/M/1 and M/M/1/Nqueues. ii. Simulation of pure and slottedALOHA. iii. Simulation of link state routingalgorithm.
6	Case study : on LAN Training kit i. Observe the behaviour & measure the throughput of reliable data transfer protocols under various Bit error rates for following DLL layerprotocols- a. Stop &Wait b. Sliding Window : Go-Back-N and SelectiveRepeat ii. Observe the behaviour & measure the throughput under various network load conditions for following MAC layerProtocols a. Aloha b. CSMA, CSMA/CD &CSMA/CA c. Token Bus & TokenRing
7	Software and hardware realization of the following: i. Encoding schemes: Manchester,NRZ. ii. Error control schemes: CRC, Hammingcode.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC4-22: Antenna and Wave Propagation Lab

Credit:1  
OL+OT+2P

Max. Marks: 100(IA:60,ETE:40)

End Term Exam: 2Hours

SN	Contents
<b>PART-I (Antenna)</b>	
1	Study the gain pattern, HPBW, FNBW and Directivity of a dipole antenna.
2	Measurement of Radiation Pattern, Gain, HPBW of a folded dipole antenna.
3	Measurement of Radiation Pattern, Gain, HPBW of a loop antenna
4	Measurement of Radiation Pattern, Gain, VSWR, input impedance and reflection coefficient for given Monopole antenna
5	Measurement of Radiation Pattern, Gain, VSWR, input impedance and reflection coefficient for given Yagiantennas
6	Study of the Radiation Pattern, Gain, HPBW of a horn antenna
7	Study of the Radiation Pattern, Gain, HPBW of a reflector antennas
8	Study the radiation pattern, gain, VSWR, and input impedance of a rectangular microstrip patch antenna
9	Study the effect of inset feed on the input impedance of a rectangular patch antenna
10	Study the effect of ground plane on the radiation pattern of an antenna
11	Study antenna designing in CST Microwave Studio
12	Design a rectangular micro strip patch antenna using CST MWS
<b>PART-II (Optical Fiber)</b>	
To perform following experiments based on Fiber Optic Trainer.	
13	To set up Fiber Optic Analog link and Digital link.
14	Measurement of Propagation loss and numerical aperture.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC4-23: Electronics Design Lab

Credit:2

Max. Marks: 100(IA:60,ETE:40)

OL+OT+4P

End Term Exam: 2Hours

SN	Contents
	<b>To design the following circuits, assemble these on bread board and test them and Simulation of these circuits with the help of appropriate software.</b>
1	Op-Amp characteristics and get data for input bias current measure the output-offset voltage and reduce it to zero and calculate slew rate.
2	Op-Amp in inverting and non-inverting modes.
3	Op-Amp as scalar, summer and voltage follower.
4	Op-Amp as differentiator and integrator.
5	Design LPF and HPF using Op-Amp 741
6	Design Band Pass and Band reject Active filters using Op-Amp 741.
7	Design Oscillators using Op-Amp (i) RC phase shift (ii) Hartley (iii) Colpitts
8	Design (i) Astable (ii) Monostable multivibrators using IC-555 timer
9	Design Triangular & square wave generator using 555 timer.
10	Design Amplifier (for given gain) using Bipolar Junction Transistor.
11	Op-Amp characteristics and get data for input bias current measure the output-offset voltage and reduce it to zero and calculate slew rate.
12	Op-Amp in inverting and non-inverting modes.
13	Op-Amp as scalar, summer and voltage follower.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

III Year - VI Semester: B.Tech. (Electronics & Communication Engineering)

### 6EC4-24: Power Electronics Lab

Credit:1

Max. Marks: 100(IA:60,ETE:40)

OL+OT+2P

End Term Exam: 2Hours

SN	Contents
1	Study the characteristics of SCR and observe the terminal configuration, Measure the breakdown voltage, latching and holding current. Plot V-I characteristics.
2	Perform experiment on triggering circuits for SCR. i.e. R triggering, R-C triggering and UJT triggering circuit.
3	Study and test AC voltage regulators using triac, anti parallel thyristors and triac&diac.
4	Study and obtain the waveforms for single-phase bridge converter.
5	Perform experiment on single phase PWM inverter.
6	Perform experiment on buck, boost and buck-boost regulators.
7	Control speed of a dc motor using a chopper and plot armature voltage versus speed characteristic.
8	Control speed of a single-phase induction motor using single phase AC voltage regulator.
9	I. Study single-phase dualconverter. II. Study speed control of dc motor using single-phase dualconverter.
10	Study single-phase cyclo converter.
11	Perform experiment on Motor control – open loop & closed loop
12	Design, observe and perform experiment on various type of pulse generation from DSP/ FPGA Platform. Perform experiment for PWM inverters and choppers.





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## Teaching & Examination Scheme B.Tech. : Electronics & Communication Engineering 4<sup>th</sup> Year - VII Semester

THEORY											
SN	Category	Course		Contact hrs/week			Marks			Cr	
		Code	Title	L	T	P	Exm Hrs	IA	ETE		Total
1	PEC	Program Elective		3	0	0	3	30	70	100	3
		7EC5-11	VLSI Design								
		7EC5-12	Mixed Signal Design								
		7EC5-13	CMOS design								
2	OE		Open Elective-I	3	0	0	3	30	70	100	3
			<b>Sub Total</b>	<b>6</b>	<b>0</b>	<b>0</b>		<b>60</b>	<b>140</b>	<b>200</b>	<b>6</b>
PRACTICAL & SESSIONAL											
3	PCC	7EC4-21	VLSI Design Lab	0	0	4	2	60	40	100	2
4		7EC4-22	Advance communication lab (MATLAB Simulation)	0	0	2	2	60	40	100	1
5		7EC4-23	Optical Communication Lab	0	0	2	2	60	40	100	1
6	PSIT	7EC7-30	Industrial Training	1	0	0		60	40	100	2.5
7		7EC7-40	Seminar	2	0	0		60	40	100	2
8	SODECA	7EC8-00	Social Outreach, Discipline & Extra Curricular Activities					60	40	100	0.5
			<b>Sub Total</b>	<b>3</b>	<b>0</b>	<b>8</b>		<b>360</b>	<b>240</b>	<b>600</b>	<b>9</b>
			<b>TOTAL of VII SEMESTER</b>	<b>9</b>	<b>0</b>	<b>8</b>		<b>420</b>	<b>380</b>	<b>800</b>	<b>15</b>

**L:** Lecture, **T:** Tutorial, **P:** Practical, **Cr:** Credits

**ETE:** End Term Exam, **IA:** Internal Assessment



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## Teaching & Examination Scheme B.Tech. : Electronics & Communication Engineering 4<sup>th</sup> Year - VIII Semester

THEORY											
SN	Category	Course Code	Course Title	Contact hrs/week			Marks				Cr
				L	T	P	Exm Hrs	IA	ETE	Total	
1	PEC	Program Elective		3	0	0	3	30	70	100	3
		8EC5-11	Artificial Intelligence And Expert Systems								
		8EC5-12	Digital Image and Video Processing								
		8EC5-13	Adaptive Signal Processing								
2	OE		Open Elective-II	3	0	0	3	30	70	100	3
<b>Sub Total</b>				<b>6</b>	<b>0</b>	<b>0</b>		<b>60</b>	<b>140</b>	<b>200</b>	<b>6</b>
PRACTICAL & SESSIONAL											
3	PCC	8EC4-21	Internet of Things (IOT) Lab	0	0	2	2	60	40	100	1
4		8EC4-22	Skill Development Lab	0	0	2	2	60	40	100	1
5	PSIT	8EC7-50	Project	3	0	0		60	40	100	7
6	SODECA	8EC8-00	Social Outreach, Discipline & Extra Curricular Activities					60	40	100	0.5
<b>Sub Total</b>				<b>3</b>	<b>0</b>	<b>4</b>		<b>240</b>	<b>160</b>	<b>400</b>	<b>9.5</b>
<b>TOTAL of VIII SEMESTER</b>				<b>9</b>	<b>0</b>	<b>4</b>		<b>300</b>	<b>300</b>	<b>600</b>	<b>15.5</b>

**L:** Lecture, **T:** Tutorial, **P:** Practical, **Cr:** Credits

**ETE:** End Term Exam, **IA:** Internal Assessment



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

<b>List of Open Electives for Electronics &amp; Communication Engineering</b>			
<b>Subject Code</b>	<b>Title</b>	<b>Subject Code</b>	<b>Title</b>
<b>Open Elective - I</b>		<b>Open Elective - II</b>	
7AG6-60.1	Human Engineering and Safety	8AG6-60.1	Energy Management
7AG6-60.2	Environmental Engineering and Disaster Management	8AG6-60.2	Waste and By-product Utilization
7AN6-60.1	Aircraft Avionic System	8AN6-60.1	Finite Element Methods
7AN6-60.2	Non-Destructive Testing	8AN6-60.2	Factor of Human Interactions
7CH6-60.1	Optimization Techniques	8CH6-60.1	Refinery Engineering Design
7CH6-60.2	Sustainable Engineering	8CH6-60.2	Fertilizer Technology
7CR6-60.1	Introduction to Ceramic Science & Technology	8CR6-60.1	Electrical and Electronic Ceramics
7CR6-60.2	Plant, Equipment and Furnace Design	8CR6-60.2	Biomaterials
7CE6-60.1	Environmental Impact Analysis	8CE6-60.1	Composite Materials
7CE6-60.2	Disaster Management	8CE6-60.2	Fire and Safety Engineering
7CS6-60.1	Quality Management/ISO 9000	8CS6-60.1	Big Data Analytics
7CS6-60.2	Cyber Security	8CS6-60.2	IPR, Copyright and Cyber Law of India
7EE6-60.1	Electrical Machines and Drives	8EE6-60.1	Energy Audit and Demand side Management
7EE6-60.2	Power Generation Sources.	8EE6-60.2	Soft Computing
7ME6-60.1	Finite Element Analysis	8ME6-60.1	Operations Research
7ME6-60.2	Quality Management	8ME6-60.2	Simulation Modeling and Analysis
7MI6-60.1	Rock Engineering	8MI6-60.1	Experimental Stress Analysis
7MI6-60.2	Mineral Processing	8MI6-60.2	Maintenance Management
7PE6-60.1	Pipeline Engineering	8PE6-60.1	Unconventional Hydrocarbon Resources
7PE6-60.2	Water Pollution control Engineering	8PE6-60.2	Energy Management & Policy
7TT6-60.1	Technical Textiles	8TT6-60.1	Material and Human Resource Management
7TT6-60.2	Garment Manufacturing Technology	8TT6-60.2	Disaster Management



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### 7EC5-11: VLSI Design (program elective-3)

Credit: 3  
3L+0T+0P

Max. Marks: 100(IA:30, ETE:70)

End Term Exam: 3 Hours

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	INTRODUCTION TO MOSFET- Basic MOS transistors, Enhancement Mode transistor action, Depletion Mode transistor action, NMOS and CMOS fabrication. Aspects of threshold voltage, threshold voltage with body effect. $I_{ds}$ versus $V_{ds}$ relationship, channel length modulation. Transistor Trans-conductance $g_m$ . MOS transistor circuit Model, Model parameter (oxide and junction capacitor, channel resistance) variation with scaling and biasing. High order effects (i.e. sub threshold conduction, hot electron effect, narrow channel effect and punch through effect.	12
3	CMOS LOGIC CIRCUITS- NMOS inverter (resistive and active load), Pull up to Pull-down ratio( $\beta_p/\beta_n$ ) for a NMOS Inverter and CMOS Inverter, determination of inverter parameter ( $V_{IL}$ , $V_{IH}$ , $V_{OL}$ , $V_{OH}$ ) and Noise Margin. Speed and power dissipation analysis of CMOS inverter. Combinational Logic, NAND Gate, NOR gate, XOR gate, Compound Gates, 2 input CMOS Multiplexer, Memory latches and registers, Transmission Gate (TG), estimation of Gate delays, Power dissipation and Transistor sizing. Basic physical design of simple Gates and Layout issues. Layout issues for CMOS inverter, Layout for NAND, NOR and Complex Logic gates, Layout of TG, Layout optimization using Euler path. DRC rules for layout and issues of interconnects, Latch up problem.	11
4	Dynamic CMOS circuits- Clocked CMOS ( $C^2$ MOS) logic, DOMINO logic, NORA logic, NP(ZIPPER) logic, PE (pre-charge and Evaluation) Logic. Basic Memory circuits, SRAM and DRAM.	08
5	Physical Design- Introduction to ECAD tools for front and back end design of VLSI circuits. Custom /ASIC design, Design using FPGA and VHDL. VHDL Code for simple Logic gates, flip-flops, shift registers.	08
	<b>Total</b>	<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

<b>Text/Reference Books:</b>	
<b>1</b>	Cmos Digital Integrated Circuits Analysis And Design. Sung-Mo (Steve) Kang, Yusuf Leblebici, McGraw Hill (2008)
<b>2</b>	N.Weste and K. Eshraghian, Principles of CMOS VLSI, 2e, Pearson Education, 2011
<b>3</b>	VLSI Design, P PSahu , , McGraw, 2013
<b>4</b>	VLSI Design, D.P. Das, Oxford, 2011
<b>5</b>	Chip Design for Submicron VLSI: CMOS Layout & Simulation, Uyemura, cengage learning, 2009



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### 7EC5-12: Mixed Signal Design(program elective-3)

**Credit: 3**  
**3L+0T+0P**

**Max. Marks: 100(IA:30, ETE:70)**  
**End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Analog and discrete-time signal processing, introduction to sampling theory; Analog continuous time filters: passive and active filters; Basics of analog discrete-time filters and Z-transform.	10
3	Basic logic gates with BJT and MOSFET combination, Switched-capacitor filters- Non idealities in switched-capacitor filters; Switched-capacitor filter architectures; Switched-capacitor filter applications.	07
4	Basics of data converters; Successive approximation ADCs, Dual slope ADCs, Flash ADCs, Pipeline ADCs, Hybrid ADC structures, High-resolution ADCs, DACs.	08
5	Mixed-signal layout, Interconnects and data transmission; Voltage-mode signal aligned data transmission; Current-mode signaling and data transmission.	08
6	Introduction to frequency synthesizers and synchronization; Basics of PLL, Analog PLLs; Digital PLLs; DLLs	06
	<b>Total</b>	<b>40</b>

<b>Text/Reference Books:</b>	
1.	R. Jacob Baker, CMOS mixed-signal circuit design, Wiley India, IEEE press, reprint 2008.
2.	Behzad Razavi, Design of analog CMOS integrated circuits, McGraw-Hill, 2003.
3.	R. Jacob Baker, CMOS circuit design, layout and simulation, Revised second edition, IEEE press, 2008.
4.	Rudy V. de Plassche, CMOS Integrated ADCs and DACs, Springer, Indian edition, 2005.
5.	Arthur B. Williams, Electronic Filter Design Handbook, McGraw-Hill, 1981.
6.	R. Schauman, Design of analog filters by, Prentice-Hall 1990 (or newer additions).
7.	M. Burns et al., An introduction to mixed-signal IC test and measurement by, Oxford university press, first Indian edition, 2008.





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### 7EC5-13: CMOS Design (program elective-3)

Credit: 3

Max. Marks: 100(IA:30, ETE:70)

3L+0T+0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Review of MOS transistor models, Non-ideal behavior of the MOS Transistor, Transistor as a switch, Inverter characteristics	08
3	Integrated Circuit Layout: Design Rules, Parasitic, Delay: RC Delay model, linear delay model, logical path efforts, Power, interconnect and Robustness in CMOS circuit layout	07
4	Combinational Circuit Design: CMOS logic families including static, dynamic and dual rail logic. NAND Gate, NOR gate, XOR gate, Compound Gates, 2 input CMOS Multiplexer, Memory latches and registers, Transmission Gate, estimation of Gate delays, Power dissipation and Transistor sizing. Basic physical design of simple Gates and Layout issues. Layout issues for CMOS inverter, Layout for NAND, NOR and Complex Logic gates,	10
5	Dynamic CMOS circuits- Clocked CMOS (C <sup>2</sup> MOS) logic, DOMINO logic, NORA logic, NP(ZIPPER) logic, PE (pre-charge and Evaluation) Logic. Basic Memory circuits, SRAM and DRAM.	08
6	Physical Design- Introduction to ECAD tools for first and back end design of VLSI circuits. Custom /ASIC design, Design using FPGA and VHDL. VHDL Code for simple Logic gates, flip-flops, shift registers.	06
	<b>Total</b>	<b>40</b>

#### Text/Reference Books:

1.	N.H.E. Weste and D.M. Harris, CMOS VLSI design: A Circuits and Systems Perspective, 4th Edition, Pearson Education India, 2011.
2.	Sung-Mo-Kang and Yusuf Leblebici, CMOS Digital Integrated Circuits Analysis & Design, McGraw Hill
3.	C.Mead and L. Conway, Introduction to VLSI Systems, Addison Wesley, 1979.
4.	J. Rabaey, Digital Integrated Circuits: A Design Perspective, Prentice Hall India, 1997.
5.	P. Douglas, VHDL: programming by example, McGraw Hill, 2013.
6.	L. Glaser and D. Dobberpuhl, The Design and Analysis of VLSI Circuits, Addison Wesley, 1985.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### 7EC4-21: VLSI Design Lab

Credit:  
OL+OT+4P

Max. Marks: 100(IA:60, ETE:40)

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
<b>PART-A</b>	Step1 Write the VHDL/Verilog code using VHDL software for following experiment and simulate them. Step 2. Burn the Written code in Xilling Board and test the output with real input signal
1	Design and simulate all the logic gates with 2 inputs using VHDL/Verilog.
2	Design and simulate 2-to-4 decoder,3-to-8 encoder and 8X1 multiplexer using VHDL/Verilog.
3	Design and simulate half adder and full adder using VHDL (data flow method)/Verilog.
4	Design and simulate D, T and J-K flip flop using VHDL/Verilog.
5	Design a 4bit binary Asynchronous and synchronous counter. Obtain its number of gates, area, and speed and power dissipation.
6	Design a 4- bit Serial in-serial out shift register. Obtain its number of gates, area, and speed and power dissipation.
<b>PART-B</b>	Step-1 Design and simulate following experiment using ECAD software Viz. Mentor graphics, Orcade Pspice, Cadence etc. Step-2 Draw the layout (without any DRC error)of the schematic obtain in step 1 and obtain post layout simulation using appropriate ECAD software.
1	Design and simulate all the logic gates (NOT, NAND and NOR) with 2 inputs in CMOS Technology.
2	Design and simulate $Y = AB (C+D)$ , $Y = A+B(C+D)$ and 4X1 multiplexer using CMOS Technology.
3	Design and simulate half adder and full adder using CMOS Technology.
4	Design and simulate SR flip flop using CMOS Technology.
5	Design and Simulate any DRAM cell.





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### 7EC4-22: Advance Communication Lab (MATLAB Simulation)

Credit: 1Max.

Marks: 100 (IA:60, ETE:40)

OL+0T+2P

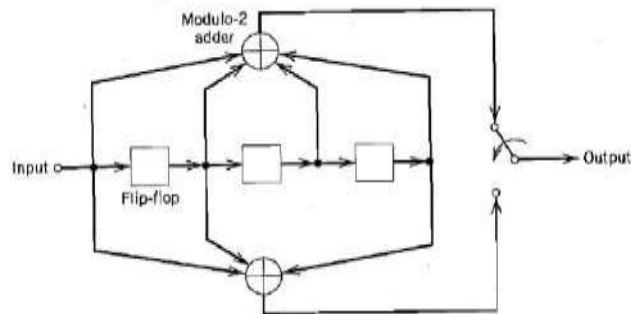
SN	Contents
1	Introduction: Objective, scope and outcome of the course.
<b>Part-A</b>	<b>Analog-to-digital conversion</b>  <ol style="list-style-type: none"><li>1. Generate a sinusoidal signal. Sample and reconstruct a signal through interpolation. Vary the sampling rate below and above the Nyquist rate and hence verify the Sampling theorem.</li><li>2. Generate a sequence of length 500 of zero-mean, unit variance Gaussian random variables. Using a uniform PCM scheme, quantize this sequence to 16, 64 and 128 levels.<ol style="list-style-type: none"><li>(a). Find and compare the resulting signal-to-quantization noise ratios.</li><li>(b). Find the first ten values of the sequence, the corresponding quantized values and the corresponding code words for each case.</li><li>(c). Plot the quantization error and the quantized value as a function of the input value for each case.</li></ol></li></ol> <b>Digital modulation techniques</b>  <ol style="list-style-type: none"><li>3. Simulate the transmitter and receiver for QPSK. Plot the signal and signal constellation diagram. Plot the average probability of symbol error as a function of SNR <math>E_b/N_o</math>, where <math>E_b</math> is the transmitted energy per bit and <math>N_o/2</math> is the double sided power spectral density of additive white Gaussian noise (AWGN) with zero mean.</li><li>4. Simulate the transmitter and receiver for 16-QAM. Plot the signal and signal constellation diagram. Plot the average probability of symbol error as a function of SNR <math>E_b/N_o</math>, where <math>E_b</math> is the transmitted energy per bit and <math>N_o/2</math> is the double sided power spectral density of additive white Gaussian noise (AWGN) with zero mean.</li></ol>
<b>PART-B Attempt any four experiment</b>	<ol style="list-style-type: none"><li>1. Find all the code words of the (15,11) Hamming code and verify that its minimum distance is equal to 3.</li><li>2. Generate an equiprobable random binary information sequence of length 15. Determine the output of the convolutional encoder shown below for this sequence.</li></ol>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)



3. Generate the  $L=31$  Gold sequences. Consider a time-synchronous CDMA system (direct sequence spread spectrum) having four users, each employing a distinct Gold sequence of length  $L=31$  and the binary ( $\pm 1$ ) modulation of their representative Gold sequences. The receiver for each user correlates the composite CDMA received signal, which is corrupted by AWGN (added on a chip-by-chip basis) with each user's respective sequence. Using 10000 information bits, estimate and plot the probability of error for each user as a function of SNR.
4. Consider a MIMO (multiple-input, multiple-output) system with  $N_T = 2$  transmit antennas and  $N_R = 2$  receive antennas. Generate the elements of the channel matrix  $\mathbf{H}$  for a Rayleigh fading (frequency nonselective) AWGN channel and the corresponding inputs to the detectors for the two receive antennas.
5. Perform feature extraction from a given Image and use Principal Components as image descriptors.
6. By using an image dataset, train a Neural Network to recognize a given Image. Apply this in context to face/object recognition and calculate recognition accuracy of the training set.
7. Develop a Fuzzy Inference System (FIS) by using a set of fuzzy rule base between some key image parameters and calculate output after defuzzification.
8. Design a Fuzzy PID controller using Matlab for a Dc Motor.
9. Classify ECG signals using Neural networks.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## 7EC4-23: Optical Communication Lab

Credit: 1Max.

Marks: 100 (IA:60, ETE:40)

OL+OT+2P

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
	Hardware based experiment;
1	To set up Fiber Optic Analog and fiber Optic Digital link.
2	Measurement of Propagation loss and numerical aperture.
3	Measurement of optical power bending loss in a plastic optical fiber.
4	Study and measure characteristics of fiber optic LED's, LDR and Laser diode.
5	OTDR Measurement of Fiber Length, Attenuation and Dispersion Loss.
	Software based experiment;
6	Design and simulate of single and multimode transmission in optical fiber system.
7	Show and simulate the optical system performance analysis using Eye diagram and measure the value of Q-factor & BER of optical signals.
8	Study and simulate the linear and parabolic waveguide structure use in optical fiber communication.
9	Design and simulate the Dispersion compensators for fiber optic communication.
10	Design and calculate the power budget for optical communication link.
11	Design and simulate the DWDM and WDM techniques use in optical communication.
12	Design and simulate the Fiber Bragg grating and find its transmission characteristics and optical band-gap.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## SEC5-11: ARTIFICIAL INTELLIGENCE AND EXPERT SYSTEMS (program elective-4)

Credit: 3

Max. Marks: 100(IA:30, ETE:70)

3L+0T+0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	<b>Introduction to Artificial Intelligence:</b> Intelligent Agents, State Space Search, Uninformed Search, Informed Search, Two Players Games, Constraint Satisfaction Problems.	08
3	<b>Knowledge Representation:</b> Knowledge Representation And Logic, Interface in Propositional Logic, First Order Logic, Reasoning Using First Order Logic, Resolution in FOPL.	07
4	<b>KNOWLEDGE ORGANIZATION:</b> Rule based System, Semantic Net, Reasoning in Semantic Net Frames, Planning	08
5	<b>KNOWLEDGE SYSTEMS:</b> Rule Based Expert System, Reasoning with Uncertainty, Fuzzy Reasoning.	08
6	<b>KNOWLEDGE ACQUISITION:</b> Introduction to Learning, Rule Induction and Decision Trees, Learning Using neural Networks, Probabilistic Learning Natural Language Processing.	08
	<b>Total</b>	<b>40</b>

### Text/Reference Books:

1.	Elaine Rich and Kevin Knight, Artificial Intelligence 3/e, TMH (1991)
2.	PADHY: ARTIFICIAL INTELLIGENCE & INTELLIGENT SYSTEMS, Oxford (2005)
3.	James A Anderson, An introduction to Neural Networks. Bradford Books 1995
4.	Dan. W Patterson, Artificial Intelligence and Expert Systems, PHI 1990
5.	Kumar Satish, "Neural Networks" Tata Mc Graw Hill 2004
6.	S. Rajsekaran& G.A. Vijayalakshmi Pai, "Neural Networks, Fuzzy Logic and Genetic Algorithm: Synthesis and Applications" Prentice Hall of India. 2006
7.	SimanHaykin, "Neural Netowrks" Prentice Hall of India 1990
8.	Artificial Intelligence, Kaushik, cengage learning 1997



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## SEC5-12: Digital Image and Video Processing (program elective-4)

**Credit: 3**

**Max. Marks: 100(IA:30, ETE:70)**

**3L+0T+0P**

**End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Digital Image Fundamentals-Elements of visual perception, image sensing and acquisition, image sampling and quantization, basic relationships between pixels neighborhood, adjacency, connectivity, distance measures.	04
3	Image Enhancements and Filtering-Gray level transformations, histogram equalization and specifications, pixel-domain smoothing filters – linear and order-statistics, pixel-domain sharpening filters – first and second derivative, two-dimensional DFT and its inverse, frequency domain filters – low-pass and high-pass.	03
4	Color Image Processing-Color models-RGB, YUV, HSI; Color transformations-formulation, color complements, color slicing, tone and color corrections; Color image smoothing and sharpening; Color Segmentation.	04
5	Image Segmentation- Detection of discontinuities, edge linking and boundary detection, Thresholding – global and adaptive, region-based segmentation.	04
6	Wavelets and Multi-resolution image processing- Uncertainty principles of Fourier Transform, Time-frequency localization, continuous wavelet transforms, wavelet bases and multi-resolution analysis, wavelets and Sub-band filter banks, wavelet packets.	06
7	Image Compression-Redundancy-inter-pixel and psycho-visual; Lossless compression – predictive, entropy; Lossy compression-predictive and transform coding; Discrete Cosine Transform; Still image compression standards – JPEG and JPEG-2000.	06
8	Fundamentals of Video Coding- Inter-frame redundancy, motion estimation techniques – full search, fast search strategies, forward and backward motion prediction, frame classification – I, P and B; Video sequence hierarchy – Group of pictures, frames, slices, macro-blocks and blocks; Elements of a video encoder and decoder; Video coding standards – MPEG and H.26X.	06
9	Video Segmentation- Temporal segmentation-shot boundary detection, hard-cuts and soft-cuts; spatial segmentation – motion-based; Video object detection and tracking.	06
	<b>Total</b>	<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

<b>Text/Reference Books:</b>	
<b>1.</b>	R.C. Gonzalez and R.E. Woods, Digital Image Processing, Second Edition, Pearson Education 3rd edition 2008
<b>2</b>	R.C. Gonzalez, R.E. Woods and S.L. Eddins, Digital Image Processing using Matlab, McGraw Hill, 2 <sup>nd</sup> Edition
<b>3.</b>	Anil Kumar Jain, Fundamentals of Digital Image Processing, Prentice Hall of India. 2 <sup>nd</sup> edition 2004
<b>4.</b>	Murat Tekalp , Digital Video Processing" Prentice Hall, 2nd edition 2015





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### SEC5-13: Adaptive Signal Processing (program elective-4)

Credit: 3

Max. Marks: 100(IA:30, ETE:70)

3L+0T+0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	General concept of adaptive filtering and estimation, applications and motivation, Review of probability, random variables and stationary random processes, Correlation structures, properties of correlation matrices.	08
3	Optimal FIR (Wiener) filter, Method of steepest descent, extension to complex valued The LMS algorithm (real, complex), convergence analysis, weight error correlation matrix, excess mean square error and mis-adjustment Variants of the LMS algorithm: the sign LMS family, normalized LMS algorithm, block LMS and FFT based realization, frequency domain adaptive filters, Sub-band adaptive filtering.	07
4	Signal space concepts - introduction to finite dimensional vector space theory, subspace, basis, dimension, linear operators, rank and nullity, inner product space, orthogonality, Gram-Schmidt orthogonalization, concepts of orthogonal projection, orthogonal decomposition of vector spaces.	08
5	Vector space of random variables, correlation as inner product, forward and backward projections, Stochastic lattice filters, recursive updating of forward and backward prediction errors, relationship with AR modeling, joint process estimator, gradient adaptive lattice.	08
6	Introduction to recursive least squares (RLS), vector space formulation of RLS estimation, pseudo-inverse of a matrix, time updating of inner products, development of RLS lattice filters, RLS transversal adaptive filters. Advanced topics: affine projection and subspace based adaptive filters, partial update algorithms, QR decomposition and systolic array.	08
	<b>Total</b>	<b>40</b>

#### Text/Reference Books:

1.	S. Haykin, Adaptive filter theory, Prentice Hall, 1986.
2.	C.Widrow and S.D. Stearns, Adaptive signal processing, Prentice Hall, 1984.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## SEC4-21: IOT Lab

Credit: 1  
OL+OT+2P

Max. Marks: 100 (IA:60, ETE:40)

LIST OF PRACTICALS	
1.	Study the fundamental of IOT softwares and components.
2.	Familiarization with Arduino/Raspberry Pi and perform necessary software installation.
3.	To interface LED/Buzzer with Arduino/Raspberry Pi and write a program to turn ON LED for 1 sec after every 2 seconds.
4.	To interface Push button/Digital sensor (IR/LDR) with Arduino/Raspberry Pi and write a program to turn ON LED when push button is pressed or at sensor detection.
5.	To interface DHT11 sensor with Arduino/Raspberry Pi and write a program to print temperature and humidity readings.
6.	To interface motor using relay with Arduino/Raspberry Pi and write a program to turn ON motor when push button is pressed.
7.	To interface OLED with Arduino/Raspberry Pi and write a program to print temperature and humidity readings on it.
8.	To interface Bluetooth with Arduino/Raspberry Pi and write a program to send sensor data to smartphone using Bluetooth.
9.	To interface Bluetooth with Arduino/Raspberry Pi and write a program to turn LED ON/OFF when '1'/'0' is received from smartphone using Bluetooth.
10.	Write a program on Arduino/Raspberry Pi to upload temperature and humidity data to thingspeak cloud.
11.	Write a program on Arduino/Raspberry Pi to retrieve temperature and humidity data from thingspeak cloud.
12.	To install MySQL database on Raspberry Pi and perform basic SQL queries.
13.	Write a program to create UDP server on Arduino/Raspberry Pi and respond with humidity data to UDP client when requested.
14.	Write a program to create TCP server on Arduino/Raspberry Pi and respond with humidity data to TCP client when requested.

LIST OF SUGGESTED BOOKS:	
1.	Vijay Madiseti, Arshdeep Bahga, Internet of Things, "A Hands on Approach", University Press.
2.	Dr. SRN Reddy, Rachit Thukral and Manasi Mishra, "Introduction to Internet of Things: A practical Approach", ETI Labs.
3.	Pethuru Raj and Anupama C. Raman, "The Internet of Things: Enabling Technologies, Platforms, and Use Cases", CRC Press
4.	Jeeva Jose, "Internet of Things", Khanna Publishing House, Delhi
5.	Adrian McEwen, "Designing the Internet of Things", Wiley
6.	Raj Kamal, "Internet of Things: Architecture and Design", McGraw Hill





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## SEC4- 22 Skill Development Lab

Credit: 1  
0L+0T+2P

Max. Marks : 100 (IA:60,ETE:40)

Part A: Training	
SN	Contents
1	Introduction: Objective, scope and outcome of the lab.
	<b>Every student has to learn any two software from the following list, with consultation of their lab in charge. Students may get online certification or is advised to learn these from available freeware. Students may register online training courses from institutes of repute i.e. IITs/NITs/AICTE/MHRD, etc. Industrial experts /professional may be deputed to train the students in department.</b>
1	Network simulator (NS <sub>2</sub> )
2	Lab view
3	Software for Robotics/Artificial intelligence (AI) /machine learning
4	Java
5	Python

PART B: Implementation	
SN	Contents
1	<b>Student has to complete any one assignment with detailed project report based on the software/tool learn in part A.</b>
2	Student can select any Social engineering project: Any problem of the society can be taken which can be solved with the help of electronics engineering software and gadgets.
3	Student can select Startup for innovation/entrepreneurship.
4	Engineering solution of any Industrial problem. Sufficient number of such problem may be identified by the department from nearby industry and may be given to the student for innovative solutions under guidance of faculty.
	<b>This lab may be evaluated by an external examiner from industry along with internal faculty.</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## Teaching & Examination Scheme B.Tech. : Electronics & Communication Engineering 4<sup>th</sup> Year - VII Semester

THEORY											
SN	Category	Course		Contact hrs/week			Marks			Cr	
		Code	Title	L	T	P	Exm Hrs	IA	ETE		Total
1	PEC	Program Elective		3	0	0	3	30	70	100	3
		7EC5-11	VLSI Design								
		7EC5-12	Mixed Signal Design								
		7EC5-13	CMOS design								
2	OE		Open Elective-I	3	0	0	3	30	70	100	3
			<b>Sub Total</b>	<b>6</b>	<b>0</b>	<b>0</b>		<b>60</b>	<b>140</b>	<b>200</b>	<b>6</b>
PRACTICAL & SESSIONAL											
3	PCC	7EC4-21	VLSI Design Lab	0	0	4	2	60	40	100	2
4		7EC4-22	Advance communication lab (MATLAB Simulation)	0	0	2	2	60	40	100	1
5		7EC4-23	Optical Communication Lab	0	0	2	2	60	40	100	1
6	PSIT	7EC7-30	Industrial Training	1	0	0		60	40	100	2.5
7		7EC7-40	Seminar	2	0	0		60	40	100	2
8	SODECA	7EC8-00	Social Outreach, Discipline & Extra Curricular Activities					60	40	100	0.5
			<b>Sub Total</b>	<b>3</b>	<b>0</b>	<b>8</b>		<b>360</b>	<b>240</b>	<b>600</b>	<b>9</b>
			<b>TOTAL of VII SEMESTER</b>	<b>9</b>	<b>0</b>	<b>8</b>		<b>420</b>	<b>380</b>	<b>800</b>	<b>15</b>

**L:** Lecture, **T:** Tutorial, **P:** Practical, **Cr:** Credits

**ETE:** End Term Exam, **IA:** Internal Assessment



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## Teaching & Examination Scheme B.Tech. : Electronics & Communication Engineering 4<sup>th</sup> Year - VIII Semester

THEORY											
SN	Category	Course Code	Course Title	Contact hrs/week			Marks				Cr
				L	T	P	Exm Hrs	IA	ETE	Total	
1	PEC	Program Elective		3	0	0	3	30	70	100	3
		8EC5-11	Artificial Intelligence And Expert Systems								
		8EC5-12	Digital Image and Video Processing								
		8EC5-13	Adaptive Signal Processing								
2	OE		Open Elective-II	3	0	0	3	30	70	100	3
<b>Sub Total</b>				<b>6</b>	<b>0</b>	<b>0</b>		<b>60</b>	<b>140</b>	<b>200</b>	<b>6</b>
PRACTICAL & SESSIONAL											
3	PCC	8EC4-21	Internet of Things (IOT) Lab	0	0	2	2	60	40	100	1
4		8EC4-22	Skill Development Lab	0	0	2	2	60	40	100	1
5	PSIT	8EC7-50	Project	3	0	0		60	40	100	7
6	SODECA	8EC8-00	Social Outreach, Discipline & Extra Curricular Activities					60	40	100	0.5
<b>Sub Total</b>				<b>3</b>	<b>0</b>	<b>4</b>		<b>240</b>	<b>160</b>	<b>400</b>	<b>9.5</b>
<b>TOTAL of VIII SEMESTER</b>				<b>9</b>	<b>0</b>	<b>4</b>		<b>300</b>	<b>300</b>	<b>600</b>	<b>15.5</b>

**L:** Lecture, **T:** Tutorial, **P:** Practical, **Cr:** Credits

**ETE:** End Term Exam, **IA:** Internal Assessment



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

<b>List of Open Electives for Electronics &amp; Communication Engineering</b>			
<b>Subject Code</b>	<b>Title</b>	<b>Subject Code</b>	<b>Title</b>
<b>Open Elective - I</b>		<b>Open Elective - II</b>	
7AG6-60.1	Human Engineering and Safety	8AG6-60.1	Energy Management
7AG6-60.2	Environmental Engineering and Disaster Management	8AG6-60.2	Waste and By-product Utilization
7AN6-60.1	Aircraft Avionic System	8AN6-60.1	Finite Element Methods
7AN6-60.2	Non-Destructive Testing	8AN6-60.2	Factor of Human Interactions
7CH6-60.1	Optimization Techniques	8CH6-60.1	Refinery Engineering Design
7CH6-60.2	Sustainable Engineering	8CH6-60.2	Fertilizer Technology
7CR6-60.1	Introduction to Ceramic Science & Technology	8CR6-60.1	Electrical and Electronic Ceramics
7CR6-60.2	Plant, Equipment and Furnace Design	8CR6-60.2	Biomaterials
7CE6-60.1	Environmental Impact Analysis	8CE6-60.1	Composite Materials
7CE6-60.2	Disaster Management	8CE6-60.2	Fire and Safety Engineering
7CS6-60.1	Quality Management/ISO 9000	8CS6-60.1	Big Data Analytics
7CS6-60.2	Cyber Security	8CS6-60.2	IPR, Copyright and Cyber Law of India
7EE6-60.1	Electrical Machines and Drives	8EE6-60.1	Energy Audit and Demand side Management
7EE6-60.2	Power Generation Sources.	8EE6-60.2	Soft Computing
7ME6-60.1	Finite Element Analysis	8ME6-60.1	Operations Research
7ME6-60.2	Quality Management	8ME6-60.2	Simulation Modeling and Analysis
7MI6-60.1	Rock Engineering	8MI6-60.1	Experimental Stress Analysis
7MI6-60.2	Mineral Processing	8MI6-60.2	Maintenance Management
7PE6-60.1	Pipeline Engineering	8PE6-60.1	Unconventional Hydrocarbon Resources
7PE6-60.2	Water Pollution control Engineering	8PE6-60.2	Energy Management & Policy
7TT6-60.1	Technical Textiles	8TT6-60.1	Material and Human Resource Management
7TT6-60.2	Garment Manufacturing Technology	8TT6-60.2	Disaster Management



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### 7EC5-11: VLSI Design (program elective-3)

Credit: 3  
3L+0T+0P

Max. Marks: 100(IA:30, ETE:70)

End Term Exam: 3 Hours

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	INTRODUCTION TO MOSFET- Basic MOS transistors, Enhancement Mode transistor action, Depletion Mode transistor action, NMOS and CMOS fabrication. Aspects of threshold voltage, threshold voltage with body effect. $I_{ds}$ versus $V_{ds}$ relationship, channel length modulation. Transistor Trans-conductance $g_m$ . MOS transistor circuit Model, Model parameter (oxide and junction capacitor, channel resistance) variation with scaling and biasing. High order effects (i.e. sub threshold conduction, hot electron effect, narrow channel effect and punch through effect.	12
3	CMOS LOGIC CIRCUITS- NMOS inverter (resistive and active load), Pull up to Pull-down ratio( $\beta_p/\beta_n$ ) for a NMOS Inverter and CMOS Inverter, determination of inverter parameter ( $V_{IL}$ , $V_{IH}$ $V_{OL}$ $V_{OH}$ ) and Noise Margin. Speed and power dissipation analysis of CMOS inverter. Combinational Logic, NAND Gate, NOR gate, XOR gate, Compound Gates, 2 input CMOS Multiplexer, Memory latches and registers, Transmission Gate (TG), estimation of Gate delays, Power dissipation and Transistor sizing. Basic physical design of simple Gates and Layout issues. Layout issues for CMOS inverter, Layout for NAND, NOR and Complex Logic gates, Layout of TG, Layout optimization using Euler path. DRC rules for layout and issues of interconnects, Latch up problem.	11
4	Dynamic CMOS circuits- Clocked CMOS ( $C^2$ MOS) logic, DOMINO logic, NORA logic, NP(ZIPPER) logic, PE (pre-charge and Evaluation) Logic. Basic Memory circuits, SRAM and DRAM.	08
5	Physical Design- Introduction to ECAD tools for front and back end design of VLSI circuits. Custom /ASIC design, Design using FPGA and VHDL. VHDL Code for simple Logic gates, flip-flops, shift registers.	08
	<b>Total</b>	<b>40</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

<b>Text/Reference Books:</b>	
<b>1</b>	Cmos Digital Integrated Circuits Analysis And Design. Sung-Mo (Steve) Kang, Yusuf Leblebici, McGraw Hill (2008)
<b>2</b>	N.Weste and K. Eshraghian, Principles of CMOS VLSI, 2e, Pearson Education, 2011
<b>3</b>	VLSI Design, P PSahu , , McGraw, 2013
<b>4</b>	VLSI Design, D.P. Das, Oxford, 2011
<b>5</b>	Chip Design for Submicron VLSI: CMOS Layout & Simulation, Uyemura, cengage learning, 2009



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### 7EC5-12: Mixed Signal Design(program elective-3)

**Credit: 3**  
**3L+0T+0P**

**Max. Marks: 100(IA:30, ETE:70)**  
**End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Analog and discrete-time signal processing, introduction to sampling theory; Analog continuous time filters: passive and active filters; Basics of analog discrete-time filters and Z-transform.	10
3	Basic logic gates with BJT and MOSFET combination, Switched-capacitor filters- Non idealities in switched-capacitor filters; Switched-capacitor filter architectures; Switched-capacitor filter applications.	07
4	Basics of data converters; Successive approximation ADCs, Dual slope ADCs, Flash ADCs, Pipeline ADCs, Hybrid ADC structures, High-resolution ADCs, DACs.	08
5	Mixed-signal layout, Interconnects and data transmission; Voltage-mode signal aligned data transmission; Current-mode signaling and data transmission.	08
6	Introduction to frequency synthesizers and synchronization; Basics of PLL, Analog PLLs; Digital PLLs; DLLs	06
	<b>Total</b>	<b>40</b>

<b>Text/Reference Books:</b>	
1.	R. Jacob Baker, CMOS mixed-signal circuit design, Wiley India, IEEE press, reprint 2008.
2.	Behzad Razavi, Design of analog CMOS integrated circuits, McGraw-Hill, 2003.
3.	R. Jacob Baker, CMOS circuit design, layout and simulation, Revised second edition, IEEE press, 2008.
4.	Rudy V. de Plassche, CMOS Integrated ADCs and DACs, Springer, Indian edition, 2005.
5.	Arthur B. Williams, Electronic Filter Design Handbook, McGraw-Hill, 1981.
6.	R. Schauman, Design of analog filters by, Prentice-Hall 1990 (or newer additions).
7.	M. Burns et al., An introduction to mixed-signal IC test and measurement by, Oxford university press, first Indian edition, 2008.





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### 7EC5-13: CMOS Design (program elective-3)

Credit: 3

Max. Marks: 100(IA:30, ETE:70)

3L+0T+0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Review of MOS transistor models, Non-ideal behavior of the MOS Transistor, Transistor as a switch, Inverter characteristics	08
3	Integrated Circuit Layout: Design Rules, Parasitic, Delay: RC Delay model, linear delay model, logical path efforts, Power, interconnect and Robustness in CMOS circuit layout	07
4	Combinational Circuit Design: CMOS logic families including static, dynamic and dual rail logic. NAND Gate, NOR gate, XOR gate, Compound Gates, 2 input CMOS Multiplexer, Memory latches and registers, Transmission Gate, estimation of Gate delays, Power dissipation and Transistor sizing. Basic physical design of simple Gates and Layout issues. Layout issues for CMOS inverter, Layout for NAND, NOR and Complex Logic gates,	10
5	Dynamic CMOS circuits- Clocked CMOS (C <sup>2</sup> MOS) logic, DOMINO logic, NORA logic, NP(ZIPPER) logic, PE (pre-charge and Evaluation) Logic. Basic Memory circuits, SRAM and DRAM.	08
6	Physical Design- Introduction to ECAD tools for first and back end design of VLSI circuits. Custom /ASIC design, Design using FPGA and VHDL. VHDL Code for simple Logic gates, flip-flops, shift registers.	06
	<b>Total</b>	<b>40</b>

#### Text/Reference Books:

1.	N.H.E. Weste and D.M. Harris, CMOS VLSI design: A Circuits and Systems Perspective, 4th Edition, Pearson Education India, 2011.
2.	Sung-Mo-Kang and Yusuf Leblebici, CMOS Digital Integrated Circuits Analysis & Design, McGraw Hill
3.	C.Mead and L. Conway, Introduction to VLSI Systems, Addison Wesley, 1979.
4.	J. Rabaey, Digital Integrated Circuits: A Design Perspective, Prentice Hall India, 1997.
5.	P. Douglas, VHDL: programming by example, McGraw Hill, 2013.
6.	L. Glaser and D. Dobberpuhl, The Design and Analysis of VLSI Circuits, Addison Wesley, 1985.





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### 7EC4-21: VLSI Design Lab

**Credit:**  
**OL+OT+4P**

**Max. Marks: 100(IA:60, ETE:40)**

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
<b>PART-A</b>	Step1 Write the VHDL/Verilog code using VHDL software for following experiment and simulate them. Step 2. Burn the Written code in Xilling Board and test the output with real input signal
1	Design and simulate all the logic gates with 2 inputs using VHDL/Verilog.
2	Design and simulate 2-to-4 decoder,3-to-8 encoder and 8X1 multiplexer using VHDL/Verilog.
3	Design and simulate half adder and full adder using VHDL (data flow method)/Verilog.
4	Design and simulate D, T and J-K flip flop using VHDL/Verilog.
5	Design a 4bit binary Asynchronous and synchronous counter. Obtain its number of gates, area, and speed and power dissipation.
6	Design a 4- bit Serial in-serial out shift register. Obtain its number of gates, area, and speed and power dissipation.
<b>PART-B</b>	Step-1 Design and simulate following experiment using ECAD software Viz. Mentor graphics, Orcade Pspice, Cadence etc. Step-2 Draw the layout (without any DRC error)of the schematic obtain in step 1 and obtain post layout simulation using appropriate ECAD software.
1	Design and simulate all the logic gates (NOT, NAND and NOR) with 2 inputs in CMOS Technology.
2	Design and simulate $Y = AB (C+D)$ , $Y = A+B(C+D)$ and 4X1 multiplexer using CMOS Technology.
3	Design and simulate half adder and full adder using CMOS Technology.
4	Design and simulate SR flip flop using CMOS Technology.
5	Design and Simulate any DRAM cell.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### 7EC4-22: Advance Communication Lab (MATLAB Simulation)

Credit: 1Max.

Marks: 100 (IA:60, ETE:40)

OL+0T+2P

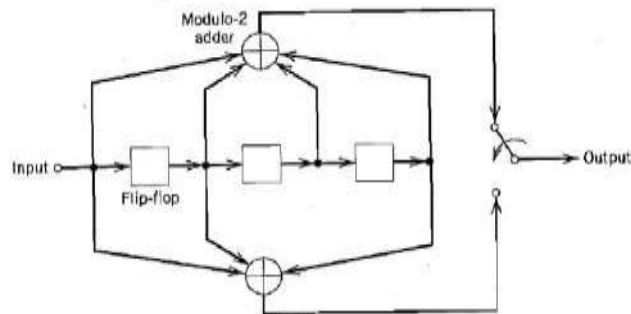
SN	Contents
1	Introduction: Objective, scope and outcome of the course.
<b>Part-A</b>	<b>Analog-to-digital conversion</b>  <ol style="list-style-type: none"><li>1. Generate a sinusoidal signal. Sample and reconstruct a signal through interpolation. Vary the sampling rate below and above the Nyquist rate and hence verify the Sampling theorem.</li><li>2. Generate a sequence of length 500 of zero-mean, unit variance Gaussian random variables. Using a uniform PCM scheme, quantize this sequence to 16, 64 and 128 levels.<ol style="list-style-type: none"><li>(a). Find and compare the resulting signal-to-quantization noise ratios.</li><li>(b). Find the first ten values of the sequence, the corresponding quantized values and the corresponding code words for each case.</li><li>(c). Plot the quantization error and the quantized value as a function of the input value for each case.</li></ol></li></ol> <b>Digital modulation techniques</b>  <ol style="list-style-type: none"><li>3. Simulate the transmitter and receiver for QPSK. Plot the signal and signal constellation diagram. Plot the average probability of symbol error as a function of SNR <math>E_b/N_o</math>, where <math>E_b</math> is the transmitted energy per bit and <math>N_o/2</math> is the double sided power spectral density of additive white Gaussian noise (AWGN) with zero mean.</li><li>4. Simulate the transmitter and receiver for 16-QAM. Plot the signal and signal constellation diagram. Plot the average probability of symbol error as a function of SNR <math>E_b/N_o</math>, where <math>E_b</math> is the transmitted energy per bit and <math>N_o/2</math> is the double sided power spectral density of additive white Gaussian noise (AWGN) with zero mean.</li></ol>
<b>PART-B Attempt any four experiment</b>	<ol style="list-style-type: none"><li>1. Find all the code words of the (15,11) Hamming code and verify that its minimum distance is equal to 3.</li><li>2. Generate an equiprobable random binary information sequence of length 15. Determine the output of the convolutional encoder shown below for this sequence.</li></ol>



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3. Generate the  $L=31$  Gold sequences. Consider a time-synchronous CDMA system (direct sequence spread spectrum) having four users, each employing a distinct Gold sequence of length  $L=31$  and the binary ( $\pm 1$ ) modulation of their representative Gold sequences. The receiver for each user correlates the composite CDMA received signal, which is corrupted by AWGN (added on a chip-by-chip basis) with each user's respective sequence. Using 10000 information bits, estimate and plot the probability of error for each user as a function of SNR.
4. Consider a MIMO (multiple-input, multiple-output) system with  $N_T = 2$  transmit antennas and  $N_R = 2$  receive antennas. Generate the elements of the channel matrix  $\mathbf{H}$  for a Rayleigh fading (frequency nonselective) AWGN channel and the corresponding inputs to the detectors for the two receive antennas.
5. Perform feature extraction from a given Image and use Principal Components as image descriptors.
6. By using an image dataset, train a Neural Network to recognize a given Image. Apply this in context to face/object recognition and calculate recognition accuracy of the training set.
7. Develop a Fuzzy Inference System (FIS) by using a set of fuzzy rule base between some key image parameters and calculate output after defuzzification.
8. Design a Fuzzy PID controller using Matlab for a Dc Motor.
9. Classify ECG signals using Neural networks.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## 7EC4-23: Optical Communication Lab

Credit: 1Max.

Marks: 100 (IA:60, ETE:40)

OL+OT+2P

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
	Hardware based experiment;
1	To set up Fiber Optic Analog and fiber Optic Digital link.
2	Measurement of Propagation loss and numerical aperture.
3	Measurement of optical power bending loss in a plastic optical fiber.
4	Study and measure characteristics of fiber optic LED's, LDR and Laser diode.
5	OTDR Measurement of Fiber Length, Attenuation and Dispersion Loss.
	Software based experiment;
6	Design and simulate of single and multimode transmission in optical fiber system.
7	Show and simulate the optical system performance analysis using Eye diagram and measure the value of Q-factor & BER of optical signals.
8	Study and simulate the linear and parabolic waveguide structure use in optical fiber communication.
9	Design and simulate the Dispersion compensators for fiber optic communication.
10	Design and calculate the power budget for optical communication link.
11	Design and simulate the DWDM and WDM techniques use in optical communication.
12	Design and simulate the Fiber Bragg grating and find its transmission characteristics and optical band-gap.



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## SEC5-11: ARTIFICIAL INTELLIGENCE AND EXPERT SYSTEMS (program elective-4)

Credit: 3

Max. Marks: 100(IA:30, ETE:70)

3L+0T+0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	<b>Introduction to Artificial Intelligence:</b> Intelligent Agents, State Space Search, Uninformed Search, Informed Search, Two Players Games, Constraint Satisfaction Problems.	08
3	<b>Knowledge Representation:</b> Knowledge Representation And Logic, Interface in Propositional Logic, First Order Logic, Reasoning Using First Order Logic, Resolution in FOPL.	07
4	<b>KNOWLEDGE ORGANIZATION:</b> Rule based System, Semantic Net, Reasoning in Semantic Net Frames, Planning	08
5	<b>KNOWLEDGE SYSTEMS:</b> Rule Based Expert System, Reasoning with Uncertainty, Fuzzy Reasoning.	08
6	<b>KNOWLEDGE ACQUISITION:</b> Introduction to Learning, Rule Induction and Decision Trees, Learning Using neural Networks, Probabilistic Learning Natural Language Processing.	08
	<b>Total</b>	<b>40</b>

### Text/Reference Books:

1.	Elaine Rich and Kevin Knight, Artificial Intelligence 3/e, TMH (1991)
2.	PADHY: ARTIFICIAL INTELLIGENCE & INTELLIGENT SYSTEMS, Oxford (2005)
3.	James A Anderson, An introduction to Neural Networks. Bradford Books 1995
4.	Dan. W Patterson, Artificial Intelligence and Expert Systems, PHI 1990
5.	Kumar Satish, "Neural Networks" Tata Mc Graw Hill 2004
6.	S. Rajsekaran& G.A. Vijayalakshmi Pai, "Neural Networks, Fuzzy Logic and Genetic Algorithm: Synthesis and Applications" Prentice Hall of India. 2006
7.	SimanHaykin, "Neural Netowrks" Prentice Hall of India 1990
8.	Artificial Intelligence, Kaushik, cengage learning 1997



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### SEC5-12: Digital Image and Video Processing (program elective-4)

**Credit: 3**

**Max. Marks: 100(IA:30, ETE:70)**

**3L+0T+0P**

**End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Digital Image Fundamentals-Elements of visual perception, image sensing and acquisition, image sampling and quantization, basic relationships between pixels neighborhood, adjacency, connectivity, distance measures.	04
3	Image Enhancements and Filtering-Gray level transformations, histogram equalization and specifications, pixel-domain smoothing filters – linear and order-statistics, pixel-domain sharpening filters – first and second derivative, two-dimensional DFT and its inverse, frequency domain filters – low-pass and high-pass.	03
4	Color Image Processing-Color models-RGB, YUV, HSI; Color transformations-formulation, color complements, color slicing, tone and color corrections; Color image smoothing and sharpening; Color Segmentation.	04
5	Image Segmentation- Detection of discontinuities, edge linking and boundary detection, Thresholding – global and adaptive, region-based segmentation.	04
6	Wavelets and Multi-resolution image processing- Uncertainty principles of Fourier Transform, Time-frequency localization, continuous wavelet transforms, wavelet bases and multi-resolution analysis, wavelets and Sub-band filter banks, wavelet packets.	06
7	Image Compression-Redundancy-inter-pixel and psycho-visual; Lossless compression – predictive, entropy; Lossy compression-predictive and transform coding; Discrete Cosine Transform; Still image compression standards – JPEG and JPEG-2000.	06
8	Fundamentals of Video Coding- Inter-frame redundancy, motion estimation techniques – full search, fast search strategies, forward and backward motion prediction, frame classification – I, P and B; Video sequence hierarchy – Group of pictures, frames, slices, macro-blocks and blocks; Elements of a video encoder and decoder; Video coding standards – MPEG and H.26X.	06
9	Video Segmentation- Temporal segmentation-shot boundary detection, hard-cuts and soft-cuts; spatial segmentation – motion-based; Video object detection and tracking.	06
	<b>Total</b>	<b>40</b>





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IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

<b>Text/Reference Books:</b>	
<b>1.</b>	R.C. Gonzalez and R.E. Woods, Digital Image Processing, Second Edition, Pearson Education 3rd edition 2008
<b>2</b>	R.C. Gonzalez, R.E. Woods and S.L. Eddins, Digital Image Processing using Matlab, McGraw Hill, 2 <sup>nd</sup> Edition
<b>3.</b>	Anil Kumar Jain, Fundamentals of Digital Image Processing, Prentice Hall of India. 2 <sup>nd</sup> edition 2004
<b>4.</b>	Murat Tekalp , Digital Video Processing" Prentice Hall, 2nd edition 2015



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

### SEC5-13: Adaptive Signal Processing (program elective-4)

Credit: 3

Max. Marks: 100(IA:30, ETE:70)

3L+0T+0P

End Term Exam: 3 Hours

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	General concept of adaptive filtering and estimation, applications and motivation, Review of probability, random variables and stationary random processes, Correlation structures, properties of correlation matrices.	08
3	Optimal FIR (Wiener) filter, Method of steepest descent, extension to complex valued The LMS algorithm (real, complex), convergence analysis, weight error correlation matrix, excess mean square error and mis-adjustment Variants of the LMS algorithm: the sign LMS family, normalized LMS algorithm, block LMS and FFT based realization, frequency domain adaptive filters, Sub-band adaptive filtering.	07
4	Signal space concepts - introduction to finite dimensional vector space theory, subspace, basis, dimension, linear operators, rank and nullity, inner product space, orthogonality, Gram-Schmidt orthogonalization, concepts of orthogonal projection, orthogonal decomposition of vector spaces.	08
5	Vector space of random variables, correlation as inner product, forward and backward projections, Stochastic lattice filters, recursive updating of forward and backward prediction errors, relationship with AR modeling, joint process estimator, gradient adaptive lattice.	08
6	Introduction to recursive least squares (RLS), vector space formulation of RLS estimation, pseudo-inverse of a matrix, time updating of inner products, development of RLS lattice filters, RLS transversal adaptive filters. Advanced topics: affine projection and subspace based adaptive filters, partial update algorithms, QR decomposition and systolic array.	08
	<b>Total</b>	<b>40</b>

#### Text/Reference Books:

1.	S. Haykin, Adaptive filter theory, Prentice Hall, 1986.
2.	C.Widrow and S.D. Stearns, Adaptive signal processing, Prentice Hall, 1984.





# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## SEC4-21: IOT Lab

Credit: 1  
OL+OT+2P

Max. Marks: 100 (IA:60, ETE:40)

LIST OF PRACTICALS	
1.	Study the fundamental of IOT softwares and components.
2.	Familiarization with Arduino/Raspberry Pi and perform necessary software installation.
3.	To interface LED/Buzzer with Arduino/Raspberry Pi and write a program to turn ON LED for 1 sec after every 2 seconds.
4.	To interface Push button/Digital sensor (IR/LDR) with Arduino/Raspberry Pi and write a program to turn ON LED when push button is pressed or at sensor detection.
5.	To interface DHT11 sensor with Arduino/Raspberry Pi and write a program to print temperature and humidity readings.
6.	To interface motor using relay with Arduino/Raspberry Pi and write a program to turn ON motor when push button is pressed.
7.	To interface OLED with Arduino/Raspberry Pi and write a program to print temperature and humidity readings on it.
8.	To interface Bluetooth with Arduino/Raspberry Pi and write a program to send sensor data to smartphone using Bluetooth.
9.	To interface Bluetooth with Arduino/Raspberry Pi and write a program to turn LED ON/OFF when '1'/'0' is received from smartphone using Bluetooth.
10.	Write a program on Arduino/Raspberry Pi to upload temperature and humidity data to thingspeak cloud.
11.	Write a program on Arduino/Raspberry Pi to retrieve temperature and humidity data from thingspeak cloud.
12.	To install MySQL database on Raspberry Pi and perform basic SQL queries.
13.	Write a program to create UDP server on Arduino/Raspberry Pi and respond with humidity data to UDP client when requested.
14.	Write a program to create TCP server on Arduino/Raspberry Pi and respond with humidity data to TCP client when requested.

LIST OF SUGGESTED BOOKS:	
1.	Vijay Madiseti, Arshdeep Bahga, Internet of Things, "A Hands on Approach", University Press.
2.	Dr. SRN Reddy, Rachit Thukral and Manasi Mishra, "Introduction to Internet of Things: A practical Approach", ETI Labs.
3.	Pethuru Raj and Anupama C. Raman, "The Internet of Things: Enabling Technologies, Platforms, and Use Cases", CRC Press
4.	Jeeva Jose, "Internet of Things", Khanna Publishing House, Delhi
5.	Adrian McEwen, "Designing the Internet of Things", Wiley
6.	Raj Kamal, "Internet of Things: Architecture and Design", McGraw Hill



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Scheme & Syllabus

IV Year- VII & VIII Semester: B. Tech. (Electronics & Communication Engineering)

## SEC4- 22 Skill Development Lab

Credit: 1  
0L+0T+2P

Max. Marks : 100 (IA:60,ETE:40)

Part A: Training	
SN	Contents
1	Introduction: Objective, scope and outcome of the lab.
	<b>Every student has to learn any two software from the following list, with consultation of their lab in charge. Students may get online certification or is advised to learn these from available freeware. Students may register online training courses from institutes of repute i.e. IITs/NITs/AICTE/MHRD, etc. Industrial experts /professional may be deputed to train the students in department.</b>
1	Network simulator (NS <sub>2</sub> )
2	Lab view
3	Software for Robotics/Artificial intelligence (AI) /machine learning
4	Java
5	Python

PART B: Implementation	
SN	Contents
1	<b>Student has to complete any one assignment with detailed project report based on the software/tool learn in part A.</b>
2	Student can select any Social engineering project: Any problem of the society can be taken which can be solved with the help of electronics engineering software and gadgets.
3	Student can select Startup for innovation/entrepreneurship.
4	Engineering solution of any Industrial problem. Sufficient number of such problem may be identified by the department from nearby industry and may be given to the student for innovative solutions under guidance of faculty.
	<b>This lab may be evaluated by an external examiner from industry along with internal faculty.</b>