Swami Keshvanand Institute of Technology,
Management & Gramothan, Jaipur

A

Report

on

One Week

Faculty Development Program (FDP)

on

"Application of Artificial Intelligence in VLSI and Communication Technology"

(AAIVCT-2022)

7th – 11th March 2022

Organized by:

Department of Electronics & Communication Engineering

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1. Objective of the event

The major objective of this FDP is to address faculty members and research scholars of different institutes about recent advancement and technical challenges in the field of communication and VLSI using Artificial intelligence (AI).

AI is one of the most rising masterworks in the world of technology and innovation that helps to make electronics more reliable and easy to use. It has also gained significant attention due to its widespread application in various domains including VLSI and communication technology.

2. Budget Approval

Swami Keshvanand Institute of Technology, Management & Gramothan, Jaipur

Department of Electronics and Communication Engineering

Date: 16 Aug 2021

Note

Following proposals of online Conference/ FDP/ workshops are being submitted for your kind approval.

S.N.	Proposed Title:	Event Type	Name of Coordinator(s)	Tentative dates	Duration	Budget
1	International conference on Advancements in Nano and Communication Technologies	International Conference	Prof. Mukesh Arora/ Dr. PK Jain	24-26 February 2022	3 days	25000
2	Signal processing using Python & MATLAB	Workshop	Dr. Monika Mathur , Ms. Kiran Rathi, Mr. Harshal Nigam , Dr. Shubhi Jain,	17-21 January 2022	1 week	30000
3	Emerging Tools and Techniques in VLSI, MEMS and MOEMS	Workshop	Ms. Manju Choudhary, Nantrata Saxena, Dr. Rakhsar Zafar, Dr. Swati Arora	22-27 November, 2021	1 week	30000
4	Student Workshop on IoT with Machine Learning & Artificial Intelligence	Workshop	Dr. Praveen Kumar Jain, Dr. Rakhsar Zafar, Mr. Ankit Agarwal, Ms. Pooja Choudhary	7-17 February 2022	2 week	56000
5	Application of VLSI in artificial intelligence	FDP	Mr. Vikas Pathak, Neera Jain, Rahul Pandey, Abhinandan Jain	13-18 December, 2021	1 week	30000
6	NBA Accreditation and Teaching - Learning Process in Engineering	FDP	Suman Sharma, Rajni Idiwal, Kiran Rathi, Mamta Jain	15-19 November, 2021	1 week	30000
7	Guest Lecture Series	-	-	Monthly	-	-

Prof. Mukesh Arora 16.8.21
HoD, ECE

Principal, SKIT

3. Event Brochure

Swami Keshvanand Institute of Technology, Management & Gramothan, Jaipur

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

organizing

One Week
Faculty Development Programme
on

APPLICATION OF ARTIFICIAL INTELLIGENCE IN VLSI AND COMMUNICATION TECHNOLOGY

(AAIVCT-2022)

07–11 MARCH, 2022

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PATRON
Sh. Surja Ram Meel, Chairman, SKIT

ADVISORY

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The major objective of this FDP is to address faculty members and research scholars of different institutes about recent advancement and technical challenges in the field of communication and VLSI using Artificial intelligence (AI). AI is one of the most rising masterworks in the world of technology and innovation that helps to make electronics more reliable and easy to use. It has also gained significant attention due to its widespread application in various domains including VLSI and communication technology.

CONTENTS OF THE FDP

- Ø Introduction to AI and its applications in real world
- Ø AI techniques in VLSI design automation
- Ø AI in Integrated Photonics
- Ø Machine Learning and Deep Learning
- Ø AI methods in Optical Networks
- Ø AI enabled intelligent 6G network
- Ø Hardware design methodology for AI
- Ø Machine Learning For Optical Communication System
- Ø Hand's on session on Lumerical FDTD and Xilinx Vivado EDA tool

RESOURCE PERSONS

The various sessions of this FDP will be taken by the eminent faculty from IITs, NITs, IIITs and research institutions.

REGISTRATION

Registration is open to students, faculty, industry persons, Scientists, Technologists, doctoral and postgraduate students, researchers, and industry personnel across India to augment them with state-of-the-art skills and knowledge in the area of VLSI, COMMUNICATION TECHNOLOGY AND ARTIFICAIAL INTELLIGENCE.

- Participants will be admitted on a first-come-first-served basis. There is no registration fee.
- The registration is to be done by filling the Google form at link: https://forms.gle/zkCUuxbIJx1o9kJs6
- After the confirmation of registration, participants need to join the Whatsapp group with this link: https://chat.whatsapp.com/F0qfC7IQzi9AzKhdoNewik
- E-certificate will be provided to the participants who have attended the FDP with minimum 80% attendance and score minimum 60% marks in the quiz including feedback of FDP.

4. Schedule of the Event

Swami Keshvanand Institute of Technology, Management & Gramothan,
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One Week Faculty Development Programme on
"Application of Artificial Intelligence on VLSI and Communication Technology"
(AAIVCT-2022)
(7th to 11th March, 2022)

Monday, March 7, 2022	**Inaugural Session:10:00 - 10:30** **Chief Guest: Prof. Ghanshyam Singh** Professor, Department of Electrical and Electronic Engineering **University of Johannesburg, South Africa** **Expert Session : 10.30-11.30 AM** **Dr. Debabrata Sikdar** Assistant Professor, Department of Electronics and Electrical Engineering, **IIT Guwahati** **Title: Promises of Integrated Photonics Devices Technology with AI**	**Expert Session: 12:30 - 2:00 PM** **Prof. Ghanshyam Singh** Professor, Department of Electrical and Electronic Engineering **University of Johannesburg, South Africa** **Title: Role of AI in 6G applications.**
Date	**Morning Session: 9.30 - 11:00 AM**	**Evening Session: 12:30 - 2:00 PM**
Tuesday, March 8, 2022	**Dr. Binod Kumar** Assistant Professor, Department of Electrical Engineering, **IIT Jodhpur** **Title: Hardware Design Methodologies for Artificial Intelligence**	**Dr. Jai Narayan Tripathi** Assistant Professor, Department of Electrical Engineering, **IIT Jodhpur,** **Title: Computational Intelligence for Designing VLSI Systems**
Wednesday, March 9, 2022	**Dr. Kuldeep Singh** Assistant Professor, Dept. of Electronics & Comm. Engineering, **MNIT, Jaipur** **Title: Machine Learning for Cooperative Spectrum Sensing**	**Dr. Vikas Pathak,** (Associate Professor Department of Electronics and Communication Engineering, **SKIT,Jaipur)** **Mr. Ankur Sangal** (Lead Application Engineer, **CoreEL Technologies**, New Delhi) **Title: Xilinx Vivdo Design flow using 7 series FPGA Architecture**
Thursday, March 10, 2022	**Dr. Satyasai Jagannath Nanda** Assistant Professor, Dept. of Electronics & Comm. Engineering, **MNIT, Jaipur** **Title: Nature inspired optimization techniques and their application in communication engineering**	**Dr. Gaurav Kumar Bharti** Electrical Engineering Department, **Chandigarh University**, Chandigarh **Title: Hands-on session on "Simulation of different photonic sensor using FDTD platform**
Friday, March 11, 2022	**Dr. Rukhsar Zafar**, Associate Professor Dept. of Electronics & Comm., **SKIT,Jaipur** **Title: Intelligent Nano Plasmonic**	**Dr. Ajay Khunteta**, Associate Professor Department of Electronics and Communication Engineering, **RTU, Kota** **Title: 5G communication and Role of AI in 5G** **Valedictory Session**

COORDINATORS:
Dr. Vikas Pathak, ECE, SKIT
Mr. Neeraj Jain, ECE, SKIT
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Registration Link: https://forms.gle/zkCUuxb1Jx1o9kJs6
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5. List of Invited Guests and Speakers

S. No	Guest/Speakers	Name and Affiliation
1.	Chief guest of Inauguration	**Prof. Ghanshyam Singh** Professor, Department of Electrical and Electronic Engineering **University of Johannesburg, South Africa**
2.	Invited Speaker 1	**Dr. Debabrata Sikdar** Assistant Professor, Department of Electronics and Electrical Engineering, **IIT Guwahati**
3.	Invited Speaker 2	**Prof. Ghanshyam Singh** Professor, Department of Electrical and Electronic Engineering **University of Johannesburg, South Africa**
4.	Invited Speaker 3	**Dr. Binod Kumar** Assistant Professor, Department of Electrical Engineering, **IIT Jodhpur**
5.	Invited Speaker 4	**Dr. Jai Narayan Tripathi** Assistant Professor, Department of Electrical Engineering, **IIT Jodhpur**
6.	Invited Speaker 5	**Dr. Kuldeep Singh** Assistant Professor, Dept. of Electronics & Comm. Engineering, **MNIT, Jaipur**
7.	Invited Speaker 6	**Dr. Vikas Pathak,** (Associate Professor Department of Electronics and Communication Engineering, **SKIT,Jaipur)** **Mr. Ankur Sangal** (Lead Application Engineer, **CoreEL Technologies**, New Delhi)
8.	Invited Speaker 7	**Dr. Satyasai Jagannath Nanda** Assistant Professor, Dept. of Electronics & Comm. Engineering, **MNIT, Jaipur**
9.	Invited Speaker 8	**Dr. Gaurav Kumar Bharti** Electrical Engineering Department, **Chandigarh University**, Chandigarh
10.	Invited Speaker 9	**Dr. Rukhsar Zafar**, Associate Professor Dept. of Electronics & Comm., **SKIT,Jaipur**
11.	Invited Speaker 10	**Dr. Ajay Khunteta**, Associate Professor Department of Electronics and Communication Engineering, **RTU, Kota**

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154.	Anne Susan Georgena S	Sri Ramakrishna Institute of Technology	annesusan.sh@srit.org
155.	AMIT SHARMA	ACEIT	amitjaipur@gmail.com
156.	Purushottam Shukleshwar Chavan	K.K.Wagh Pokytechnic,Nashik	pschavan@kkwagh.edu.in
157.	Kailas Chandrakant Deshmukh	Nanasaheb Y N Chavan Arts,Science and Commerce College	kcdmeg@rediffmail.com
158.	Nitika Arora	MMICT&BM	aroranitika26@gmail.com
159.	Pradeep Kumar Jangid	Arya College of Engineering and IT	pksalunia83@gmail.com
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161.	Ramesh Maradi	University of Agricultural Sciences, Dharwad	maradiramesh2011@gmail.com

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166.	Tripti Dua	JECRC	tripti.dua05@gmail.com
167.	Anju Rajput	JECRC	anju.rajput1409@gmail.com
168.	Santosh Jangid	B K Birla Institute of Engineering & Technology,Pilani(Raj.)	jangid.santosh@gmail.com
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170.	Anshuman Singh	Noida Institute of Engineering and Technology, Greater Noida	anshuman1317@gmail.com
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176.	RAJKUMAR GUPTA	amity university rajasthan	guptarajkumar.19@gmail.com
177.	Vinod Kumar Adigopula	Madanapalle Institute of Technology & Science	vinodkumarmtech@gmail.com
178.	PUSHPA GOTHWAL	AMITY SCHOOL OF ENGINEERING AND TECHNOLOGYAmity university Rajasthan jaipur	pushpa.gothwal@gmail.com
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182.	Dr VISHNU KUMAR SAXENA	SCHOOL OF ENGINEERING & TECHNOLOGY	vksaxena2002@yahoo.co.in

183.	Dr Neeraj Gupta	Amity University Haryana	neerajsingla007@gmail.com
184.	Mahaboob Basha Qureshi	Pratap Institute of Technology and Science	qureshi.mahaboob@gmail.com
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186.	Sudhanshu Singh	Amity University Rajasthan	singhs1183@gmail.com
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193.	Ankur Saxena	Arya Institute of engineering & Technology	ankur_saxena6481@yahoo.com
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197.	Rakhi Mutha	Poornima Institute of Engineerign and Technology, Jaipur, Rajasthan	doctor.rakhi.mutha@gmail.com
198.	DHARMAIAH GURRAM	Narasaraopeta Engineering College	raodg42@gmail.com
199.	Manusankar C	Sree Sankara Vidyapeetom College	manusankarc@gmail.com
200.	Javid Ahmad Hajam	Central University of kashmir	javidspecial2531@gmail.com
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210.	Ritu Bhoi	Bhilai institute of technology Raipur	ritu.bhoi@bitraipur.ac.in
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216.	Tanvi Tyagi	JIMS JEMTEC College, GGSIP Uinversity	tanvityagi.77@gmail.com
217.	Diwakar Dubey	IGNTU Central University	diwakardubey728@gmail.com
218.	Anne Susan Georgena S	Sri Ramakrishna Institute of Technology	annesusan.sh@srit.org
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221.	MOHIT KUMAR PANDEY	Rajshree Institute of Management & Technology, Bareilly	mohitforme@gmail.com
222.	Dr PURVEE BHARDWAJ	Rabindranath Tagore University Bhopal	purveebhardwaj@gmail.com

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232.	Vivek Sharma	SKIT	viveksha1985@gmail.com
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234.	Ajeet Singh	Hindustan College of science & Technology Mathura	ajeetsinghkain@gmail.com
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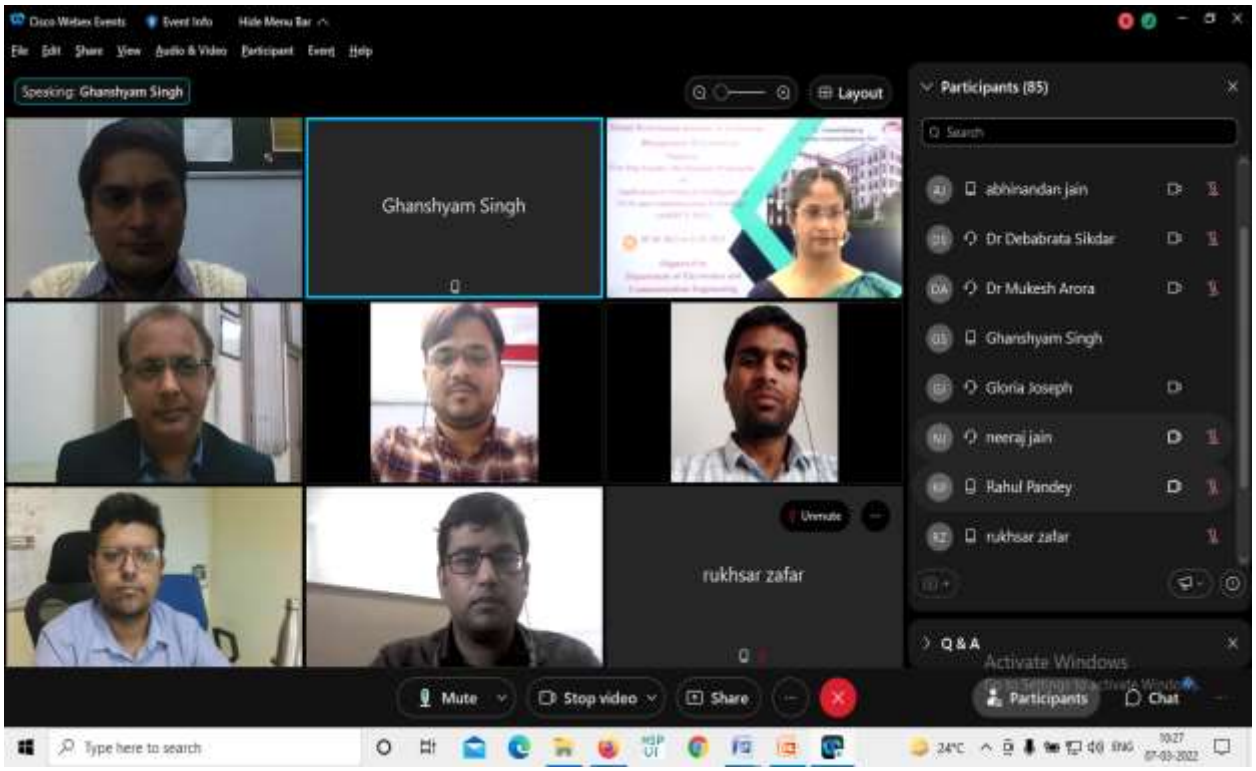
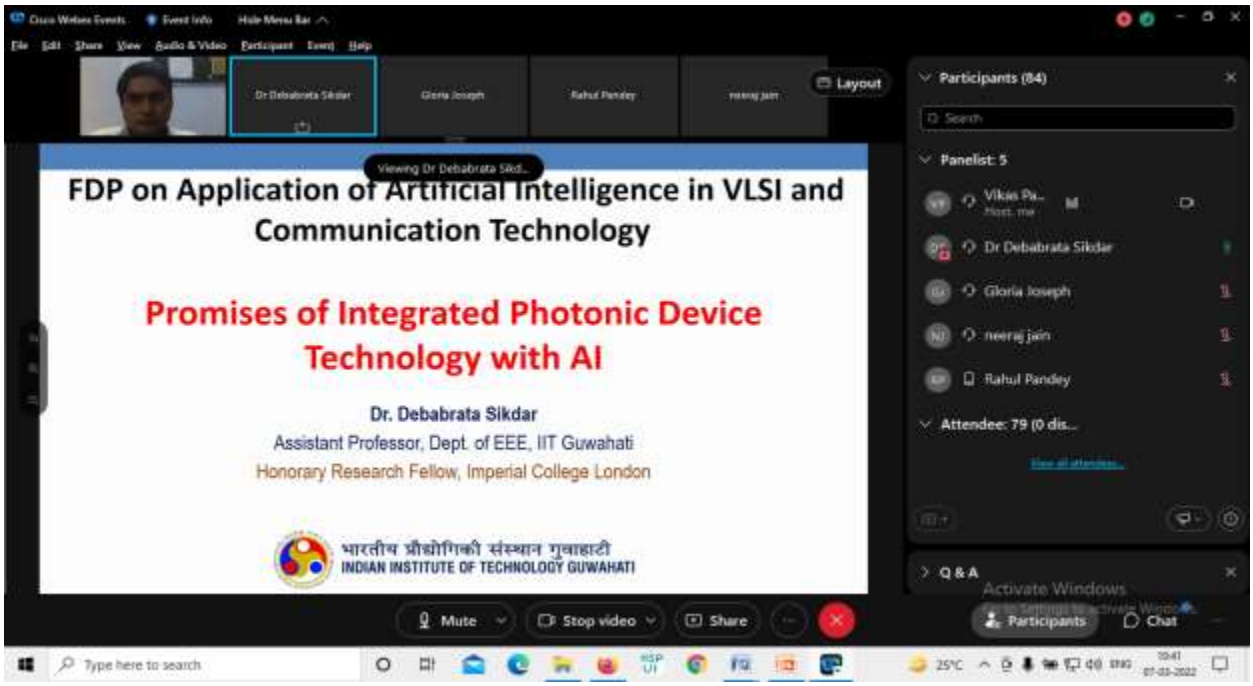
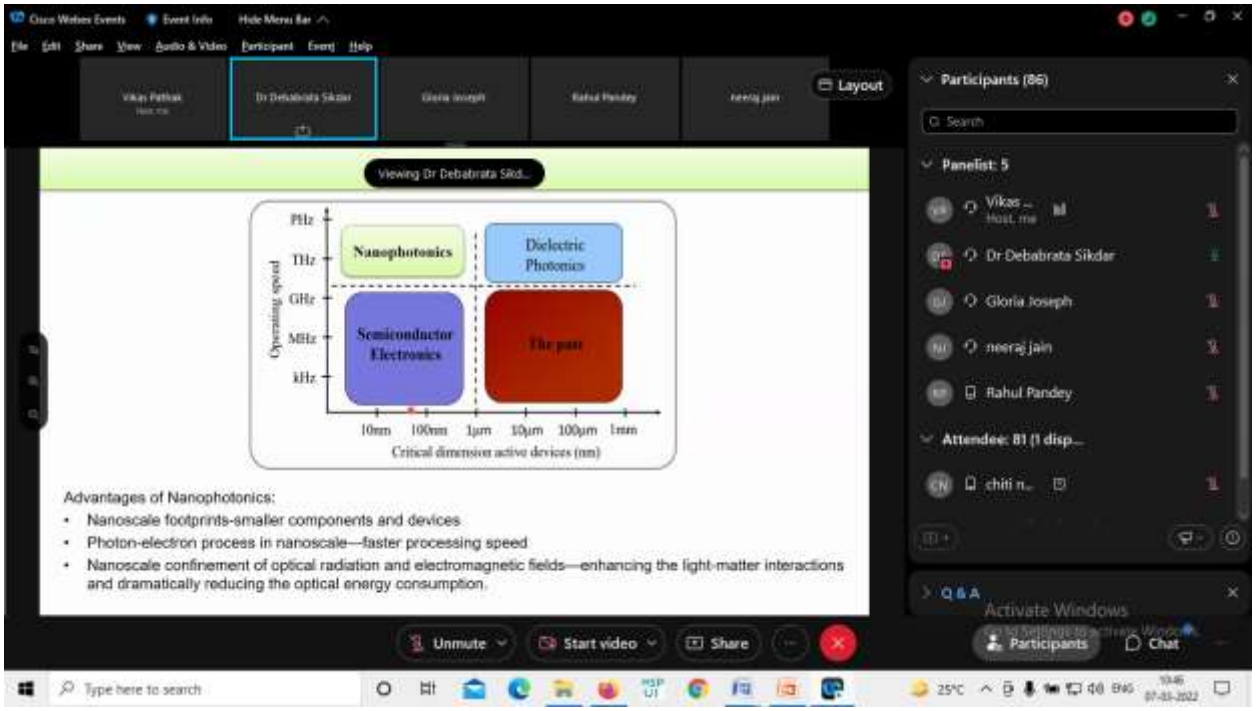
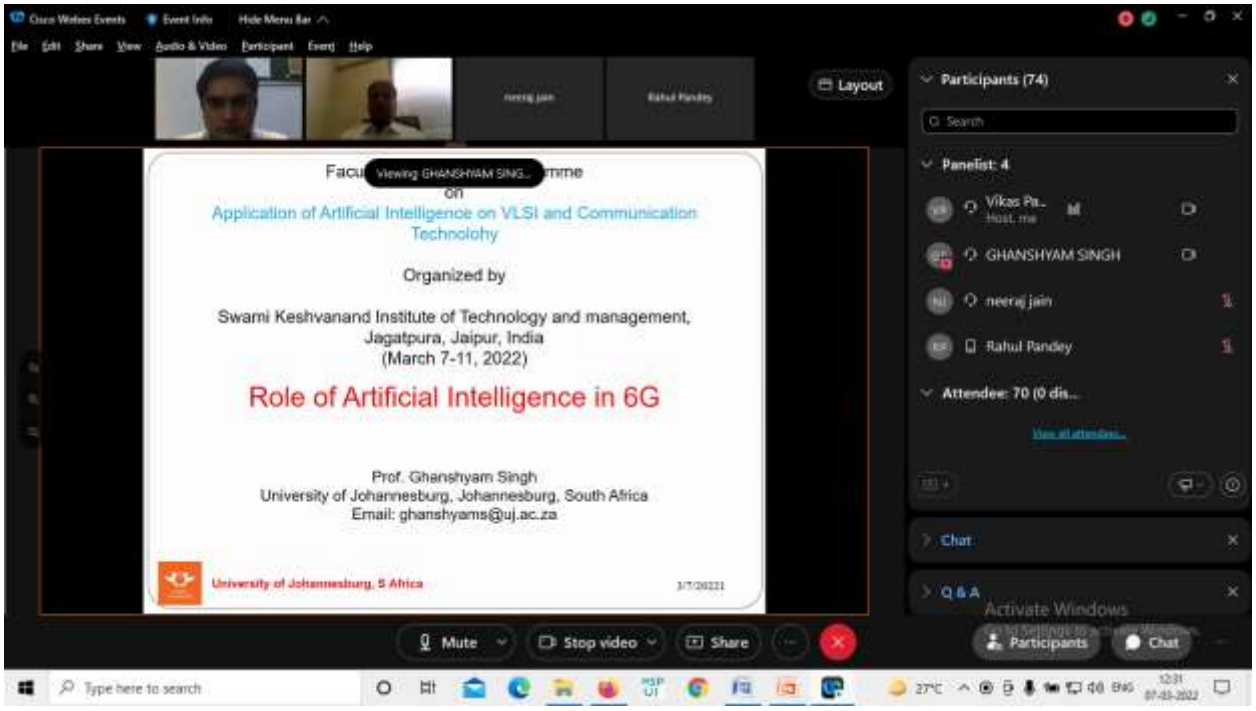
7. Screenshots of the Event

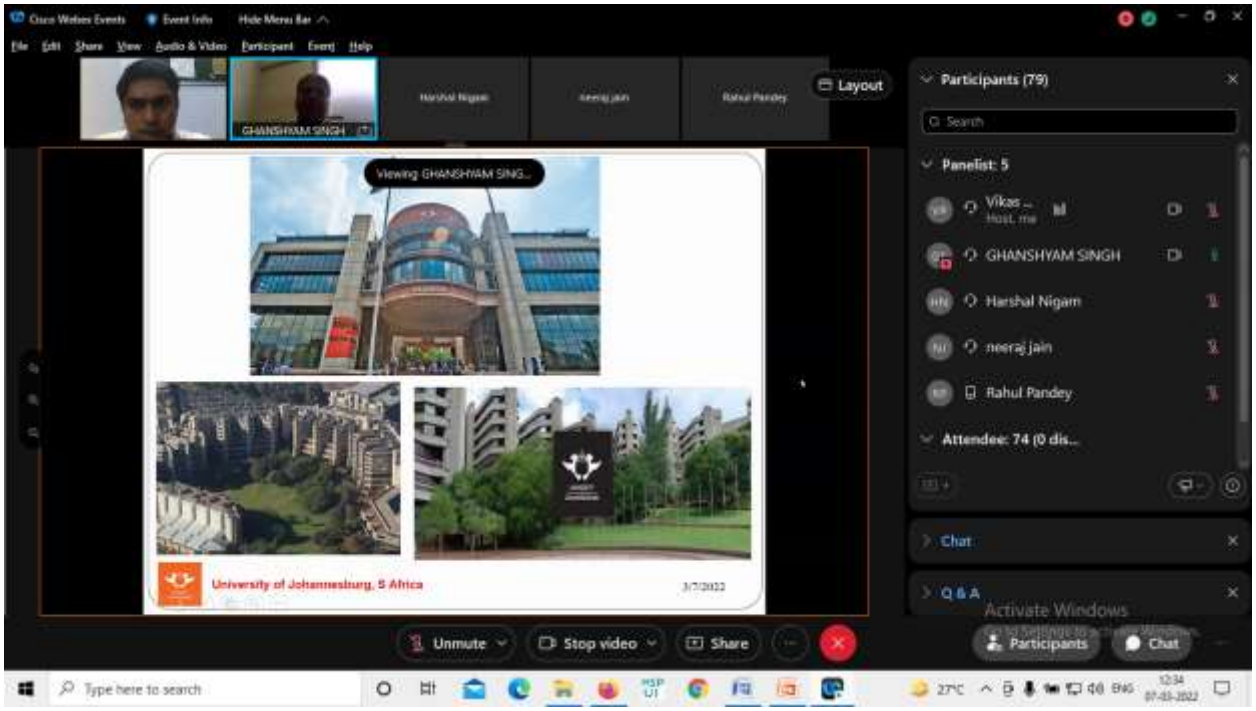
Day-1 Photos

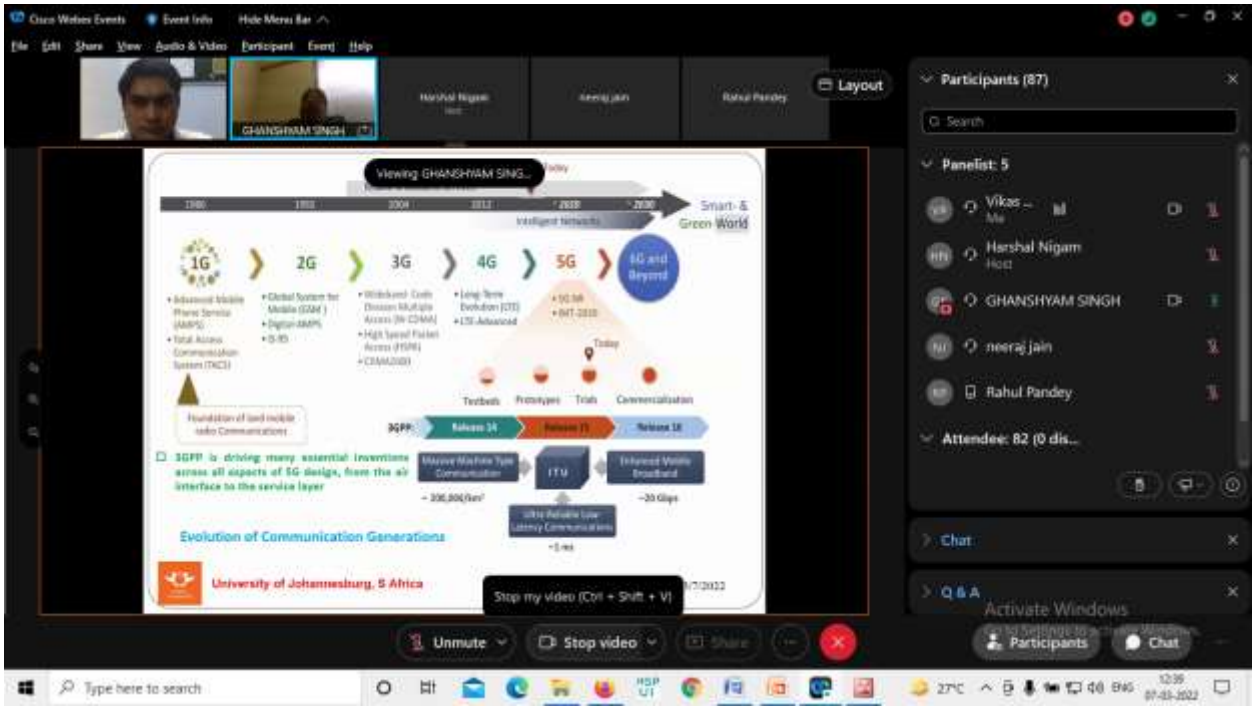




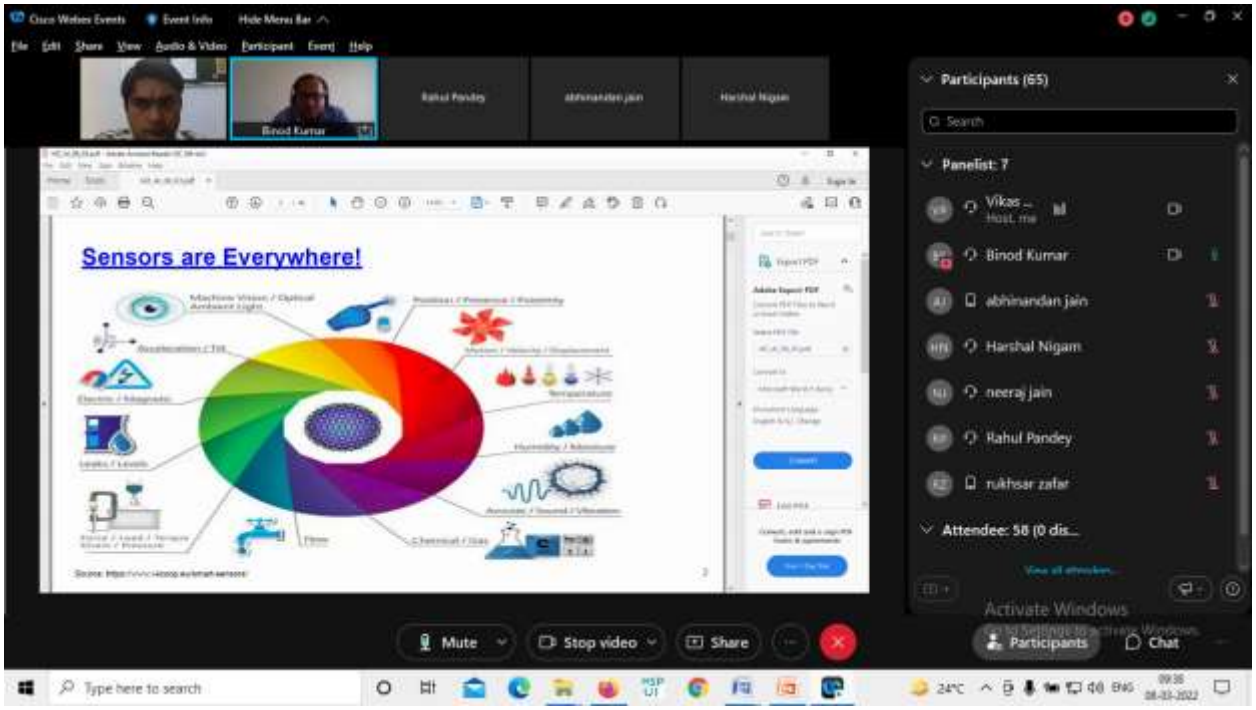

Viewing Dr Debabrata Sikd...
Nanophotonics
Dielectric Photonics
Semiconductor Electronics
The past
Operating speed
Critical dimension active devices (nm)
Advantages of Nanophotonics:
• Nanoscale footprints-smaller components and devices
• Photon-electron process in nanoscale—faster processing speed
• Nanoscale confinement of optical radiation and electromagnetic fields—enhancing the light-matter interactions and dramatically reducing the optical energy consumption.
Participants (86)
Panelist: 5
Dr Debabrata Sikdar
Gloria Joseph
neeraj jain
Rahul Pandey
Attendee: 81 (1 disp...
Unmute
Start video
Share
Participants
Chat

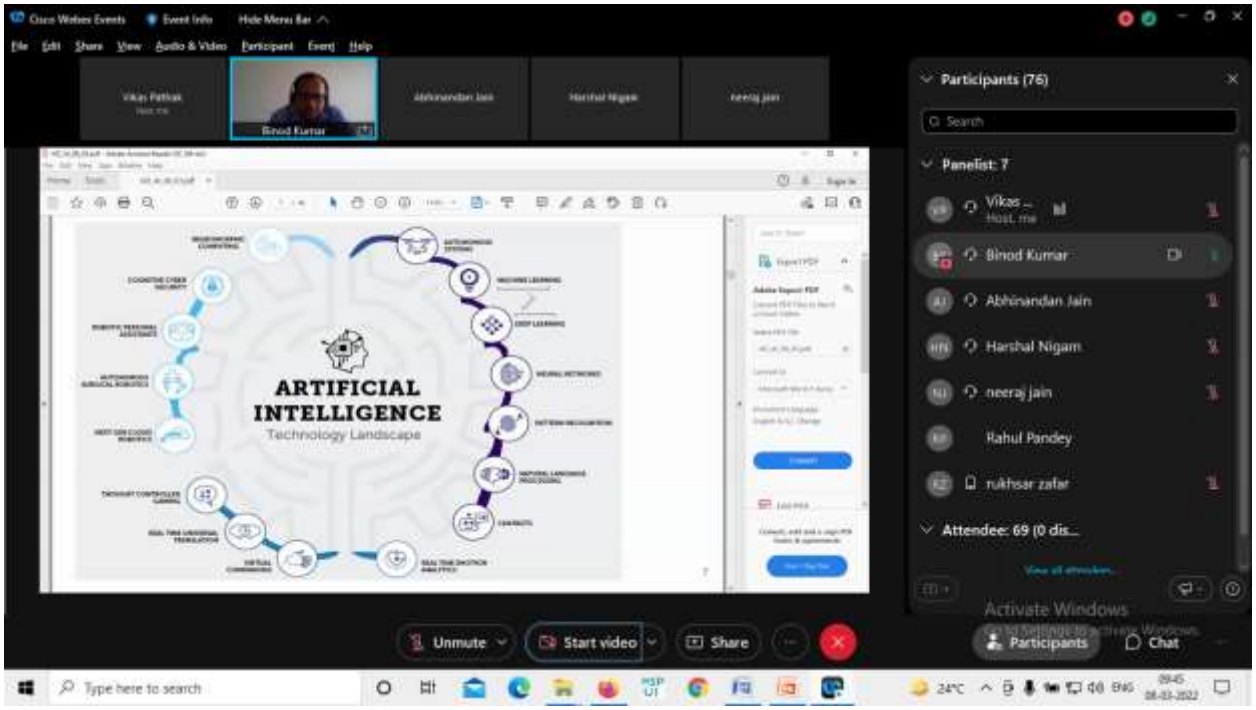

Viewing GHANSHYAM SING...
Application of Artificial Intelligence on VLSI and Communication Technolohy
Organized by
Swami Keshvanand Institute of Technology and management, Jagatpura, Jaipur, India
(March 7-11, 2022)
Role of Artificial Intelligence in 6G
Prof. Ghanshyam Singh
University of Johannesburg, Johannesburg, South Africa
Email: ghanshyams@uj.ac.za
University of Johannesburg, S Africa
Participants (74)
Panelist: 4
GHANSHYAM SINGH
neeraj jain
Rahul Pandey
Attendee: 70 (0 dis...
Mute
Stop video
Share
Participants
Chat

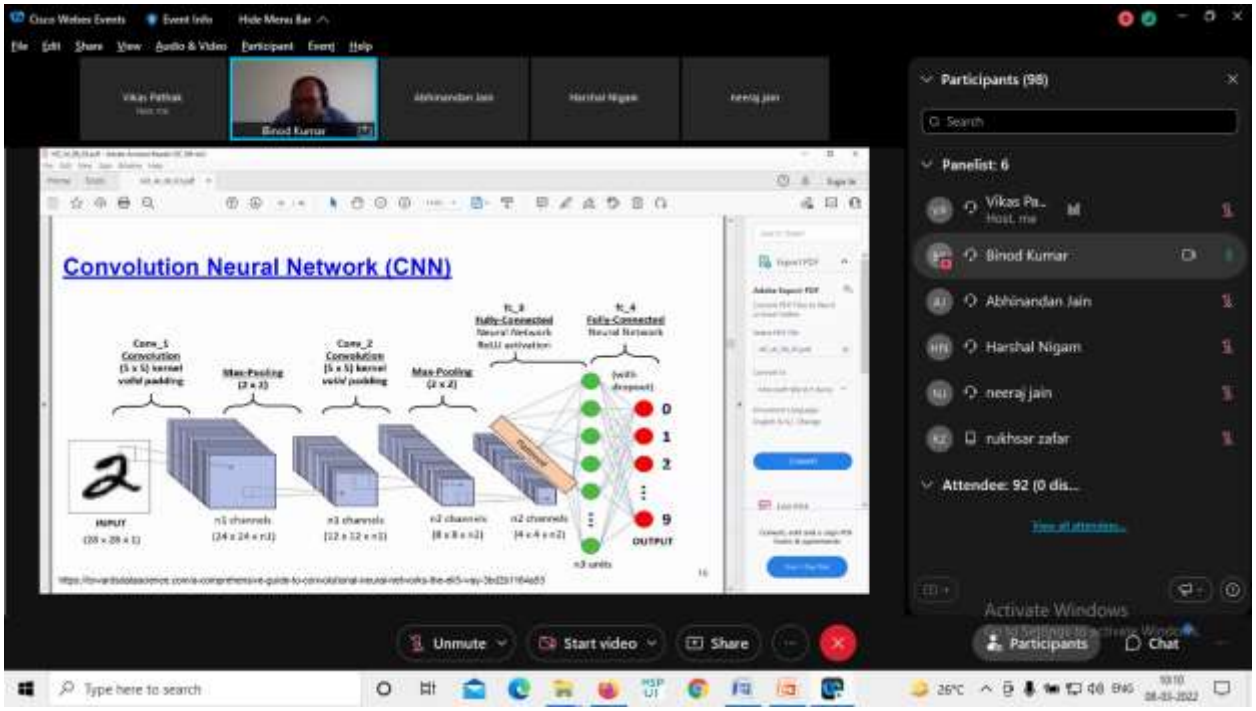

Viewing GHANSHYAM SING...
University of Johannesburg, S Africa
Participants (79)
Panelist: 5
Vikas ...
GHANSHYAM SINGH
Harshal Nigam
neeraj jain
Rahul Pandey
Attendee: 74 (0 dis...
Chat
Q & A
Unmute
Stop video
Share
Participants
Chat

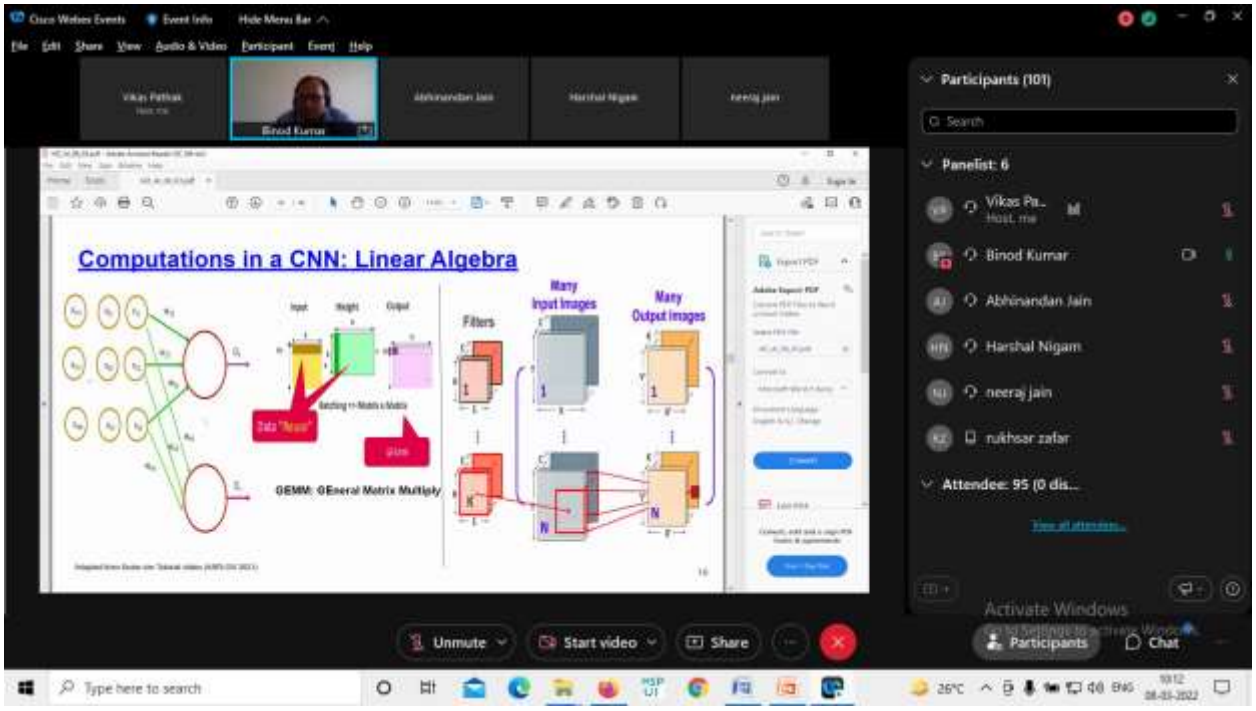

Viewing GHANSHYAM SING...
1G
2G
3G
4G
5G
4G and Beyond
Smart- & Green World
Evolution of Communication Generations
University of Johannesburg, S Africa
Participants (87)
Panelist: 5
Vikas ...
Harshal Nigam
GHANSHYAM SINGH
neeraj jain
Rahul Pandey
Attendee: 82 (0 dis...
Chat
Q & A
Unmute
Stop video
Participants
Chat

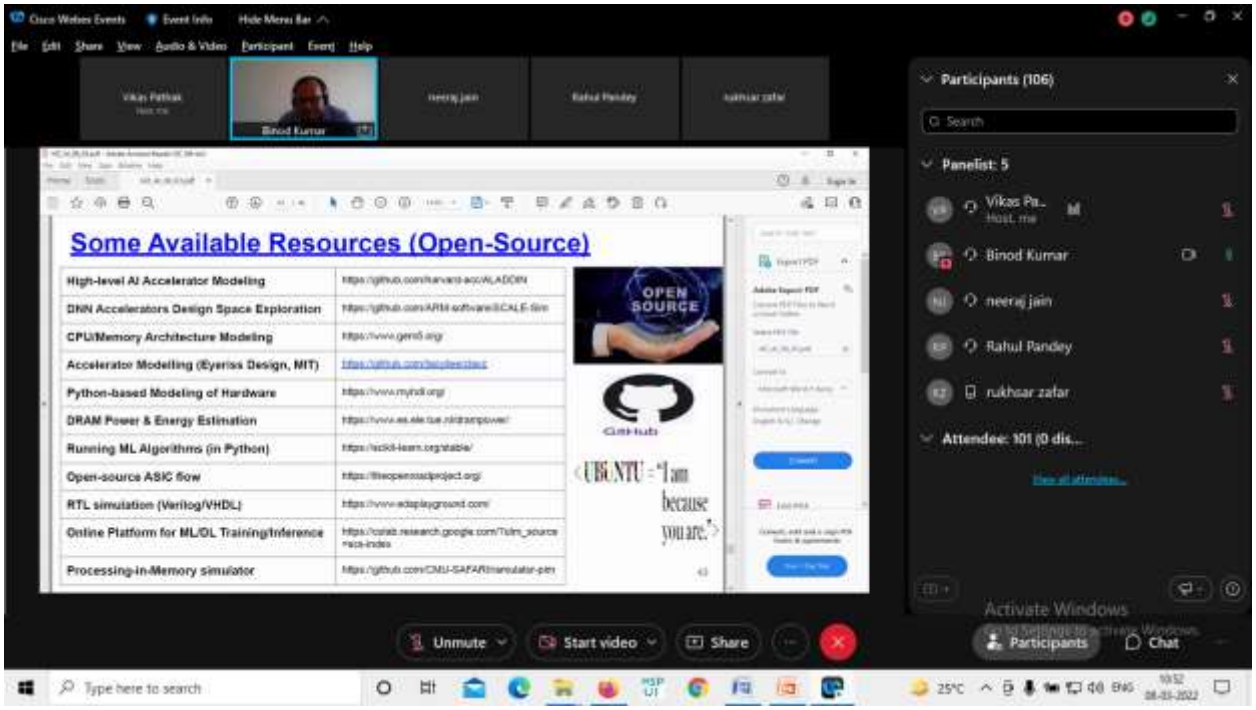
Day-2

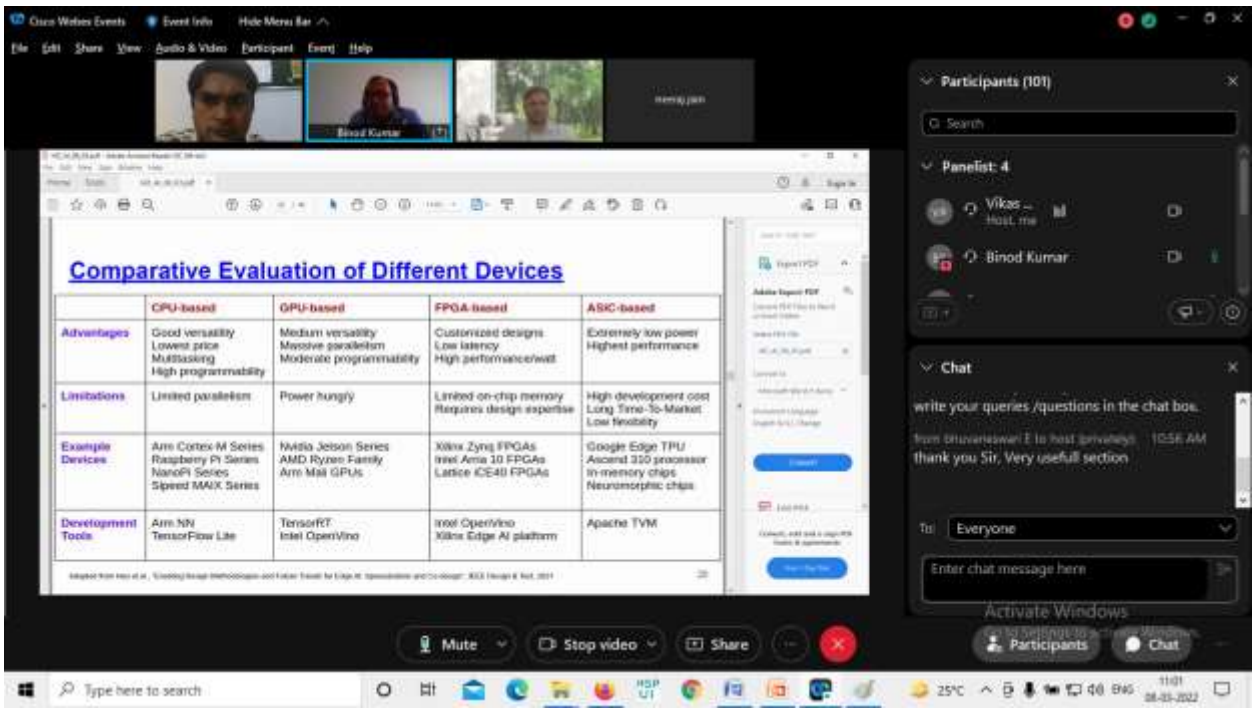

Sensors are Everywhere!
Participants (65)
Panelist: 7
Vikas ...
Binod Kumar
abhinandan jain
Harshal Nigam
neeraj jain
Rahul Pandey
rukhsar zafar
Attendee: 58 (0 dis...
Mute
Stop video
Share
Participants
Chat
Type here to search


ARTIFICIAL INTELLIGENCE
Technology Landscape
Participants (76)
Panelist: 7
Vikas ...
Binod Kumar
Abhinandan Jain
Harshal Nigam
neeraj jain
Rahul Pandey
rukhsar zafar
Attendee: 69 (0 dis...
Unmute
Start video
Share
Participants
Chat
Type here to search


Convolution Neural Network (CNN)
Participants (98)
Panelist: 6
Vikas Pa.. Host, me
Binod Kumar
Abhinandan Jain
Harshal Nigam
neeraj jain
rukhsar zafar
Attendee: 92 (0 dis...
Unmute
Start video
Share
Participants
Chat
Activate Windows


Computations in a CNN: Linear Algebra
Many Input Images
Many Output Images
Filters
GEMM: GEneral Matrix Multiply
Participants (101)
Panelist: 6
Vikas Pa.. Host, me
Binod Kumar
Abhinandan Jain
Harshal Nigam
neeraj jain
rukhsar zafar
Attendee: 95 (0 dis...
Unmute
Start video
Share
Participants
Chat
Activate Windows


Some Available Resources (Open-Source)
High-level AI Accelerator Modeling
DNN Accelerators Design Space Exploration
CPU/Memory Architecture Modeling
Accelerator Modelling (Eyeriss Design, MIT)
Python-based Modeling of Hardware
DRAM Power & Energy Estimation
Running ML Algorithms (in Python)
Open-source ASIC flow
RTL simulation (Verilog/VHDL)
Online Platform for ML/DL Training/Inference
Processing-in-Memory simulator
OPEN SOURCE
GitHub
UBUNTU - "I am because you are."
Participants (106)
Panelist: 5
Vikas Pa... Host, me
Binod Kumar
neeraj jain
Rahul Pandey
rukhsar zafar
Attendee: 101 (0 dis...
Unmute
Start video
Share
Participants
Chat
Activate Windows


Comparative Evaluation of Different Devices
CPU-based
GPU-based
FPGA-based
ASIC-based
Advantages
Limitations
Example Devices
Development Tools
Participants (101)
Panelist: 4
Vikas ... Host, me
Binod Kumar
Chat
write your queries /questions in the chat box.
thank you Sir, Very usefull section
To: Everyone
Enter chat message here
Mute
Stop video
Share
Participants
Chat
Activate Windows


Computational Intelligence for Designing Integrated Circuits and Systems
Dr. Jai Narayan Tripathi
IIT Jodhpur, India.
'Faculty Development Programme' on
Application of Artificial Intelligence in VLSI and Communication Technology
(AAIVCT-2022), SKIT, Jaipur,
8 March 2022.
Participants (72)
Panelist: 7
Dr Jai Narayan Tripathi
Abhinandan Jain
ankit agarwal
neeraj jain
neeraj jain
Suman Sharma
Attendee: 65 (0 dis...
Mute
Stop video
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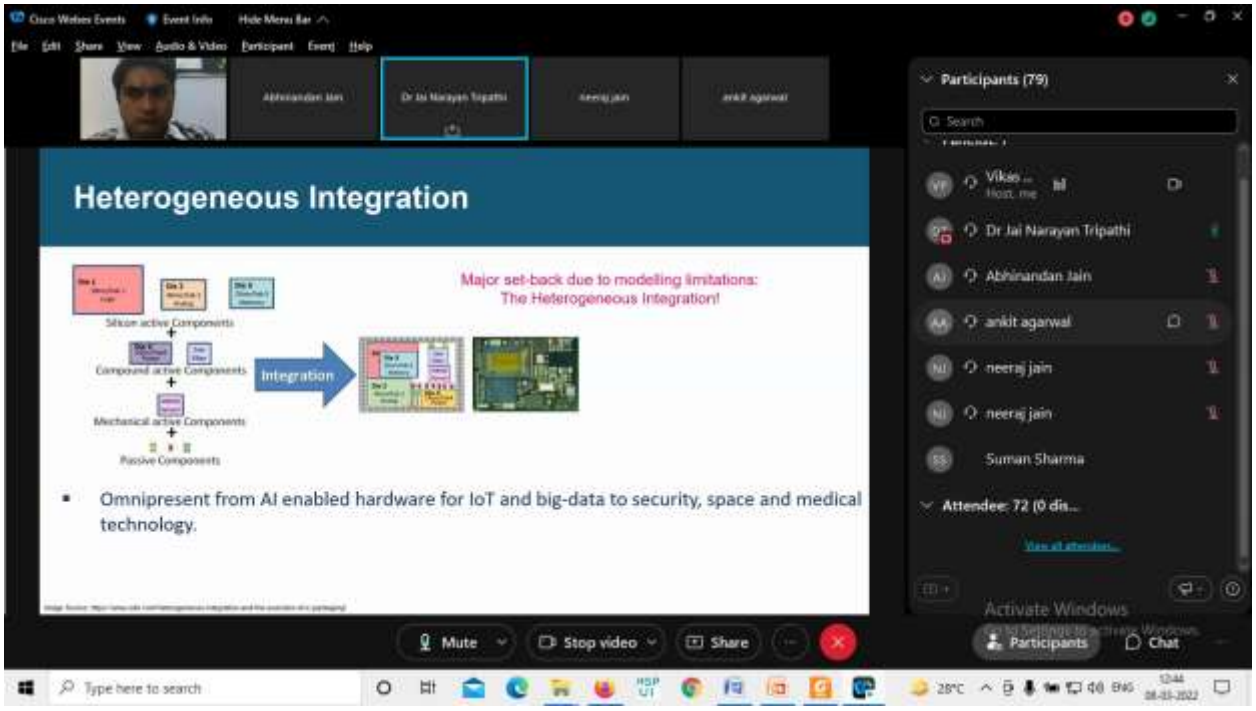
Participants
Chat


Heterogeneous Integration
Major set-back due to modelling limitations:
The Heterogeneous Integration!
Silicon active Components
Compound active Components
Mechanical active Components
Passive Components
Integration
Omnipresent from AI enabled hardware for IoT and big-data to security, space and medical technology.
Participants (79)
Dr Jai Narayan Tripathi
Abhinandan Jain
ankit agarwal
neeraj jain
neeraj jain
Suman Sharma
Attendee: 72 (0 dis...
Mute
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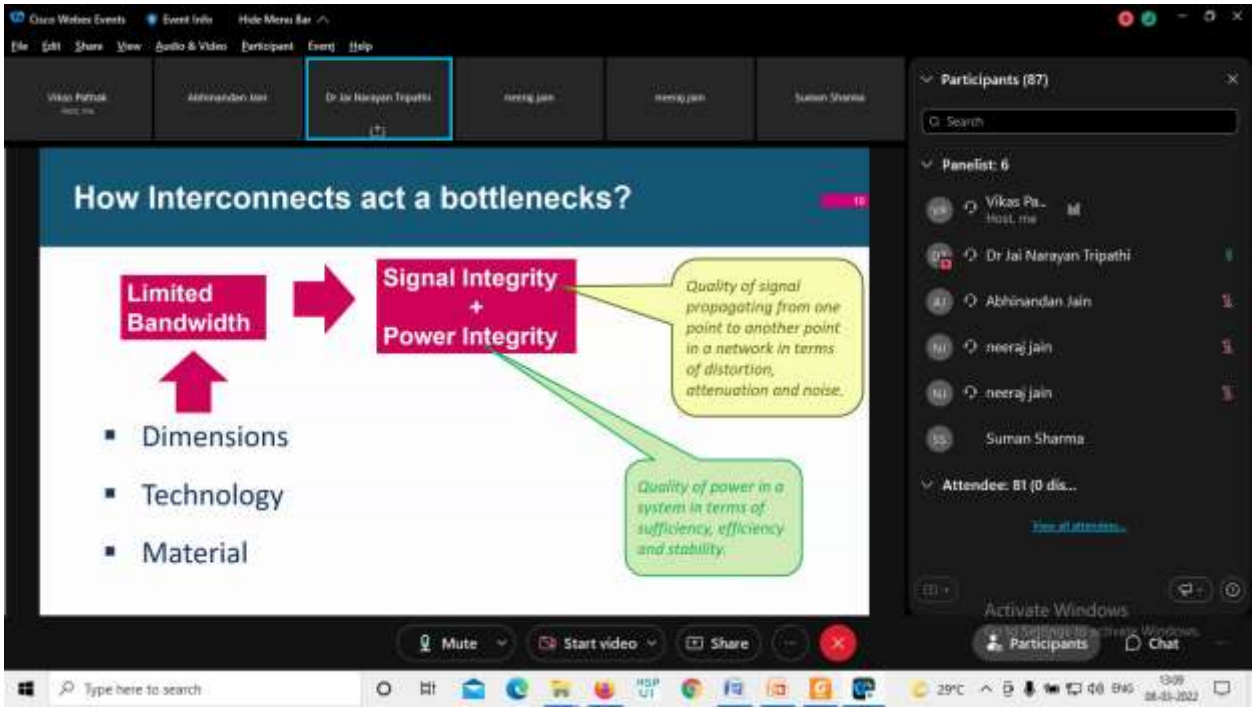
Participants
Chat


How Interconnects act a bottlenecks?
Limited Bandwidth
Signal Integrity + Power Integrity
Quality of signal propagating from one point to another point in a network in terms of distortion, attenuation and noise.
Quality of power in a system in terms of sufficiency, efficiency and stability.
Dimensions
Technology
Material
Participants (87)
Panelist: 6
Vikas Pa..
Dr Jai Narayan Tripathi
Abhinandan Jain
neeraj jain
neeraj jain
Suman Sharma
Attendee: 81 (0 dis...
Mute
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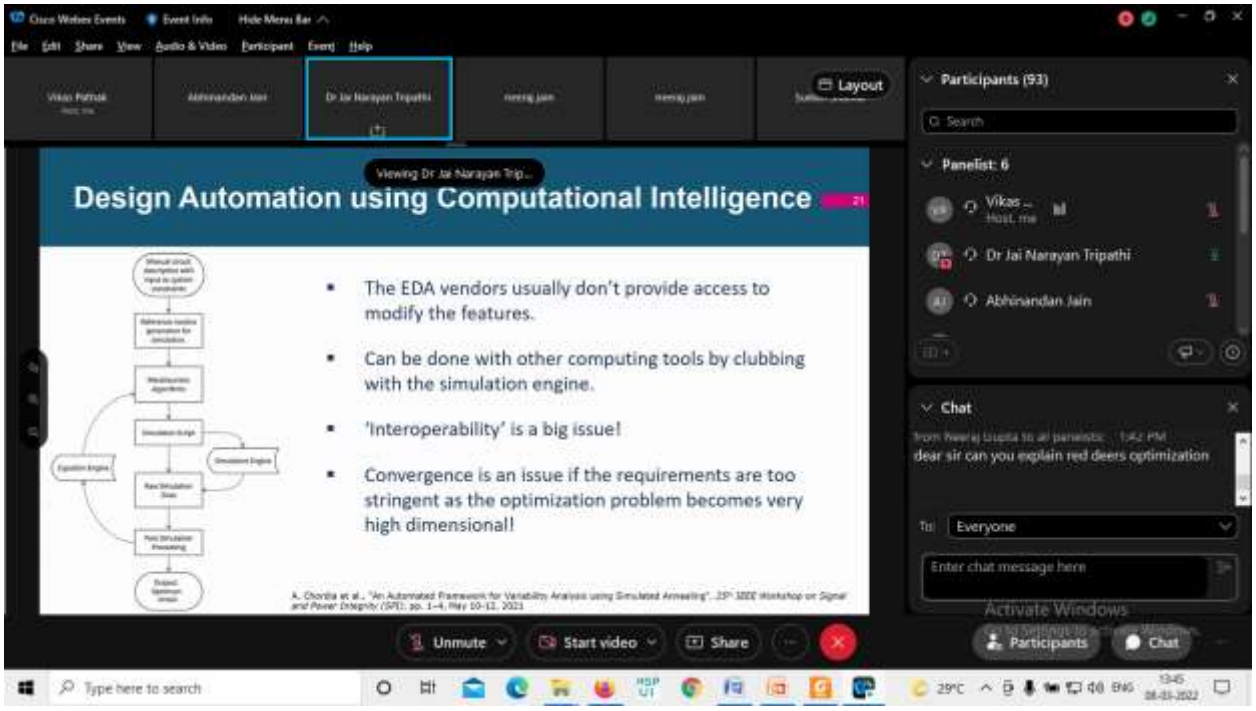
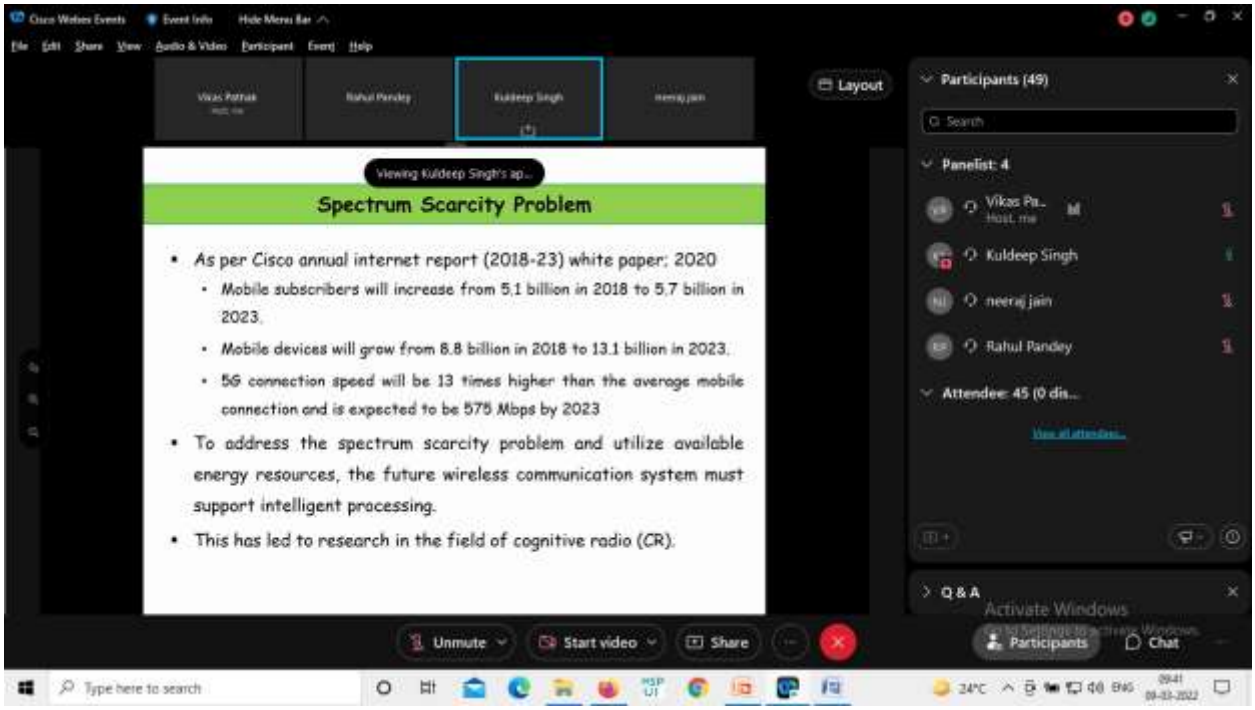
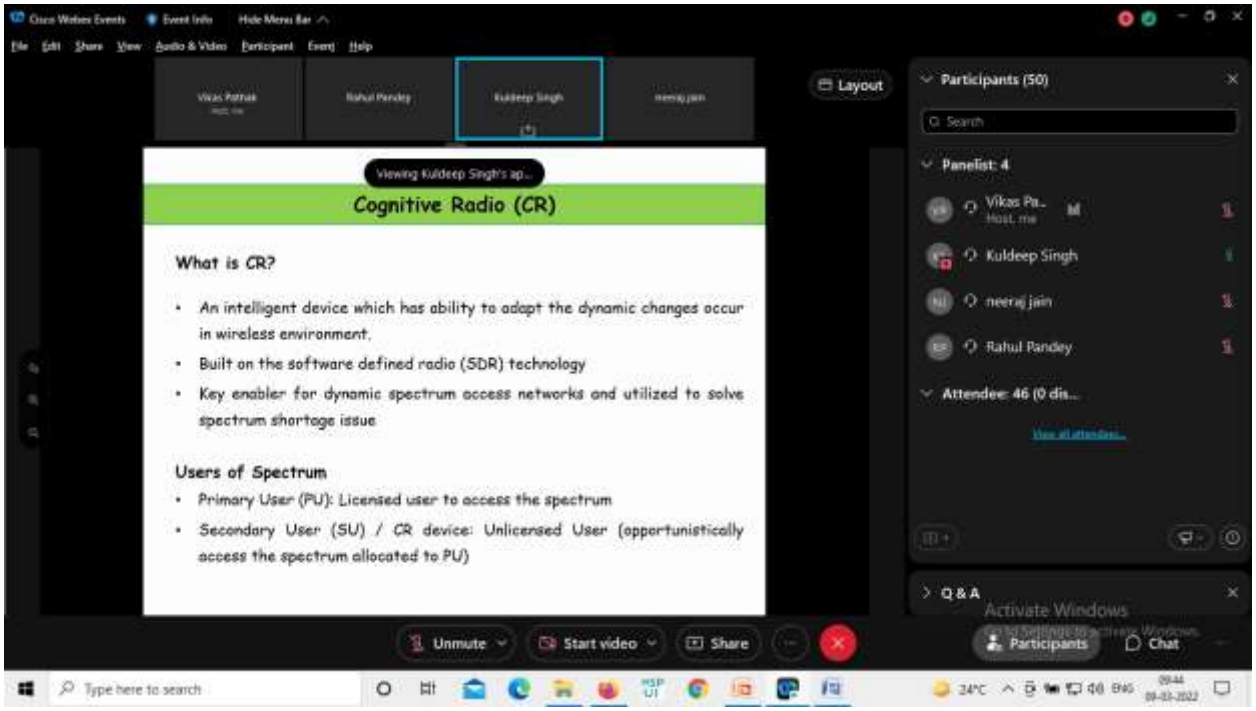
Share
Participants
Chat

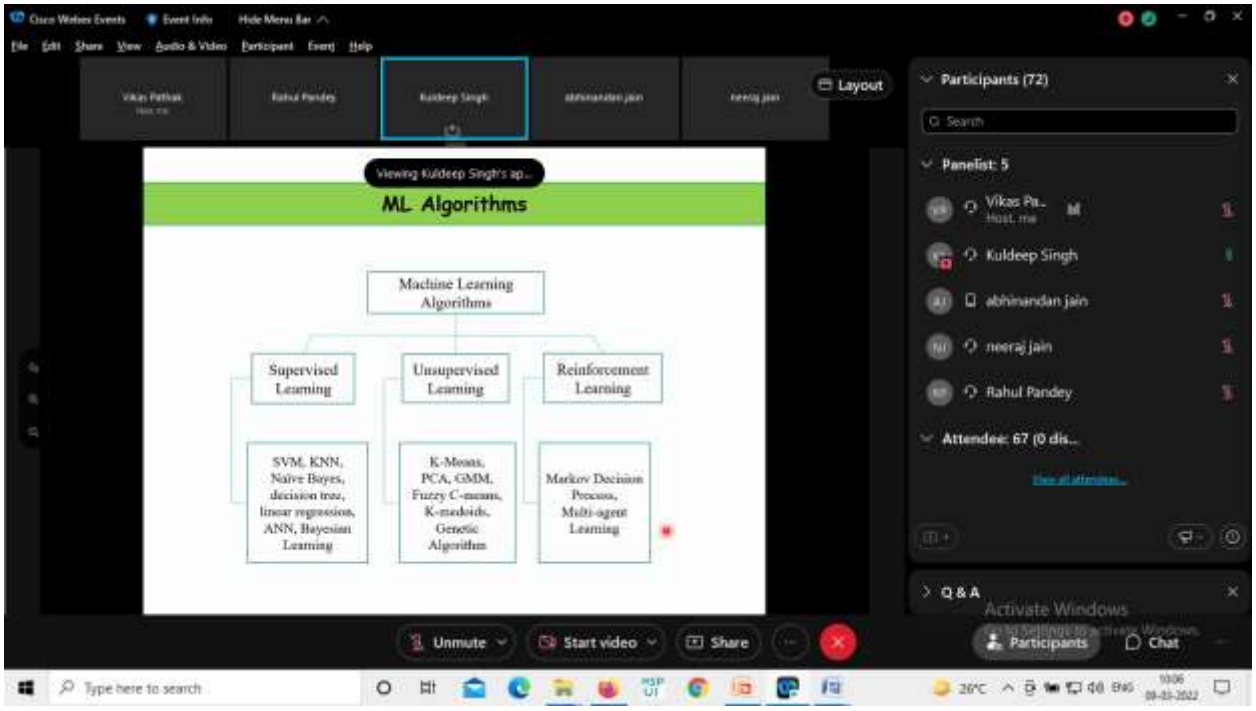

Design Automation using Computational Intelligence
The EDA vendors usually don't provide access to modify the features.
Can be done with other computing tools by clubbing with the simulation engine.
'Interoperability' is a big issue!
Convergence is an issue if the requirements are too stringent as the optimization problem becomes very high dimensional!
Participants (93)
Panelist: 6
Dr Jai Narayan Tripathi
Abhinandan Jain
Chat
dear sir can you explain red deers optimization
Everyone
Enter chat message here
Unmute
Start video
Share
Participants
Chat

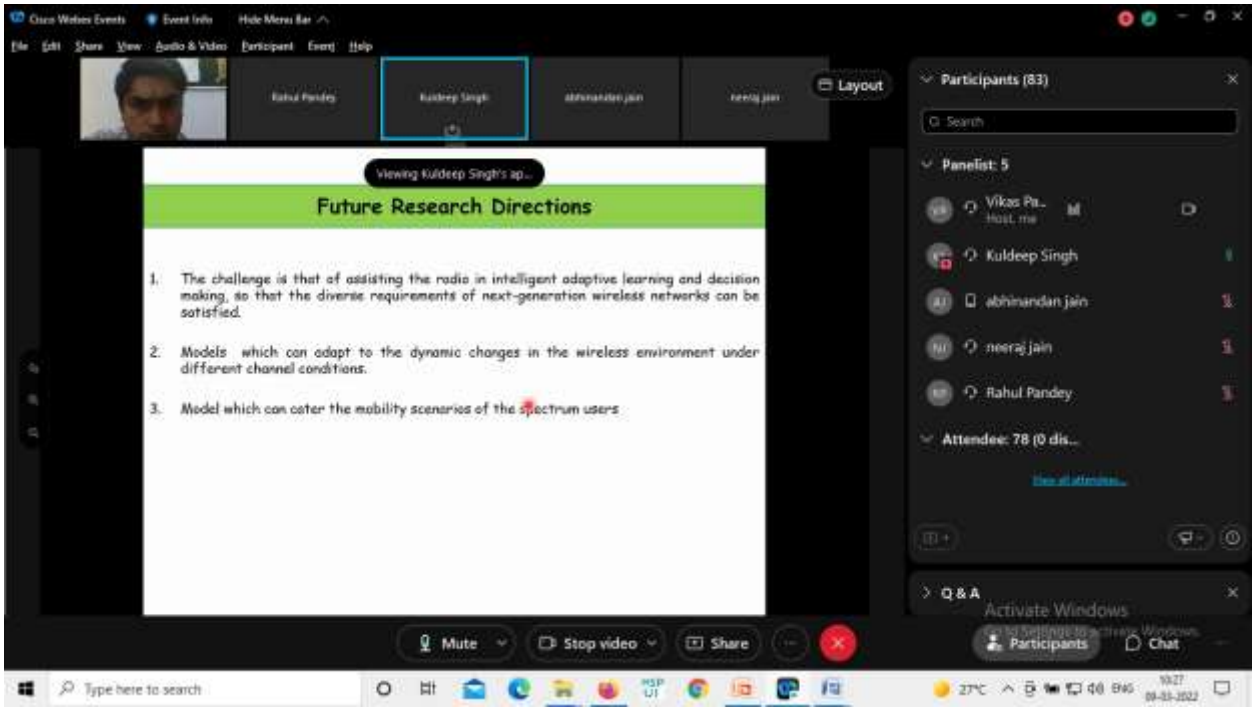
Day-3

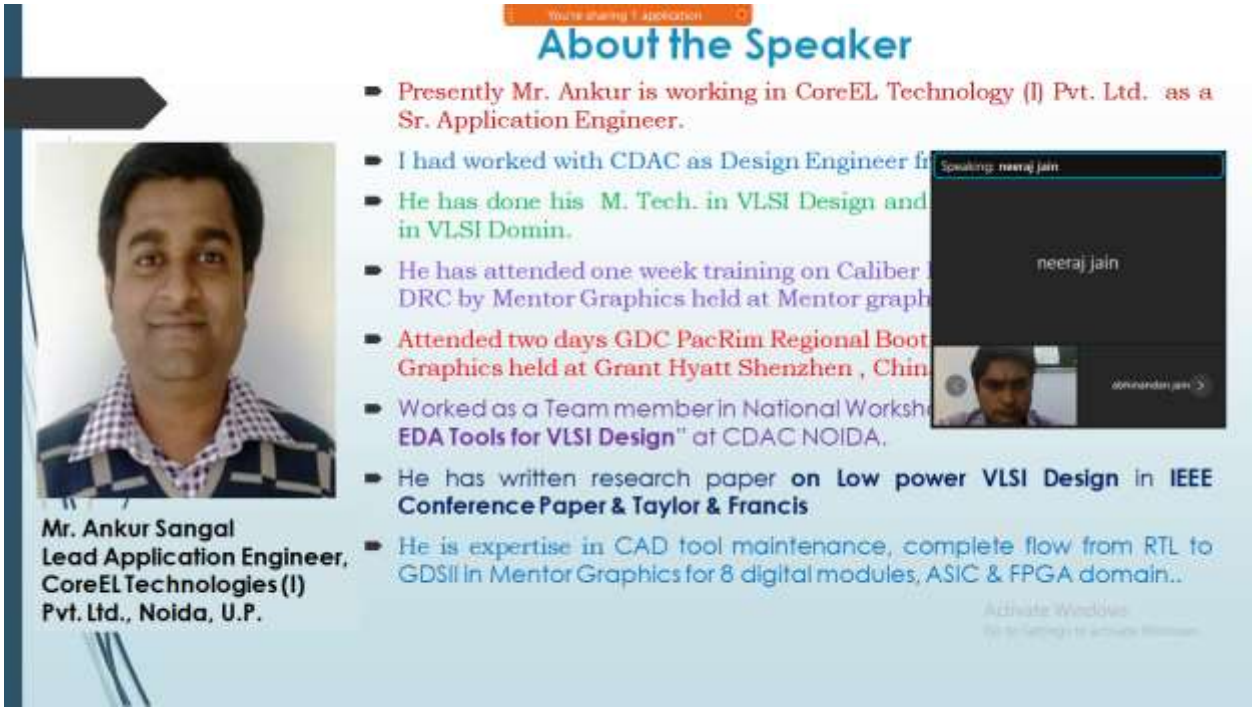

About the Speaker
At present Dr. Kuldeep Singh is working as a Assistant Professor in Dept. of Electronics & Comm. Engineering, MNIT, Jaipur
He has received his Ph.D. degree in Computer Vision from Delhi Technological University, New Delhi, India and
Processing from Netaji Subhash Institute of Techn
He has worked as postdoctoral fellow at Univ
where he was involved in development of deep
prototype for agriculture applications.
Before joining MNIT, he has worked at Central R
Ghaziabad as Scientist from Jan 2007 to Mar 201
has worked on various projects of national importance. He was involved in software development of various C4I systems for Indian & foreign defense forces. He has been awarded R & D Excellence award at Bharat Electronics Ltd. in 2009 and 2011.
He is reviewer of various IEEE transactions, Elsevier & Springer journals.
His research interest includes Machine/Deep Learning, Computer Vision, Artificial Intelligence, Biometrics, Cyber Security
Speaking: Rahul Pandey
Rahul Pandey
Dr. Kuldeep Singh
Assistant Professor,
Dept. of Electronics &
Comm. Engineering,
MNIT, Jaipur

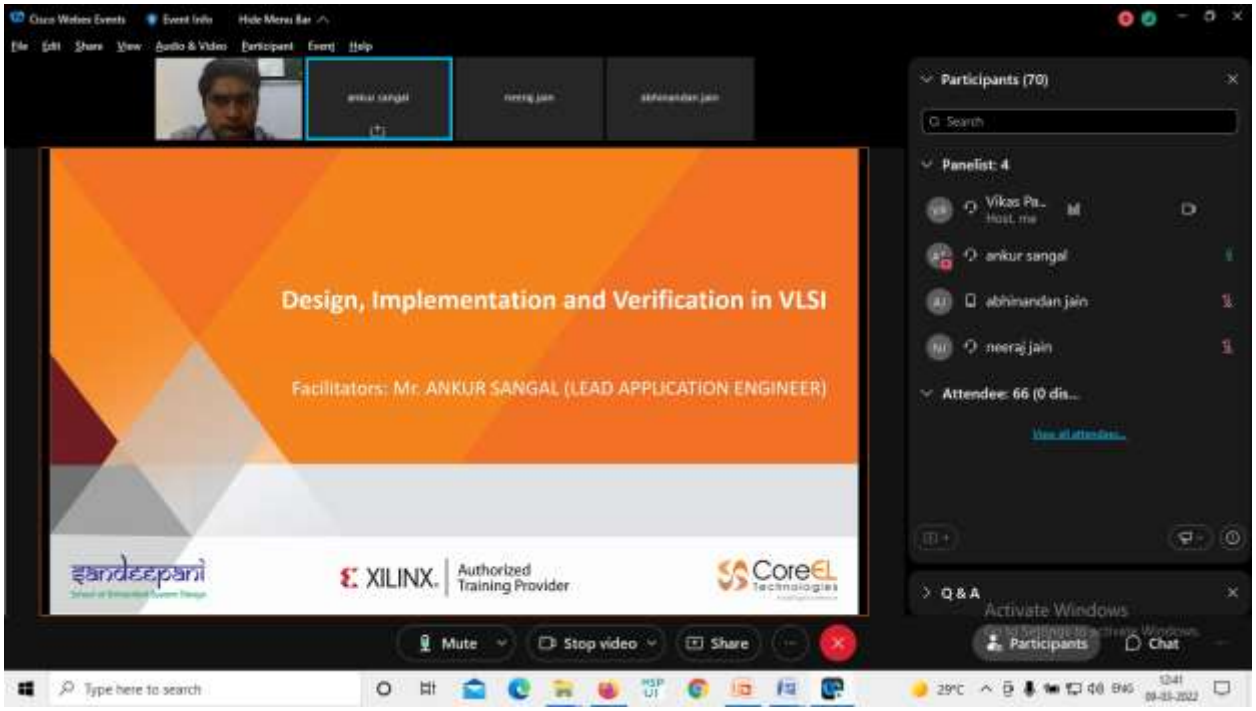

Spectrum Scarcity Problem
As per Cisco annual internet report (2018-23) white paper; 2020
Mobile subscribers will increase from 5.1 billion in 2018 to 5.7 billion in 2023.
Mobile devices will grow from 8.8 billion in 2018 to 13.1 billion in 2023.
5G connection speed will be 13 times higher than the average mobile connection and is expected to be 575 Mbps by 2023
To address the spectrum scarcity problem and utilize available energy resources, the future wireless communication system must support intelligent processing.
This has led to research in the field of cognitive radio (CR).
Participants (49)
Panelist: 4
Kuldeep Singh
neeraj jain
Rahul Pandey
Attendee: 45 (0 dis...
Q & A
Unmute
Start video
Share
Participants
Chat
Activate Windows

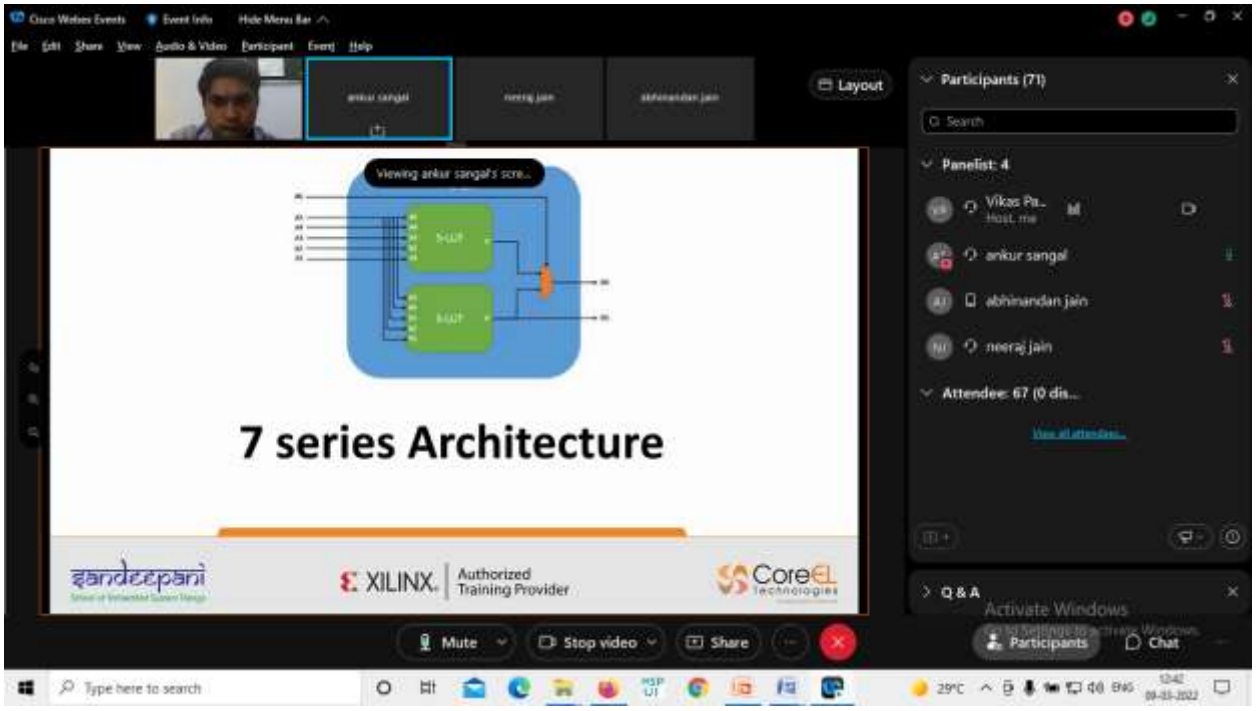

Cognitive Radio (CR)
What is CR?
• An intelligent device which has ability to adapt the dynamic changes occur in wireless environment.
• Built on the software defined radio (SDR) technology
• Key enabler for dynamic spectrum access networks and utilized to solve spectrum shortage issue
Users of Spectrum
• Primary User (PU): Licensed user to access the spectrum
• Secondary User (SU) / CR device: Unlicensed User (opportunistically access the spectrum allocated to PU)
Participants (50)
Panelist: 4
Kuldeep Singh
neeraj jain
Rahul Pandey
Attendee: 46 (0 dis...
Q&A
Unmute
Start video
Share
Participants
Chat

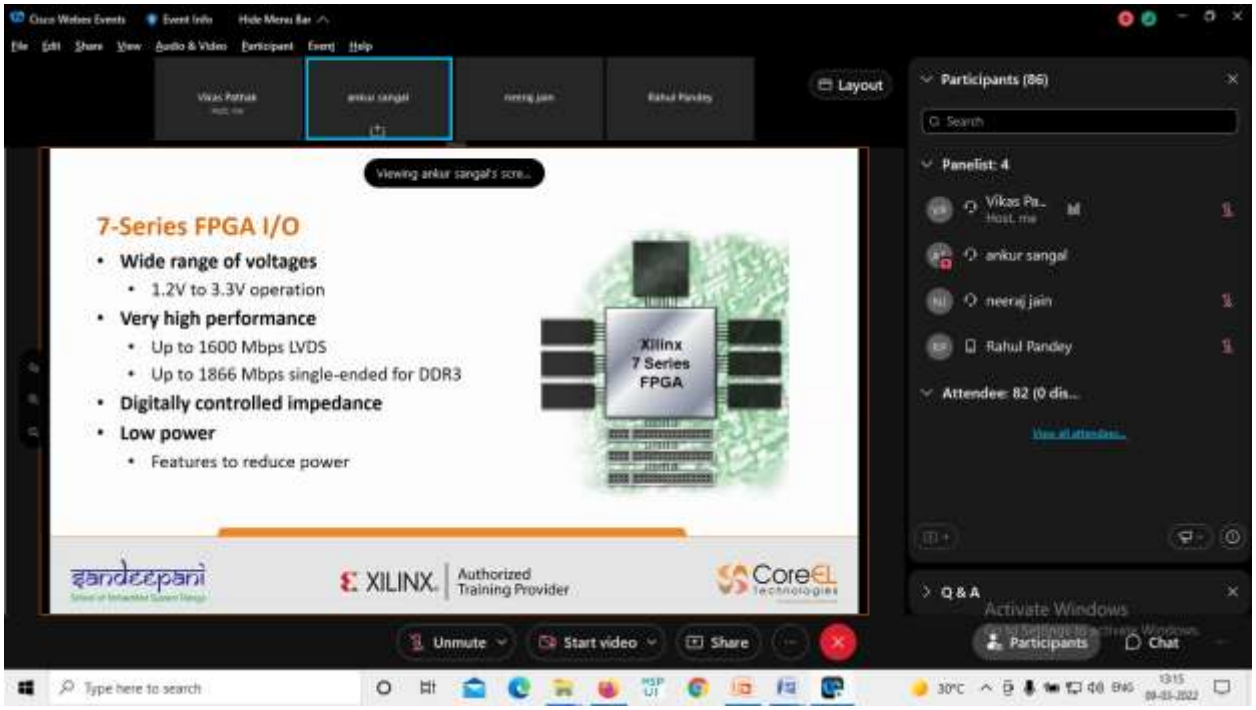

ML Algorithms
Machine Learning Algorithms
Supervised Learning
Unsupervised Learning
Reinforcement Learning
SVM, KNN, Naïve Bayes, decision tree, linear regression, ANN, Bayesian Learning
K-Means, PCA, GMM, Fuzzy C-means, K-medoids, Genetic Algorithm
Markov Decision Process, Multi-agent Learning
Participants (72)
Panelist: 5
Kuldeep Singh
abhinandan jain
neeraj jain
Rahul Pandey
Attendee: 67 (0 dis...
Q&A
Unmute
Start video
Share
Participants
Chat

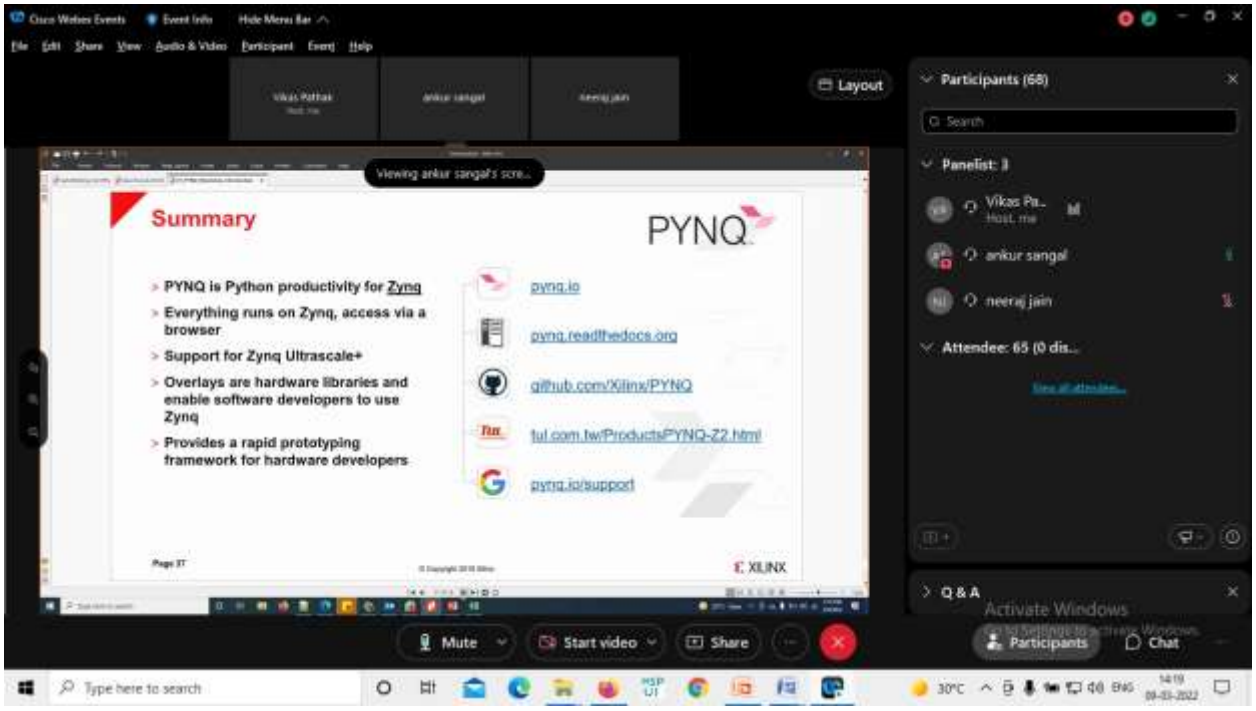

Future Research Directions
1. The challenge is that of assisting the radio in intelligent adaptive learning and decision making, so that the diverse requirements of next-generation wireless networks can be satisfied.
2. Models which can adapt to the dynamic changes in the wireless environment under different channel conditions.
3. Model which can cater the mobility scenarios of the spectrum users
Participants (83)
Panelist: 5
Vikas Pa...
Kuldeep Singh
abhinandan jain
neeraj jain
Rahul Pandey
Attendee: 78 (0 dis...
Q & A
Mute
Stop video
Share
Participants
Chat


About the Speaker
Presently Mr. Ankur is working in CoreEL Technology (I) Pvt. Ltd. as a Sr. Application Engineer.
I had worked with CDAC as Design Engineer fr
He has done his M. Tech. in VLSI Design and in VLSI Domin.
He has attended one week training on Caliber DRC by Mentor Graphics held at Mentor graph
Attended two days GDC PacRim Regional Boot Graphics held at Grant Hyatt Shenzhen , Chin
Worked as a Team member in National Worksh EDA Tools for VLSI Design" at CDAC NOIDA.
He has written research paper on Low power VLSI Design in IEEE Conference Paper & Taylor & Francis
He is expertise in CAD tool maintenance, complete flow from RTL to GDSII in Mentor Graphics for 8 digital modules, ASIC & FPGA domain..
Speaking: neeraj jain
neeraj jain
Mr. Ankur Sangal
Lead Application Engineer,
CoreEL Technologies (I)
Pvt. Ltd., Noida, U.P.

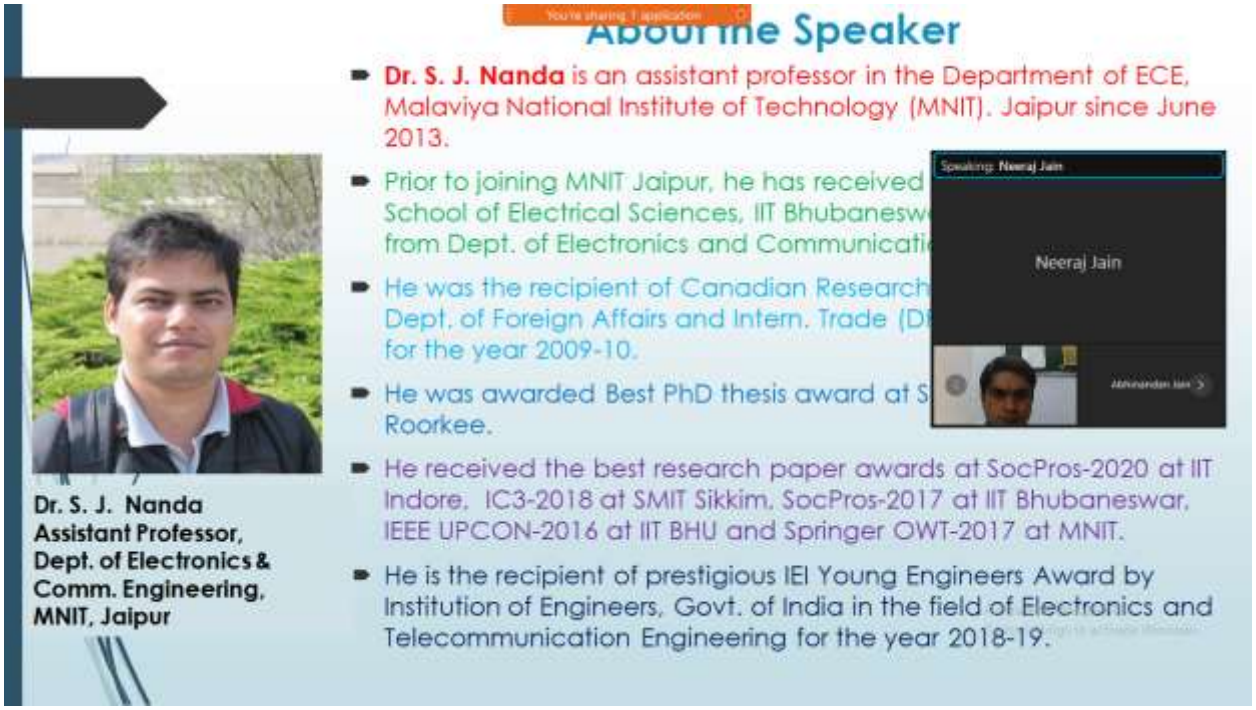

Design, Implementation and Verification in VLSI
Facilitators: Mr. ANKUR SANGAL (LEAD APPLICATION ENGINEER)
sandeepani
XILINX. Authorized Training Provider
CoreEL Technologies
Participants (70)
Panelist: 4
Vikas Pa..
ankur sangal
abhinandan jain
neeraj jain
Attendee: 66 (0 dis..
Q & A
Mute
Stop video
Share
Participants
Chat
Activate Windows

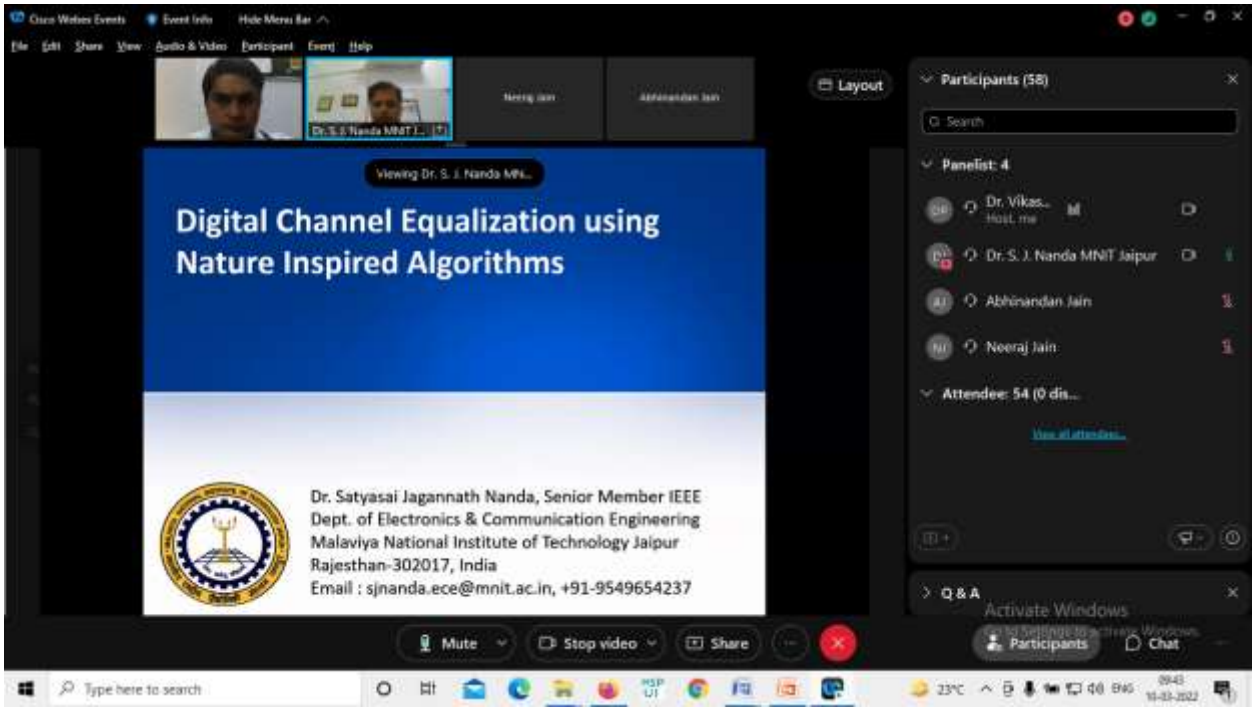

Viewing ankur sangal's scre...
7 series Architecture
sandeepani
XILINX. Authorized Training Provider
CoreEL Technologies
Layout
Participants (71)
Panelist: 4
Vikas Pa..
ankur sangal
abhinandan jain
neeraj jain
Attendee: 67 (0 dis..
Q & A
Mute
Stop video
Share
Participants
Chat
Activate Windows

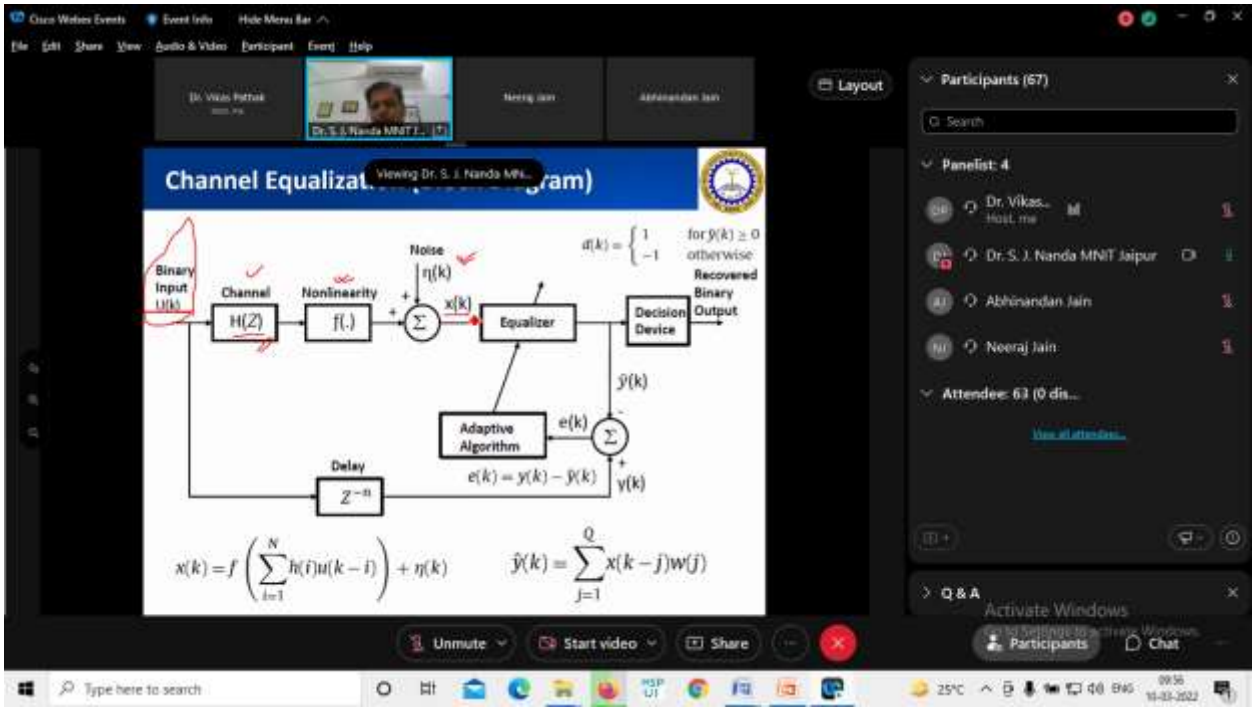

Viewing ankur sangal's scre...
7-Series FPGA I/O
Wide range of voltages
1.2V to 3.3V operation
Very high performance
Up to 1600 Mbps LVDS
Up to 1866 Mbps single-ended for DDR3
Digitally controlled impedance
Low power
Features to reduce power
Xilinx 7 Series FPGA
sandeepani
XILINX
Authorized Training Provider
CoreEL Technologies
Participants (86)
Panelist: 4
Vikas Pa.. Host, me
ankur sangal
neeraj jain
Rahul Pandey
Attendee: 82 (0 dis...
Q & A
Unmute
Start video
Share
Participants
Chat

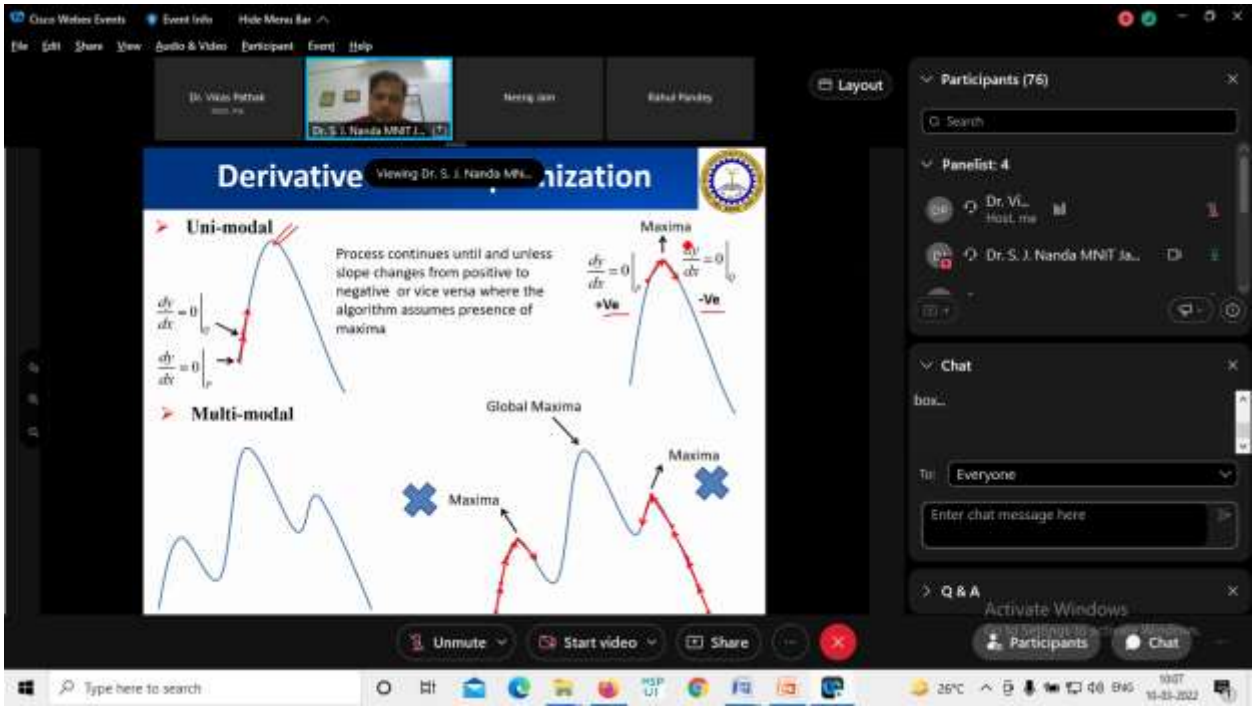

Viewing ankur sangal's scre...
Summary
PYNQ
PYNQ is Python productivity for Zynq
Everything runs on Zynq, access via a browser
Support for Zynq Ultrascale+
Overlays are hardware libraries and enable software developers to use Zynq
Provides a rapid prototyping framework for hardware developers
pynq.io
pynq.readthedocs.org
github.com/Xilinx/PYNQ
ful.com.tw/Products/PYNQ-Z2.html
pynq.io/support
XILINX
Participants (68)
Panelist: 3
Vikas Pa.. Host, me
ankur sangal
neeraj jain
Attendee: 65 (0 dis...
Q & A
Mute
Start video
Share
Participants
Chat

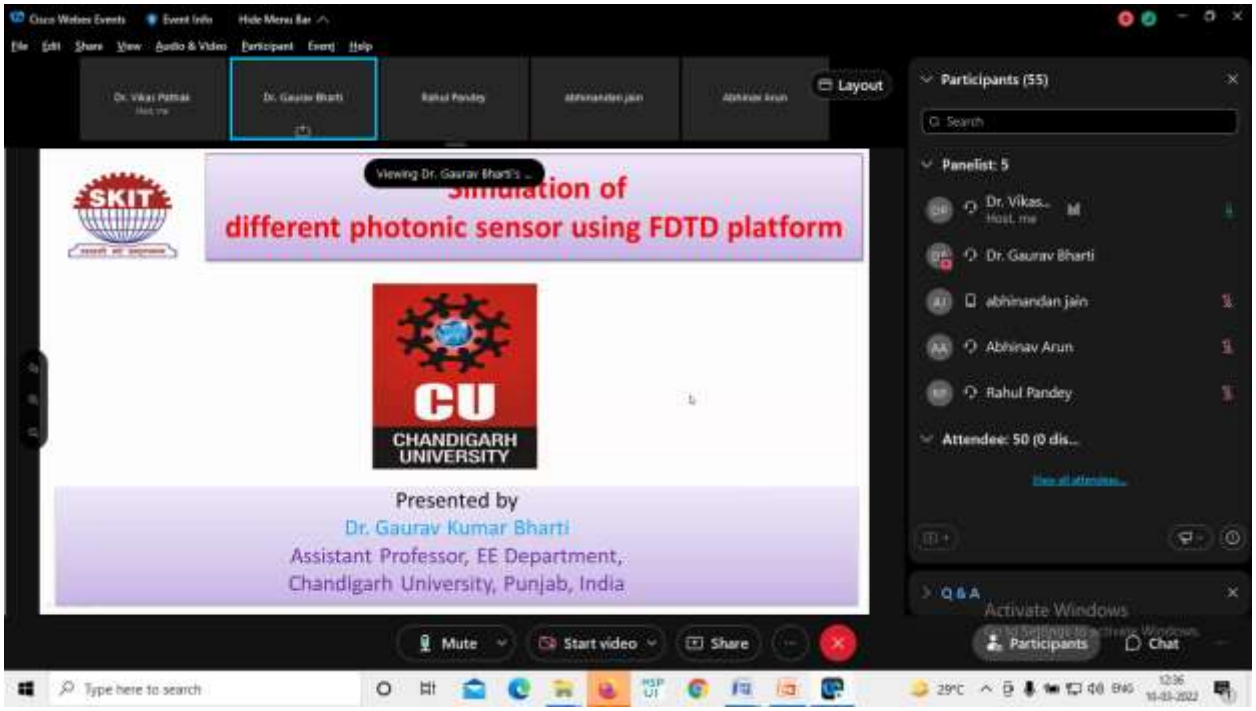
Day-4

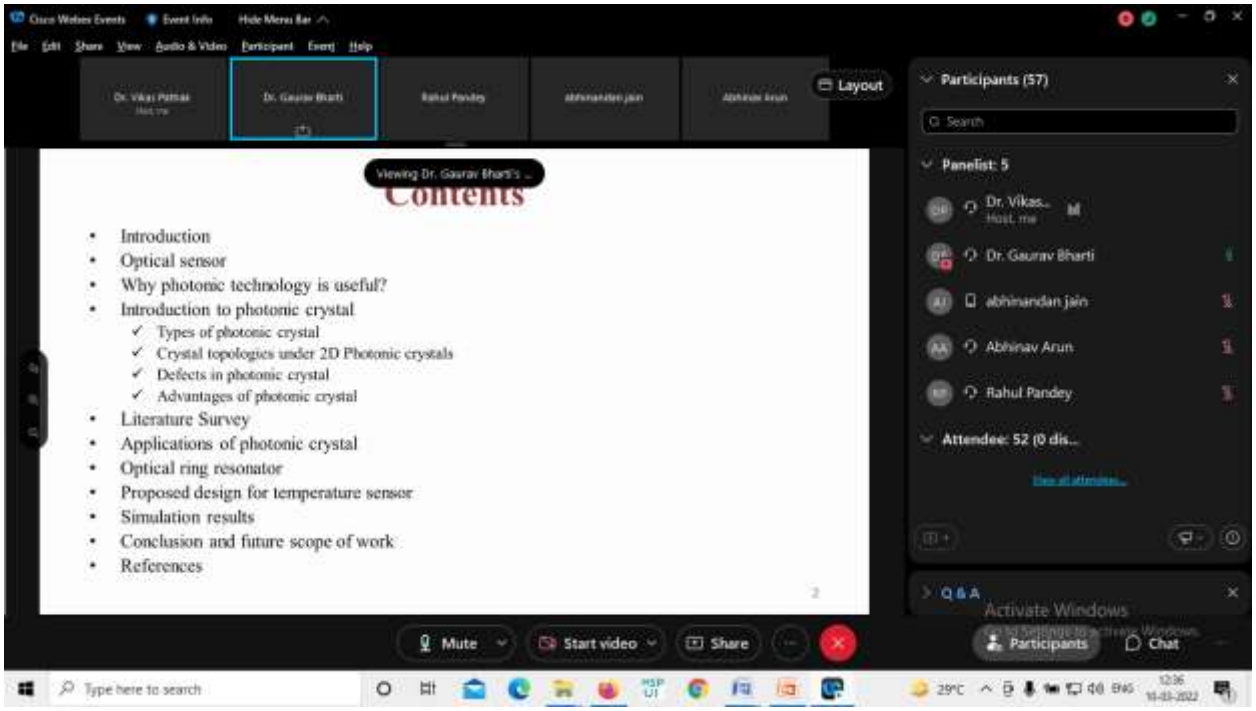

About the Speaker
Dr. S. J. Nanda is an assistant professor in the Department of ECE, Malaviya National Institute of Technology (MNIT), Jaipur since June 2013.
Prior to joining MNIT Jaipur, he has received
School of Electrical Sciences, IIT Bhubaneswa
from Dept. of Electronics and Communicati
He was the recipient of Canadian Research
Dept. of Foreign Affairs and Intern. Trade (DF
for the year 2009-10.
He was awarded Best PhD thesis award at S
Roorkee.
He received the best research paper awards at SocPros-2020 at IIT Indore, IC3-2018 at SMIT Sikkim, SocPros-2017 at IIT Bhubaneswar, IEEE UPCON-2016 at IIT BHU and Springer OWT-2017 at MNIT.
He is the recipient of prestigious IEI Young Engineers Award by Institution of Engineers, Govt. of India in the field of Electronics and Telecommunication Engineering for the year 2018-19.
Speaking: Neeraj Jain
Neeraj Jain
Dr. S. J. Nanda
Assistant Professor,
Dept. of Electronics &
Comm. Engineering,
MNIT, Jaipur


Viewing Dr. S. J. Nanda MN...
Digital Channel Equalization using Nature Inspired Algorithms
Dr. Satyasai Jagannath Nanda, Senior Member IEEE
Dept. of Electronics & Communication Engineering
Malaviya National Institute of Technology Jaipur
Rajesthan-302017, India
Email : sjnanda.ece@mnit.ac.in, +91-9549654237
Layout
Participants (58)
Panelist: 4
Dr. Vikas...
Dr. S. J. Nanda MNIT Jaipur
Abhinandan Jain
Neeraj Jain
Attendee: 54 (0 dis...
Q & A
Mute
Stop video
Share
Participants
Chat
Activate Windows


Channel Equalizat
Viewing Dr. S. J. Nanda MN
Binary Input
Channel
H(Z)
Nonlinearity
f(.)
Noise
η(k)
x(k)
Equalizer
Decision Device
Recovered Binary Output
Adaptive Algorithm
e(k)
Delay
y(k)
Participants (67)
Panelist: 4
Dr. Vikas...
Dr. S. J. Nanda MNIT Jaipur
Abhinandan Jain
Neeraj Jain
Attendee: 63 (0 dis...
Q & A
Unmute
Start video
Share
Participants
Chat

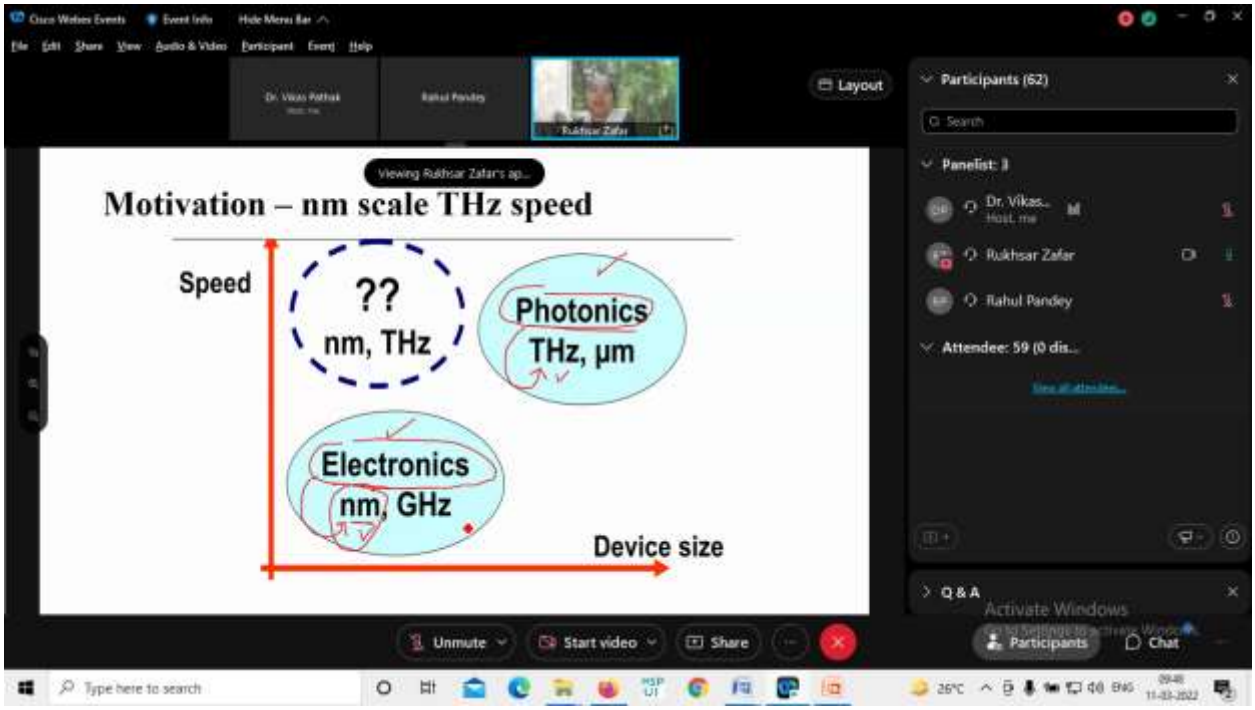

Derivative
Viewing Dr. S. J. Nanda MN
Uni-modal
Process continues until and unless slope changes from positive to negative or vice versa where the algorithm assumes presence of maxima
Maxima
+Ve
-Ve
Multi-modal
Global Maxima
Maxima
Maxima
Participants (76)
Panelist: 4
Dr. S. J. Nanda MNIT Ja...
Chat
To: Everyone
Enter chat message here
Q & A
Unmute
Start video
Share
Participants
Chat

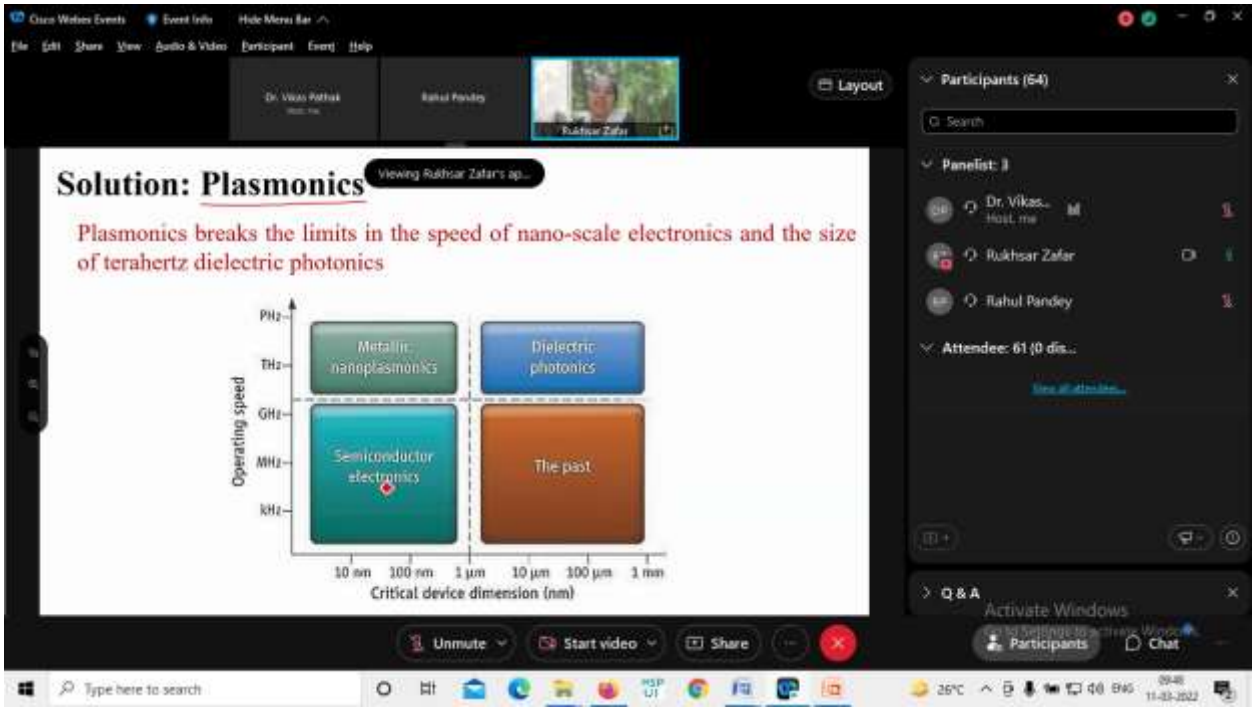

Viewing Dr. Gaurav Bharti's ...
Simulation of
different photonic sensor using FDTD platform
SKIT
CU
CHANDIGARH UNIVERSITY
Presented by
Dr. Gaurav Kumar Bharti
Assistant Professor, EE Department,
Chandigarh University, Punjab, India
Participants (55)
Panelist: 5
Dr. Vikas...
Dr. Gaurav Bharti
abhinandan jain
Abhinav Arun
Rahul Pandey
Attendee: 50 (0 dis...
Q & A
Mute
Start video
Share
Participants
Chat

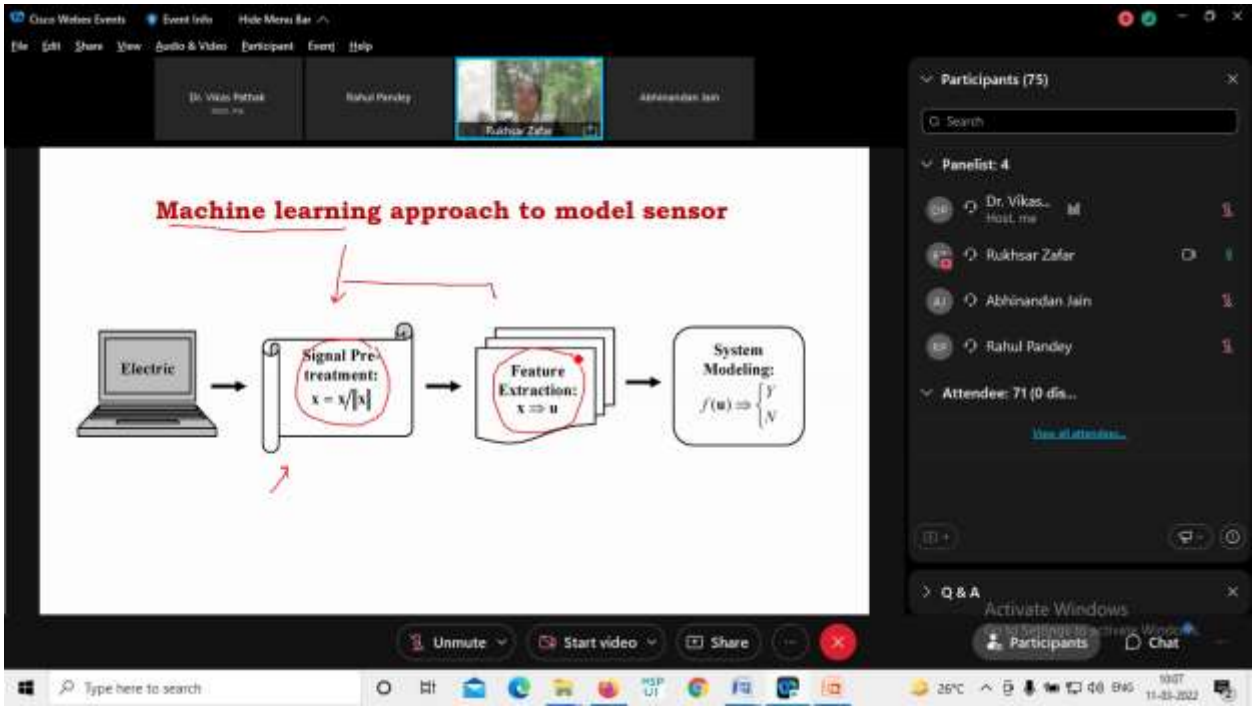

Viewing Dr. Gaurav Bharti's ...
Contents
Introduction
Optical sensor
Why photonic technology is useful?
Introduction to photonic crystal
Types of photonic crystal
Crystal topologies under 2D Photonic crystals
Defects in photonic crystal
Advantages of photonic crystal
Literature Survey
Applications of photonic crystal
Optical ring resonator
Proposed design for temperature sensor
Simulation results
Conclusion and future scope of work
References
Participants (57)
Panelist: 5
Dr. Vikas...
Dr. Gaurav Bharti
abhinandan jain
Abhinav Arun
Rahul Pandey
Attendee: 52 (0 dis...
Q & A
Mute
Start video
Share
Participants
Chat

Day-5

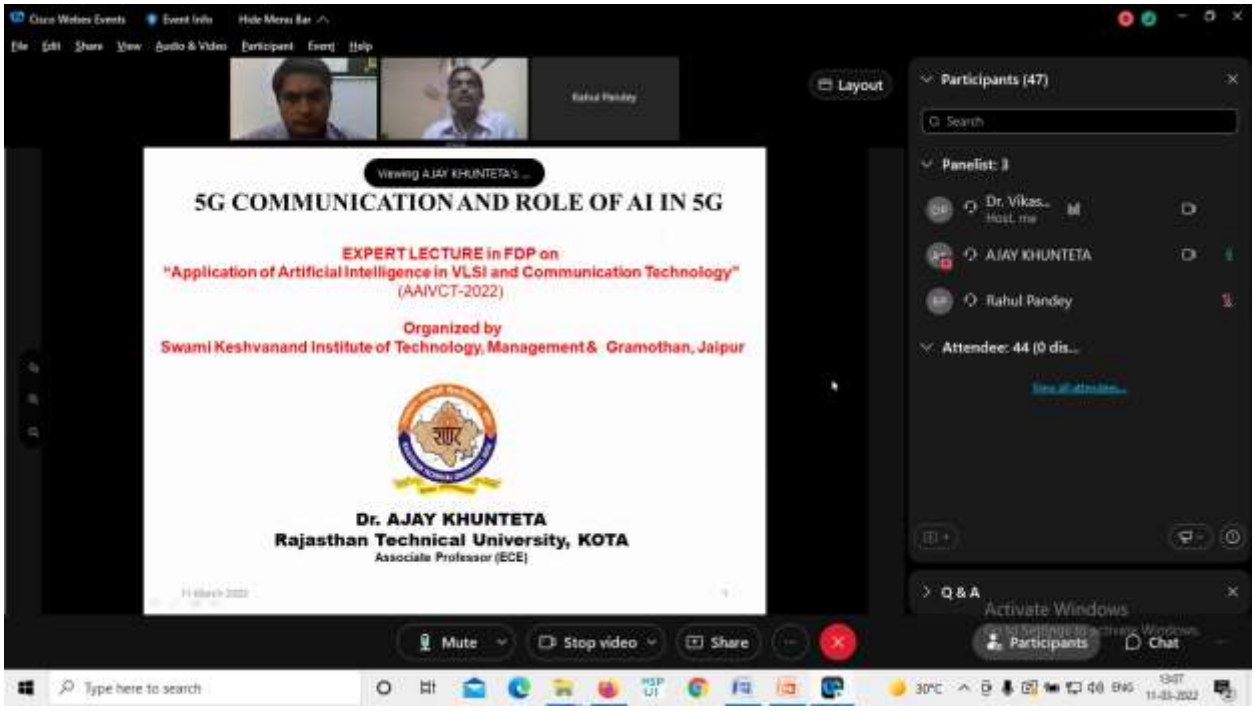

About the Speaker
Dr. Rukhsar Zafar received PhD degree from the Malaviya National Institute of Technology, Jaipur.
She is currently working as an Associa
Department of Electronic and Commu
Swami Keshvanand Institute of Technol
Gramothan, Jaipur.
Her research interests include photonic crystal, plasmonics, sensors, photonic integrated circuits, and slow light and has published over 30 research papers in refereed journals and conferences. She has guided several M. Tech. Dissertation and guiding 2 research scholars.
She is a Member of OSA, OSI, IE, IEEE and ISTE and reviewer of several reputed journals (IEEE, OSA, Springer etc.)
Dr. Rukhsar Zafar, Associate Professor, Deptt. of Electronics & Comm. Engineering, SKIT, Jaipur
Rahul Pandey

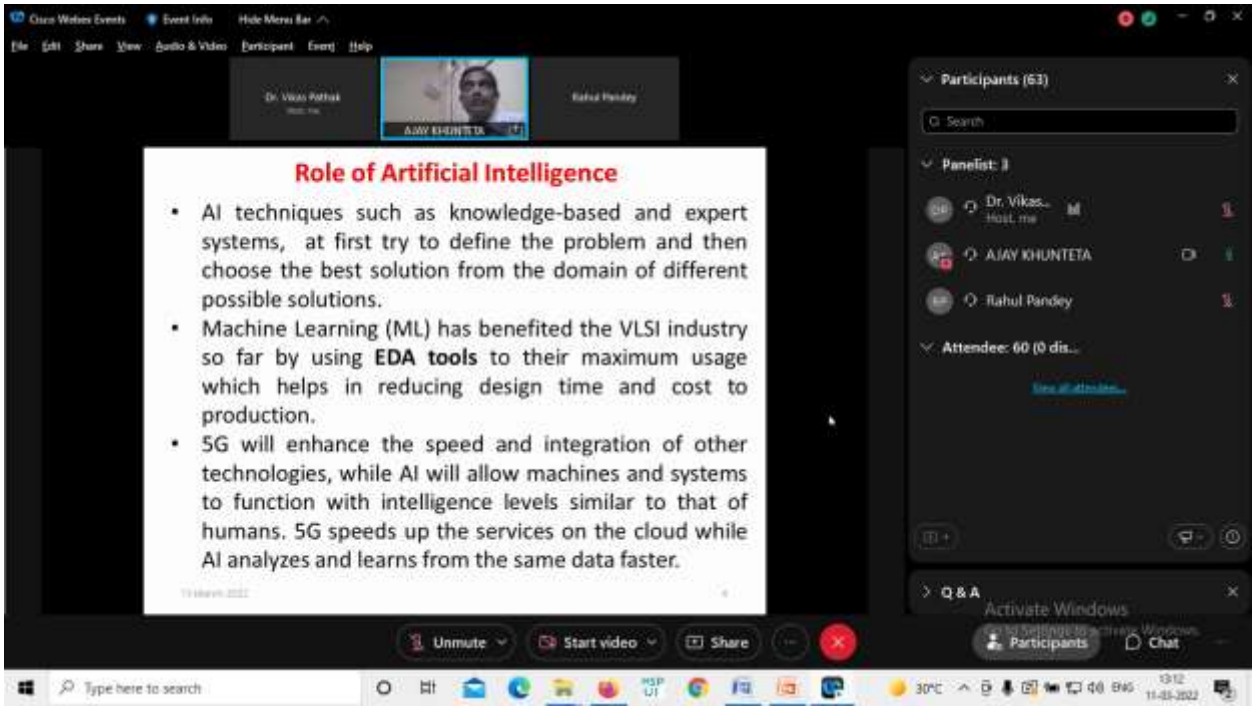

Motivation – nm scale THz speed
Speed
??
nm, THz
Photonics
THz, μm
Electronics
nm, GHz
Device size
Participants (62)
Panelist: 3
Dr. Vikas...
Rukhsar Zafar
Rahul Pandey
Attendee: 59 (0 dis...
Q & A
Unmute
Start video
Share
Participants
Chat
Layout

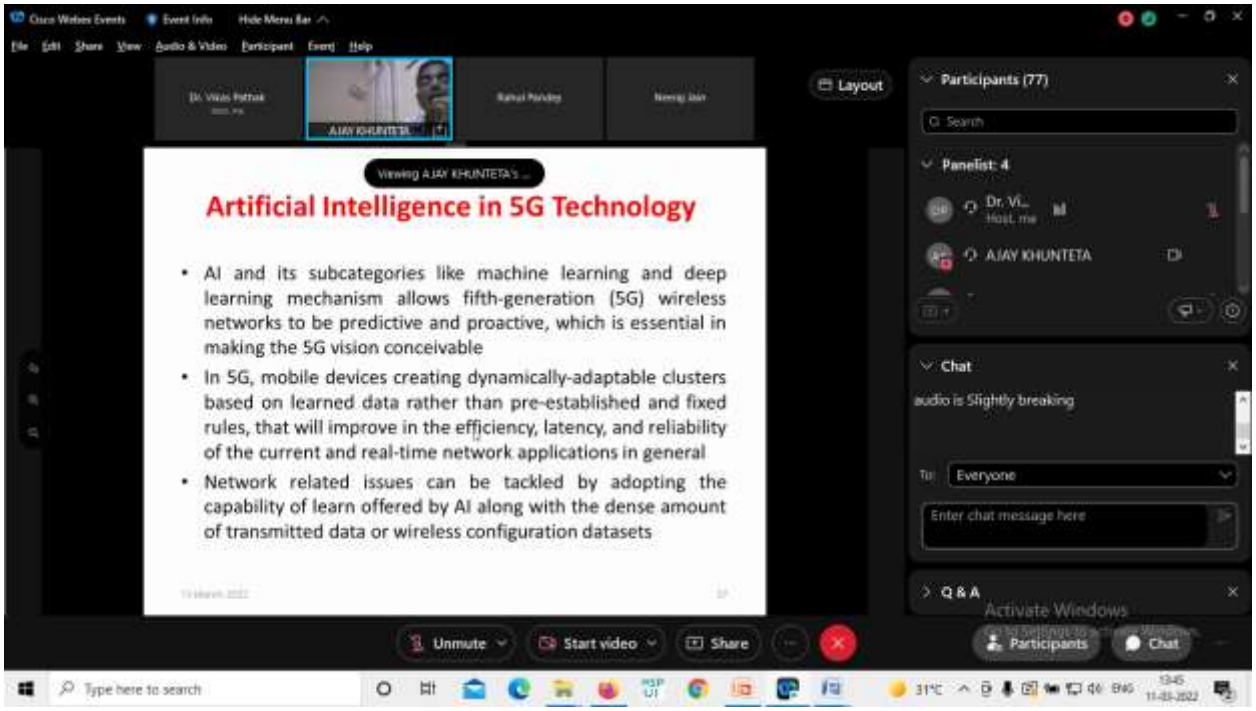

Solution: Plasmonics
Viewing Rukhsar Zafar's ap...
Plasmonics breaks the limits in the speed of nano-scale electronics and the size of terahertz dielectric photonics
Operating speed
PHz
THz
GHz
MHz
kHz
Metallic nanoplasmonics
Dielectric photonics
Semiconductor electronics
The past
10 nm
100 nm
1 μm
10 μm
100 μm
1 mm
Critical device dimension (nm)
Participants (64)
Panelist: 3
Dr. Vikas...
Rukhsar Zafar
Rahul Pandey
Attendee: 61 (0 dis...
Q & A
Unmute
Start video
Share
Participants
Chat

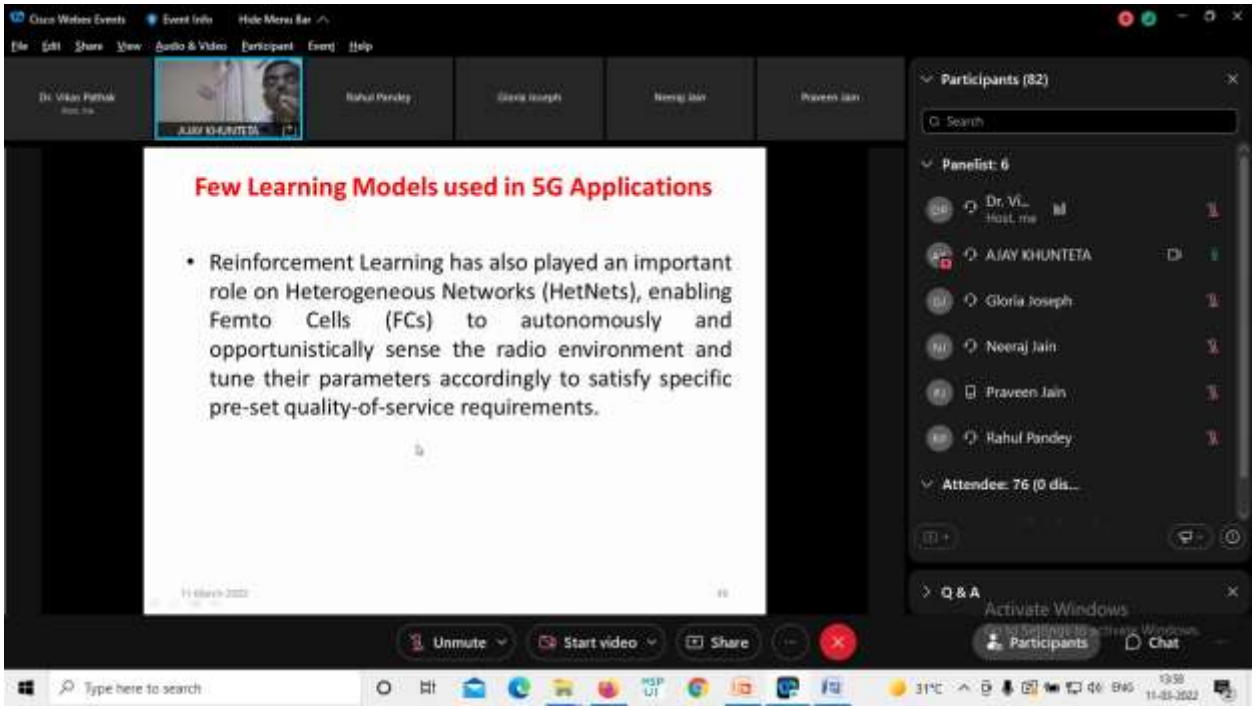

Machine learning approach to model sensor
Electric
Signal Pre-treatment:
Feature Extraction:
System Modeling:
Participants (75)
Panelist: 4
Dr. Vikas...
Rukhsar Zafar
Abhinandan Jain
Rahul Pandey
Attendee: 71 (0 dis...
Q & A
Unmute
Start video
Share
Participants
Chat

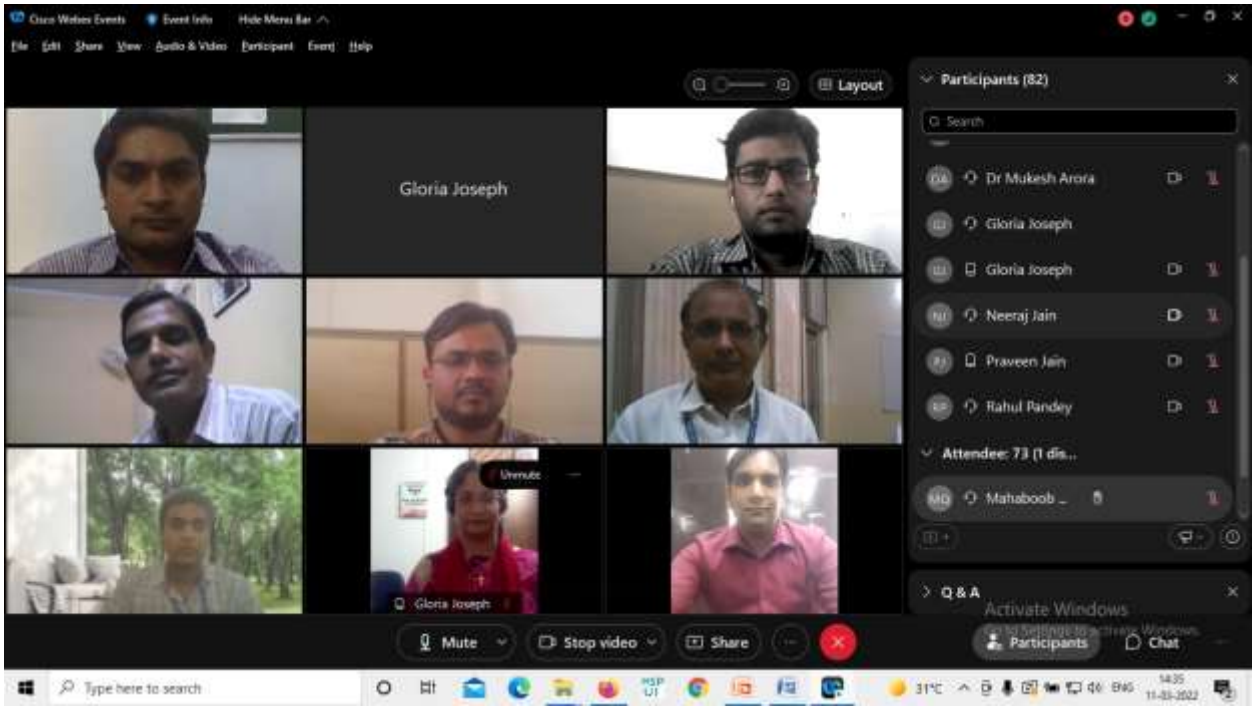

About the Speaker
Dr. Ajay is working as Associate Professor in Electronics Engineering Department at Rajasthan Technical University, Kota (Rajasthan)
He has Awarded Ph.D. degree in the field
from Electronics & Comm. Engg. Deptt., I.I.
He has taught various subject related to cor
at UG and PG level.
Presently he is supervising 3 Ph.D
and already guided 15 PG disser
award of M. Tech. (Digital Comm.
He has been served the various administrative responsibilities in RTU, Kota.
His research interest includes Signal and Image Processing, Communication Systems, Wireless Communication Networks
Dr. Ajay Khunteta,
Associate Professor,
Department of Electronics and Comm. Engineering,
RTU, Kota


5G COMMUNICATION AND ROLE OF AI IN 5G
EXPERT LECTURE in FDP on
"Application of Artificial Intelligence in VLSI and Communication Technology"
(AAIVCT-2022)
Organized by
Swami Keshvanand Institute of Technology, Management & Gramothan, Jaipur
Dr. AJAY KHUNTETA
Rajasthan Technical University, KOTA
Associate Professor (ECE)
Participants (47)
Panelist: 3
Dr. Vikas...
AJAY KHUNTETA
Rahul Pandey
Attendee: 44 (0 dis...
Q&A
Mute
Stop video
Share
Participants
Chat


Role of Artificial Intelligence
• AI techniques such as knowledge-based and expert systems, at first try to define the problem and then choose the best solution from the domain of different possible solutions.
• Machine Learning (ML) has benefited the VLSI industry so far by using EDA tools to their maximum usage which helps in reducing design time and cost to production.
• 5G will enhance the speed and integration of other technologies, while AI will allow machines and systems to function with intelligence levels similar to that of humans. 5G speeds up the services on the cloud while AI analyzes and learns from the same data faster.
Participants (63)
Panelist: 3
Dr. Vikas...
AJAY KHUNTETA
Rahul Pandey
Attendee: 60 (0 dis...
Q & A
Unmute
Start video
Share
Participants
Chat


Artificial Intelligence in 5G Technology
• AI and its subcategories like machine learning and deep learning mechanism allows fifth-generation (5G) wireless networks to be predictive and proactive, which is essential in making the 5G vision conceivable
• In 5G, mobile devices creating dynamically-adaptable clusters based on learned data rather than pre-established and fixed rules, that will improve in the efficiency, latency, and reliability of the current and real-time network applications in general
• Network related issues can be tackled by adopting the capability of learn offered by AI along with the dense amount of transmitted data or wireless configuration datasets
Layout
Participants (77)
Panelist: 4
AJAY KHUNTETA
Chat
audio is Slightly breaking
To: Everyone
Enter chat message here
Q & A
Unmute
Start video
Share
Participants
Chat


Few Learning Models used in 5G Applications
• Reinforcement Learning has also played an important role on Heterogeneous Networks (HetNets), enabling Femto Cells (FCs) to autonomously and opportunistically sense the radio environment and tune their parameters accordingly to satisfy specific pre-set quality-of-service requirements.
Participants (82)
Panelist: 6
Dr. Vi...
AJAY KHUNTETA
Gloria Joseph
Neeraj Jain
Praveen Jain
Rahul Pandey
Attendee: 76 (0 dis...
Q & A
Unmute
Start video
Share
Participants
Chat


Layout
Gloria Joseph
Participants (82)
Dr Mukesh Arora
Gloria Joseph
Gloria Joseph
Neeraj Jain
Praveen Jain
Rahul Pandey
Attendee: 73 (1 dis...
Mahaboob ...
Q & A
Mute
Stop video
Share
Participants
Chat

8. Sample Copy of Certificate

Faculty Development Programme
on
Application of Artificial Intelligence on VLSI and Communication Technology
(AAIVCT-2022)
Organised By
Swami Keshvanand Institute of Technology, Management & Gramothan, Jaipur

Certificate of Appreciation

Ref No./SKIT/2021-22/<<Sr. No.>>

This is to certify that <<Salute>> <<Participate Name>> of <<Institute Name>> has participated in the five days Faculty Development Programme on "Application of Artificial Intelligence on VLSI and Communication Technology (AAIVCT-2022)" held from 07/03/2022 to 11/03/2022 at "Swami Keshvanand Institute of Technology, Management & Gramothan, Jaipur".

Coordinator (SKIT, Jaipur) | Coordinator (SKIT, Jaipur) | Coordinator (SKIT, Jaipur) | Coordinator (SKIT, Jaipur)

Expert Certificate

Faculty Development Programme
on
Application of Artificial Intelligence on VLSI and Communication Technology
(AAIVCT-2022)
Organised By
Swami Keshvanand Institute of Technology, Management & Gramothan, Jaipur

Certificate of Appreciation

Ref No./SKIT/2021-22/<<Sr. No.>>

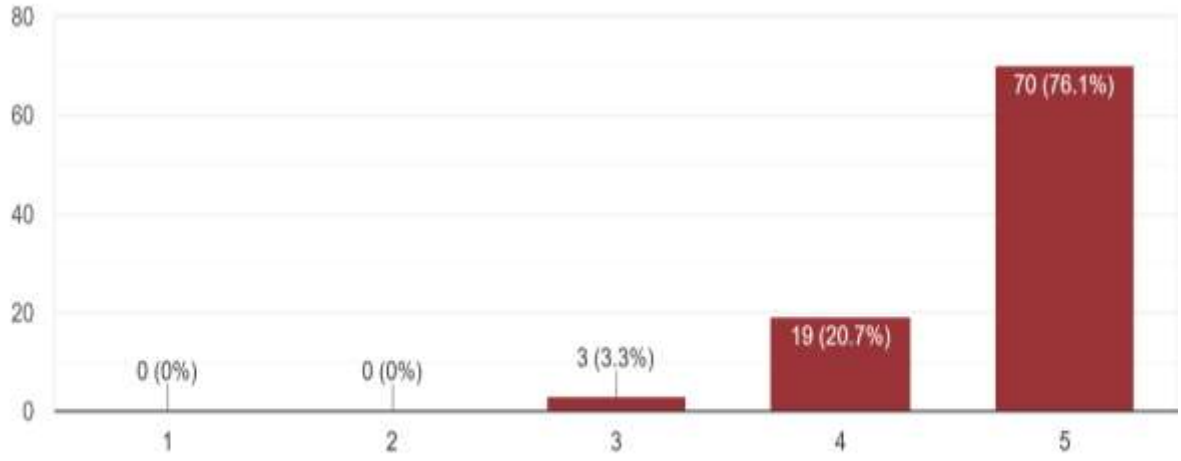
*This is to certify that <<Salute>> <<Participate Name>> of <<Institute Name>> has contributed as **resource person** in the five days Faculty Development Programme on "Application of Artificial Intelligence on VLSI and Communication Technology (AAIVCT-2022)" held from 07/03/2022 to 11/03/2022 at "Swami Keshvanand Institute of Technology, Management & Gramothan, Jaipur".*

Prof. Mukesh Arora
Convenor
AAIVCT-2022

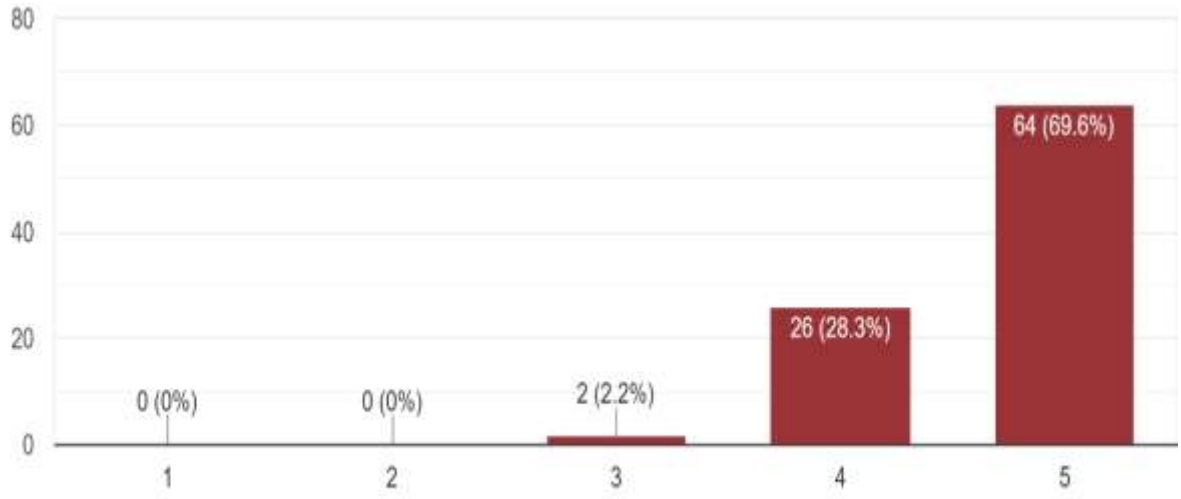
Prof. Praveen Kumar Jain
Convenor
AAIVCT-2022

9. Feedback report

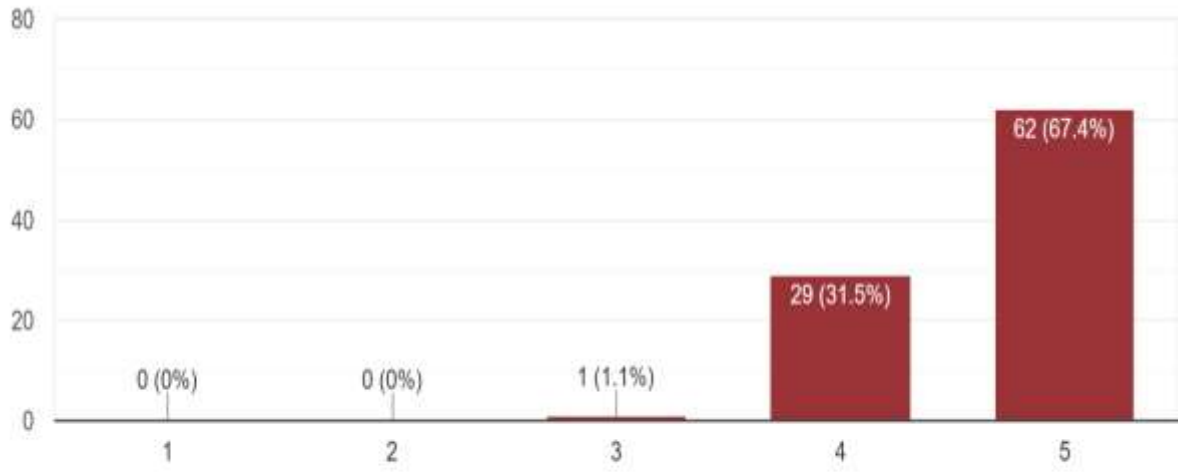
1. Your experience about the FDP



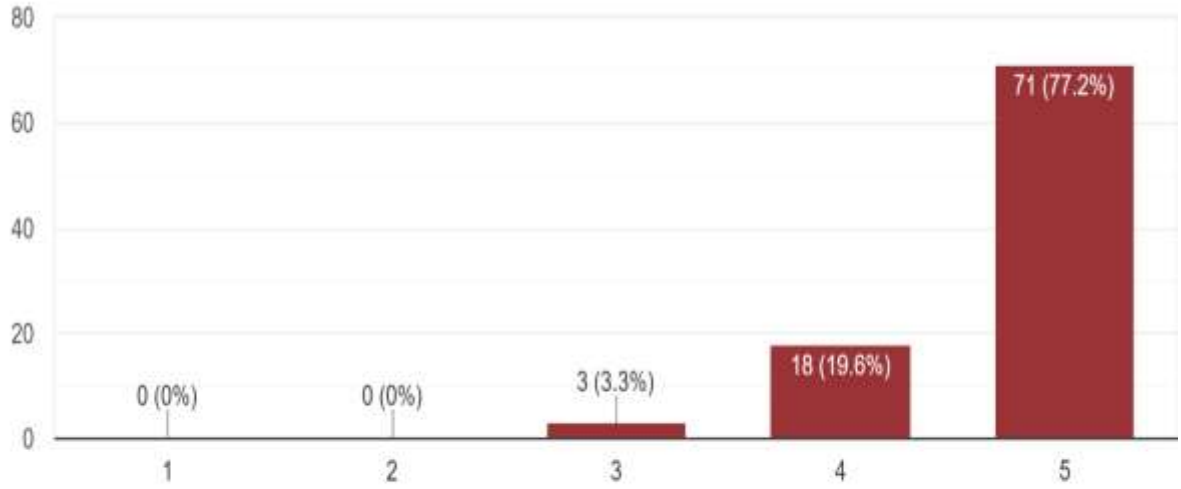
2. Overall, how do you rate the program you attended in terms of usefulness and effectiveness?



3. How do you rate the content of the FDP?



4. About speaker

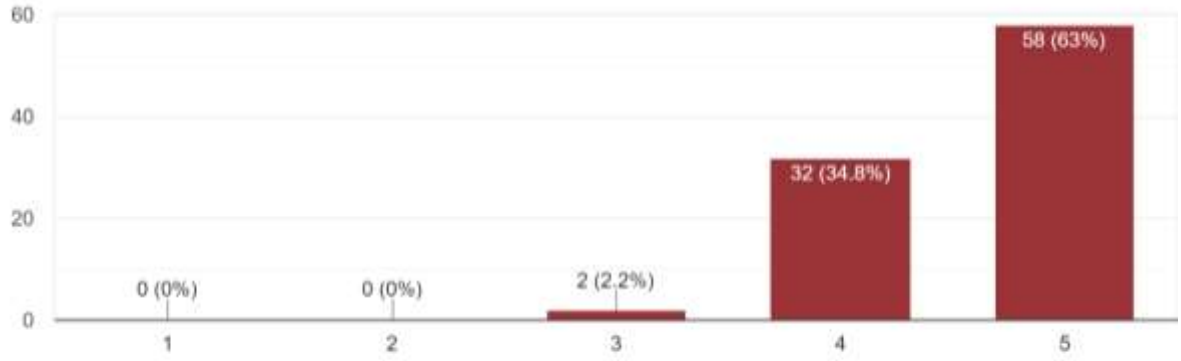


5. Knowledge gained by the FDP.

Yes
Good
Excellent
great
A lot
yes
MEMS
VERY GOOD
Knowledge about tools and technique in VLSI,MEMS,MOEMS in different fields like medical (early diagnosis) science etc.

Advancements in mems and moems,and tools used in vlsi and smart sensors.
i Learnt about technical knowledge of vlsi designing.
Key points and impacts of modern techniques and electronic gadgets
Very informative
EXCELLENT SPEAKERS AND LATEST TOPIC
Update my knowledge
An exposure to sensor & and its engineering
YES, Specially about MEMS and other sensors.
50 PER

6. Relevancy of topics



10. Media Coverage

ग्रामीण लोग मौजूद रहे।

एसकेआईटी मे पांच दिवसीय फैकल्टी डेवलपमेंट प्रोग्राम की शुरुआत

P3 Police Public Politics

जयपुर ! जगतपुरा स्थित स्वामी केशवानंद इंस्टीट्यूट ऑफ टेक्नोलॉजी, मैनेजमेंट एंड ग्रामोथान में इलेक्ट्रॉनिक्स एवं कम्युनिकेशन डिपार्टमेंट द्वारा ÷एप्लिकेशन ऑफ आर्टिफिशियल इंटेलिजेंस इन वीएलएसआई एंड कम्युनिकेशन टेक्नोलॉजी÷ पर पांच दिवसीय फैकल्टी डेवलपमेंट प्रोग्राम की शुरुआत हुई। इस कार्यक्रम के मुख्य अतिथि प्रो. घनश्याम सिंह (जोहान्सबर्ग यूनिवर्सिटी, साउथ अफ्रीका) तथा विशिष्ट अतिथि डॉक्टर देवव्रत सिकदर(असिस्टेंट प्रोफेसर आईआईटी गुवाहाटी) रहे। कार्यक्रम की शुरुआत में इलेक्ट्रॉनिक्स एवं संचार विभाग के विभागाध्यक्ष डॉ. मुकेश अरोड़ा ने सभी अतिथियों का स्वागत करते हुए आर्टिफिशियल इंटेलिजेंस के महत्त्व पर प्रकाश डाला। कार्यक्रम के समन्वयक मि. नीरज जैन ने सभी प्रतिभागियों के साथ एफडीपी का विस्तृत प्रारूप साझा किया। प्रोफेसर घनश्याम सिंह ने आर्टिफिशियल इंटेलिजेंस के हेल्थ

केयर, स्मार्ट सिटी, स्मार्ट ट्रांसपोर्ट सिस्टम आदि क्षेत्रों में अनुप्रयोगों के बारे में बताया,तथा उन्होंने शोधकर्ताओं विद्यार्थियों और व्याख्याताओं को प्रोत्साहित किया कि आर्टिफिशियल इंटेलिजेंस का प्रयोग करके समाज के विभिन्न समस्याओं का समाधान करने हेतु शोध करने का प्रयास करें। उद्घाटन समारोह के अंत में डॉ. विकास पाठक ने सभी आगंतुकों को धन्यवाद प्रस्तुत किया। एफडीपी के पहले दिन कुल दो सत्र आयोजित किए गए।

11. Technical Report

The One Week **One Week Faculty Development Programme (FDP) on "*Application of Artificial Intelligence on VLSI and Communication Technology*"** organized by SKIT Jaipur, from 7th - 11th March, 2022. This FDP is conducted on Webex online platform. In this FDP over 300 participants from all over India are well connected through WebEx for the entire duration of the FDP.

Prof. Ghanshyam Singh, Professor, Department of Electrical and Electronic Engineering, University of Johannesburg, South Africa was chief guest of FDP. The FDP began with the welcome address by Prof. Mukesh Arora (Head ECE &OFA), SKIT. The Inauguration ceremony also witnesses the presence of Prof. Praveen K. Jain (Dy.Head, ECE, SKIT), Dr. Rukhsar Zafar, Mr. Rahul Pandey, Mr. Neeraj Jain, Mr. Abhinandan Jain.
On the first session of the first day Dr Sikdar delivered an expert talk on Promises of Integrated Photonics Devices Technology with AI where he linked photonics plasmonics with AI . In the second session Prof Ghanshyam singh shared his valuable knowledge on the : Role of AI in 6G applications.

On the second day first session is started with the expert talk of Dr Binod Kumar, Ass Professor, IIT Jodhpur, in which participant learnt about Hardware Design Methodologies for Artificial Intelligence. He also explored with his own research and suggested further scope in this area to the participants. In the second session Dr Jai Narayan Tripathi, Assistant Professor, IIT Jodhpur, has delivered expert lecture on Computational Intelligence for Designing VLSI Systems.

Third day was started with the expert talk of Dr Kuldeep singh of MNIT Jaipur,and he has shared valuable knowledge on Nature inspired optimization techniques and their application in communication engineering

.In the second session of third day a hands on session is taken by Dr. Vikas Pathak and Mr. Ankur Sangal (Lead Application Engineer, CoreEL Technologies, New Delhi) on the Xilinx Vivdo Design flow using 7 series FPGA Architecture.

On the IV day first session is started by DR. S. J. Nanada of MNIT Jaipur, on Machine Learning for Cooperative Spectrum Sensing. In the second session a hands on session is taken by Dr. Gaurav Kumar Bharti Electrical Engineering Department, Chandigarh University, Chandigarh Title: Hands-on session on "Simulation of different photonic sensor using FDTD platform

On the last day of this FDP had 2 session .session 1 was delivered by Dr. Rukhsar zafar associate prof. SKIT Jaipur and she has enlightned us with her expert talk of Intelligent photonics. Session 2 was delivered by Dr. Ajay Khunteta associate professor of RTU kota he has delivered valuable talk on 5G communication and Role of AI in 5G.

This FDP enables the researchers, students, faculties to explore a new realm of technology and to work in this particular field to the next extent.

---Thank You-----